

# FreeBSD動作検証レポート

## T110h-S / 11.0-RELEASE-p1

### 対象ハードウェア

maker:NEC, product:Express5800/T110h-S [N8100-2310Y],  
serial:0000003, version:FR1.5

### 対象OSバージョン

FreeBSD 11.0-RELEASE-p1 #0 r306420: Thu Sep 29 01:43:23 UTC 2016

2017年2月3日版

BSDコンサルティング株式会社

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# 動作検証レポート

# 要約

## ハードウェア基本情報

製品UUID	4c9dd732-6b77-11e6-8001-408d5ca9e535
製品データ	maker:NEC, product:Express5800/T110h-S [N8100-2310Y], serial:0000003, version:FR1.5
筐体データ	maker:NEC, serial:04, tag:_____, version:856-061964-001
CPU	Intel(R) Xeon(R) CPU E3-1230 v5 @ 3.40GHz
MEMORY	16777216[KB]
BIOS	reldate:07/21/2016, vendor:American Megatrends Inc., version:5.0.5013
ACPI	oem:AMI , revision:2, rsdp:0x000f05b0, rsdt:0x7d1e7028, xsdt:0x000000007d1e7080, disabled:1
OS	FreeBSD 11.0-RELEASE-p1 #0 r306420: Thu Sep 29 01:43:23 UTC 2016

## 基礎性能データ

メモリアクセス速度	52.107 GBi/sec
IPv4スタック処理速度	767.352 MBi/sec
IPv6スタック処理速度	738.190 MBi/sec

## 動作検証要約

### "Legacy"モードでlway/2waアーキテクチャを使用する場合

FreeBSD 11.0-RELEASE-p1 #0 r306420: Thu Sep 29 01:43:23 UTC 2016は、次の条件を満たす場合に検証対象ハードウェア「maker:NEC, product:Express5800/T110h-S [N8100-2310Y], serial:0000003, version:FR1.5」において"Legacy"モードで正常に動作する。

- BIOSをレガシーモードで使用
- 512バイトセクタのストレージを使用
- LSI MegaRAID RAIDコントローラドライバとしてmfi(4)ではなくmrsas(4)を使用

工場出荷時のBIOSはUEFIブートを使用する設定になっているため、次のようにBIOSの設定を変更する必要がある。

システム起動時、マザーボードの起動画面が表示されたらF2キーを押してBIOSの設定モードに入る。



```
Version 2.16.1243n Copyright (C) 2014 American Megatrends, Inc.
BIOS Date: 12/19/2014 21:29:27 Ver: 5.0.0005

2 Processor(s) Detected, Core(s) per Processor = 6
Intel(R) Xeon(R) CPU E5-2603 v3 @ 1.60GHz
6x256 KB L2 Cache, 15360 KB L3 Cache

65536 MB System RAM Passed
Memory Mode: Independent

IPMI Base Board Management Controller was detected.
Device ID      : 88      Device Revision  : 01
IPMI Version   : 2.0     Firmware Revision : 02.11
Self Test Result : 5500

Press <F2> SETUP, <F3> Internal Flash Memory, <F4> ROM Utility, <F12> Network
```

カーソルキーでBootを選択し、さらに項目「Boot Mode」に合わせてからEnterキーを押す。

```

Apdio Setup Utility - Copyright (C) 2014 American Megatrends, Inc.
Main Advanced Security Server Boot Save & Exit

-----
Boot Configuration
* Quiet Boot [Unavailable]
*****
Setup Notice :
When the console redirection settings are
configured to enable, "Quiet Boot"
operates in the state of [Disabled].
*****
Bootup NumLock State [Off]
Setup Prompt Timeout 0

Boot Mode [UEFI]

Boot Option Priorities
Boot Option #1 [CD/DVD]
Boot Option #2 [Removable]
Boot Option #3 [HDD]
Boot Option #4 [Network]

-----
Select boot mode
UEFI/Legacy.

-----
F1: General Help
F4: Save & Exit Setup
ESC: Exit

-----
Version 2.16.1243n Copyright (C) 2014 American Megatrends, Inc.
AB

```

表示される項目からLegacyを選択してEnterキーを押す。

```

Apdio Setup Utility - Copyright (C) 2014 American Megatrends, Inc.
Main Advanced Security Server Boot Save & Exit

-----
Boot Configuration
* Quiet Boot [Unavailable]
*****
Setup Notice :
When the console redirection settings are
configured to enable, "Quiet Boot"
operates in the state of [Disabled].
*****
Bootup NumLock State [0]
Setup Prompt Timeout 0

Boot Mode [UEFI]
----- Boot Mode -----
Legacy
UEFI
-----

Boot Option Priorities
Boot Option #1 [CD/DVD]
Boot Option #2 [Removable]
Boot Option #3 [HDD]
Boot Option #4 [Network]

-----
Select boot mode
UEFI/Legacy.

-----
F1: General Help
F4: Save & Exit Setup
ESC: Exit

-----
Version 2.16.1243n Copyright (C) 2014 American Megatrends, Inc.
AB

```

カーソルキーでSave & Exitを選択し、さらに項目「Save Changes and Exit」に合わせてからEnterキーを押して設定の保存と再起動を実行する。

```

Apdio Setup Utility - Copyright (C) 2014 American Megatrends, Inc.
Main Advanced Security Server Boot Save & Exit

-----
Save & Exit Options
Save Changes and Exit
Discard Changes and Exit
Save Changes and Power Off
Discard Changes and Power Off
Load Setup Defaults

-----
Exit system setup
after saving the
changes.

-----
F1: General Help
F4: Save & Exit Setup
ESC: Exit

-----
Version 2.16.1243n Copyright (C) 2014 American Megatrends, Inc.
AB

```

なお、512バイトセクタのストレージに関しては注文時にその旨を伝える必要がある。mrsas(4)については後述する。

#### "UEFI"モードでIway/2waアーキテクチャを使用する場合

FreeBSD 11.0-RELEASE-p1 #0 r306420: Thu Sep 29 01:43:23 UTC 2016は、次の条件を満たす場合に検証対象ハードウェア「maker:NEC, product:Express5800/T110h-S [N8100-2310Y], serial:0000003, version:FR1.5」において"UEFI"モードで正常に動作する。

- BIOSをUEFIモードで使用
- LSI MegaRAID RAIDコントローラドライバとしてmfi(4)ではなくmrsas(4)を使用

システム起動時、マザーボードの起動画面が表示されたらF2キーを押して設定モードに入る。

```

BIOS Date: 12/19/2014 21:29:27 Ver: 5.0.0005

1 Processor(s) Detected, Core(s) per Processor = 6
Intel(R) Xeon(R) CPU E5-2603 v3 @ 1.60GHz
6x256 KB L2 Cache, 15360 KB L3 Cache

65536 MB System RAM Passed
Memory Mode: Independent

IPMI Base Board Management Controller was detected.
Device ID      : 88      Device Revision  : 01
IPMI Version   : 2.0      Firmware Revision : 02.11
Self Test Result : 5500

Press <F2> SETUP, <F3> Internal Flash Memory, <F4> ROM Utility, <F12> Network
<F3> is no longer available.

Press <F2> SETUP, <F4> ROM Utility, <F12> Network
  
```

工場出荷時のBIOSはUEFIブートを使用する設定になっており、カーソルキーでBootを選択し、項目「Boot Mode」がUEFIになっていることを確認する。Legacyになっている場合は「Boot Mode」に合わせてEnterキーを押し、表示される項目からUEFIを選択しEnterキーを押す。

```

Aptio Setup Utility - Copyright (C) 2014 American Megatrends, Inc.
Main Advanced Security Server Boot Save & Exit

-----
Boot Configuration                               ^|Select boot mode
* Quiet Boot           [Unavailable]           *|UEFI/Legacy.
*****
Setup Notice :
  When the console redirection settings are
  configured to enable, "Quiet Boot"
  operates in the state of [Disabled].
*****
Bootup NumLock State [Off]                    *|
Setup Prompt Timeout 0                        *|
-----
Boot Mode           [UEFI]                     *|
-----
Boot Option Priorities
Boot Option #1      [CD/DVD:MATSHITADVD-ROM    +|
                   UJ8E2]                    +|
-----
Boot Option #2      [Removable]               +|F1: General Help
Boot Option #3      [HDD]                    +|F4: Save & Exit Setup
                                           +|ESC: Exit
-----
Version 2.16.1243n Copyright (C) 2014 American Megatrends, Inc.
  
```

カーソルキーでSave & Exitを選択し、さらに項目「Save Changes and Exit」に合わせてからEnterキーを押して設定の保存と再起動を実行する。

```

Aptio Setup Utility - Copyright (C) 2014 American Megatrends, Inc.
Main Advanced Security Server Boot Save & Exit

-----
Save & Exit Options                               |Exit system setup
Save Changes and Exit                          |after saving the
Discard Changes and Exit                       |changes.
Save Changes and Power Off
Discard Changes and Power Off
Load Setup Defaults
-----
-----
|F1: General Help
|F4: Save & Exit Setup
|ESC: Exit
-----
Version 2.16.1243n Copyright (C) 2014 American Megatrends, Inc.
  
```

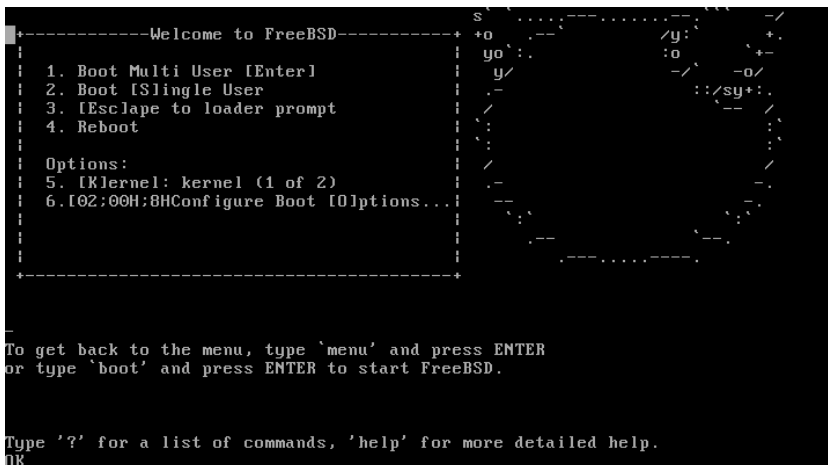
なお、mrsas(4)については後述する。

## 1way/2way共通

FreeBSD 11.0-RELEASEはデフォルトではLSI MegaRAID RAIDコントローラドライバとしてmfi(4)を使用する。ハードディスクはmfi(4)で認識はされるが、動作が不安定であるため、インストーラを起動する段階でmfi(4)ではなくmrsas(4)を使用するように設定を変更する必要がある。



インストーラ起動時にブートローダが表示された段階で3キーを押し、ローダプロンプトへ移行する。



ローダプロンプトで次のように入力を実施。mfi(4)を無効化し、mrsas(4)を有効化したインストーラを起動する。

```
set hw.mfi.mrsas_enable=1
boot
```

インストールの最後の段階でLive CDの起動を選択し、/mnt/boot/loader.confに次の設定を書き込む。

```
hw.mfi.mrsas_enable=1
```

BIOSをレガシーモードで使用している場合、基本的にストレージは512セクタの製品を使用することが推奨される。BIOSは4Kセクタのストレージを認識しないため、4Kセクタのストレージを起動対象として処理できない。

起動にPEXブートやUSBメモリからの起動を使用するということであれば、LSI MegaRAID RAIDコントローラドライバとしてmrsas(4)ではなくmfi(4)を使用することでFreeBSD側から利用できるようにすることもできる。

インストールしたFreeBSD 11.0-RELEASE-p1 #0 r306420: Thu Sep 29 01:43:23 UTC 2016を以降のバー

ジョンにアップグレードする場合には注意が必要。以降のバージョンではLSI MegaRAID SASドライバとしてmfi(4)ではなくmrsas(4)がデフォルトでアタッチされるように挙動が変更される可能性がある。

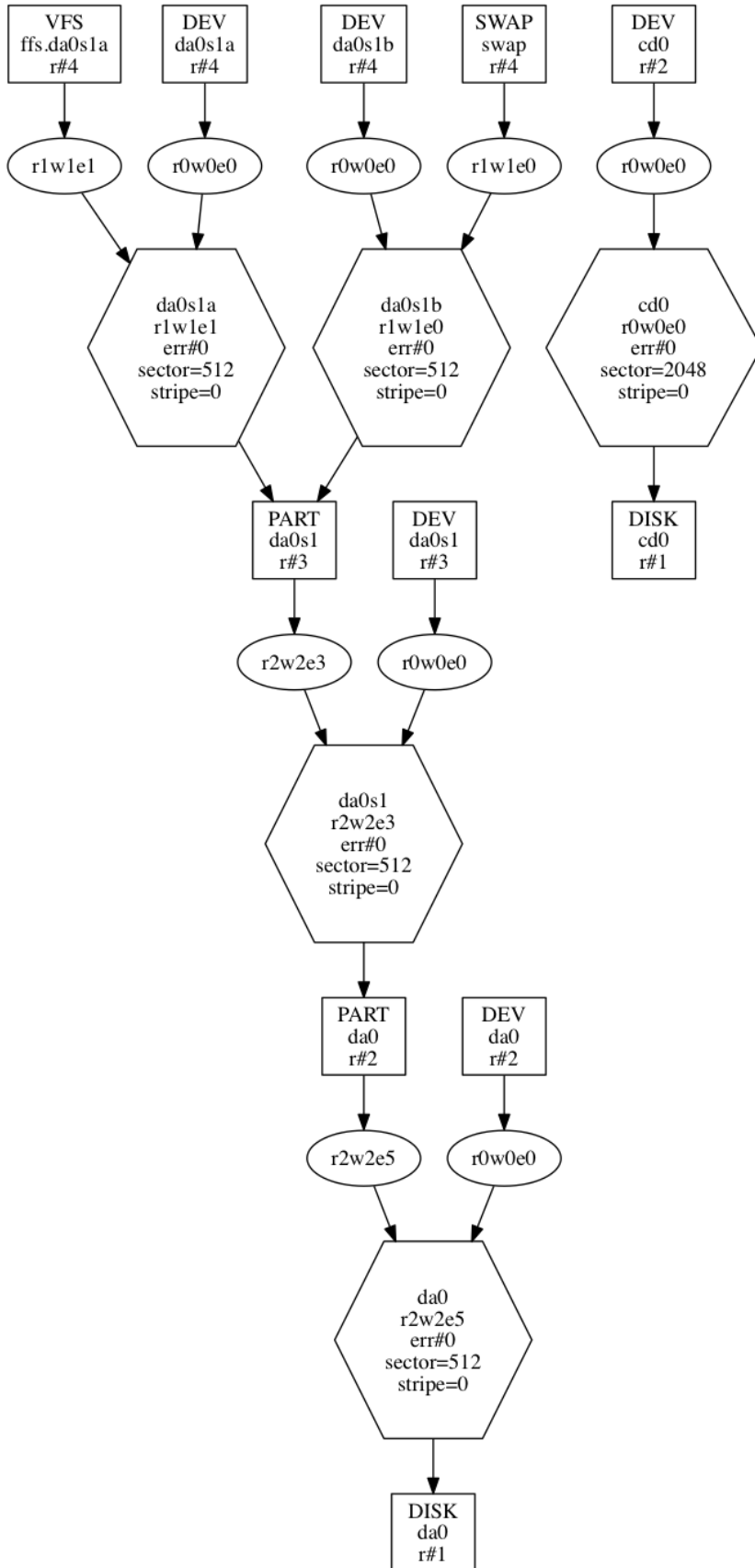
LSI MegaRAID SASドライバとしてmrsas(4)が使われた場合、/dev/以下に表示されるファイル名は次のように変更される。

```
mfi0    → mrsas0
mfi0s1  → da0s1a
mfi0s2  → da0s1b
```

論理パーティションの表示や名称は今後変更される可能性もあり、上記とは違う名前になる可能性もある。アップグレード対象のバージョンのリリースノートを参考にするとともに、適切な名称で/etc/fstabを書き換えるなどしてアップグレードを実施する必要がある。

FreeBSDプロジェクトは、FreeBSD 11.0-RELEASE以降はブランチでの最低5年間サポートの開始を予定している。FreeBSD 11.0-RELEASE以降は、ブランチにおける最新のリリースバージョンのみがサポートの対象となる。このため、FreeBSD 11.0-RELEASE以降はマイナーアップグレードごとにアップグレードを実施し、FreeBSD 11.1-RELEASE、FreeBSD 11.2-RELEASEへとバージョンアップを実施する必要がある。カーネルをカスタマイズしない限り、freebsd-update(8)コマンドを使ってバイナリアップデートを実施できる。

# 検証機GEOMツリー





# 高負荷耐久試験

# 高負荷耐久試験

## 試験結果

検証対象ハードウェア(maker:NEC, product:Express5800/T110h-S [N8100-2310Y], serial:0000003, version:FR1.5)は24時間高負荷耐久試験をクリアしており、所定の負荷に対して安定して動作するものとみられる。

不正処理高負荷耐久試験	クリア
通常ファイル作成高負荷耐久試験	クリア
FIFOファイル作成高負荷耐久試験	クリア
ディレクトリ作成高負荷耐久試験	クリア
ハードリンクファイル作成高負荷耐久試験	クリア
シンボリックリンクファイル作成高負荷耐久試験	クリア
ファイルロック機構高負荷耐久試験	クリア
メモリファイルマッピング機能高負荷耐久試験	クリア
ファイルシステム階層構造トラバース高負荷耐久試験	クリア
ファイルシステムパス解析高負荷耐久試験	クリア
ファイルシステムパスリネーム高負荷耐久試験	クリア
ファイルシステムライト・キャッシュリード高負荷耐久試験	クリア
ファイルシステムスワップ高負荷耐久試験	クリア
スレッド機構高負荷耐久試験	クリア
間接システムコール高負荷耐久試験	クリア
擬似ターミナル機構高負荷耐久試験	クリア
共有メモリ機構高負荷耐久試験	クリア
ネットワークソケット高負荷耐久試験	クリア
TCPスタック高負荷耐久試験	クリア
UDPスタック高負荷耐久試験	クリア

# 動作検証レポート

# 動作検証レポート

## ACPI認識データ

ACPI_APIC	Revision=3, OEMID=AMI, OEM_Table ID=0707, OEM_Revision=0x1072009, Creator_ID=AMI, Creator_Revision=0x10013
ACPI_BERT	Revision=1, OEMID=AMI, OEM_Table ID=AMI.BERT, OEM_Revision=0x0, Creator_ID=AMI, Creator_Revision=0x0, 0x52, Zero) & 0x2000) == Zero), 0x52, Zero) & 0x2000) == Zero), 0x52, Zero) & 0x2000) == Zero), Zero) == 0x02), Zero) == 0x02), Zero) == 0x02)
ACPI_DMAR	Revision=1, OEMID=INTEL, OEM_Table ID=SKL, OEM_Revision=0x1, Creator_ID=INTEL, Creator_Revision=0x1
ACPI_DSDT	Revision=2, OEMID=AMI, OEM_Table ID=0707, OEM_Revision=0x1072009, Creator_ID=INTEL, Creator_Revision=0x20120913
ACPI_FACP	Revision=5, OEMID=AMI, OEM_Table ID=0707, OEM_Revision=0x1072009, Creator_ID=AMI, Creator_Revision=0x10013, FACS=0x7daf8f80, DSDT=0x7d1e7188, SMI_CMD=0xb2, ACPI_ENABLE=0xa0, ACPI_DISABLE=0xa1, S4BIOS_REQ=0x0, FLUSH_STRIDE=16, DUTY_WIDTH=0
ACPI_FACS	HwSig=0x86005d5e, Firm_Wake_Vec=0x00000000
ACPI_FIDT	Length=156, Revision=1, Checksum=75, OEMID=AMI, OEM_Table ID=0707, OEM_Revision=0x1072009, Creator_ID=AMI, Creator_Revision=0x10013
ACPI_HPET	Revision=1, OEMID=AMI, OEM_Table ID=0707, OEM_Revision=0x1072009, Creator_ID=AMI, Creator_Revision=0x5000b
ACPI_MCFG	Revision=1, OEMID=AMI, OEM_Table ID=0707, OEM_Revision=0x1072009, Creator_ID=MSFT, Creator_Revision=0x97
ACPI_RSD_PTR	OEM=AMI, ACPI_Rev=2.0x_(2), XSDT=0x000000007d1e7080
ACPI_XSDT	Revision=1, OEMID=AMI, OEM_Table ID=0707, OEM_Revision=0x1072009, Creator_ID=AMI, Creator_Revision=0x10013
ACPI_SSDT_1	Length=8210, Revision=2, Checksum=145, OEMID=SaSsd, OEM_Table ID=SaSsd, OEM_Revision=0x3000, Creator_ID=INTEL, Creator_Revision=0x20120913
ACPI_SSDT_2	Length=3699, Revision=2, Checksum=39, OEMID=CpuRef, OEM_Table ID=CpuSsd, OEM_Revision=0x3000, Creator_ID=INTEL, Creator_Revision=0x20120913
ACPI_SSDT_3	Length=869, Revision=2, Checksum=252, OEMID=PmRef, OEM_Table ID=Cpu0Tst, OEM_Revision=0x3000, Creator_ID=INTEL, Creator_Revision=0x20120913
ACPI_SSDT_4	Length=959, Revision=2, Checksum=69, OEMID=PmRef, OEM_Table ID=ApTst, OEM_Revision=0x3000, Creator_ID=INTEL, Creator_Revision=0x20120913

## MFIデバイス認識データ

アダプタ	該当データなし
ドライバ	該当データなし
ボリューム	該当データなし
コンフィグ	該当データなし

※ 対象となるデバイスがmfi(4)ドライバを使用しないストレージ構成の場合、当項目は該当データなし。

## MPTデバイス認識データ

アダプタ	該当データなし
ドライバ	該当データなし
ボリューム	該当データなし
コンフィグ	該当データなし

※ 対象となるデバイスがmpt(4)ドライバを使用しないストレージ構成の場合、当項目は該当データなし。

## NMIデバイス認識データ

NMIスイッチ動作確認	クリア
-------------	-----

※ NMI機能を使用するには、"options DDB"を有効にしたカーネルである必要がある。また、sysctl(8)の"machdep.panic\_on\_nmi"及び"machdep.kdb\_on\_nmi"の値がともに"1"である必要がある。

## NIC認識データ

NIC	bge0, bge1, bge2, bge3, bge4, bge5, bge6, bge7, bge8, bge9
bge0	flags=8802<BROADCAST,SIMPLEX,MULTICAST>, metric=0, mtu=1500, options=c019b<RXCSUM, TXCSUM, VLAN_MTU, VLAN_HWTAGGING, VLAN_HWCSUM, TSO4, VLAN_HWTSO, LINKSTATE>, ether=8c:df:9d:81:8d:40, nd6_options=29<PERFORMNUD, IFDISABLED, AUTO_LINKLOCAL>, media=Ethernet autoselect
bge1	flags=8802<BROADCAST,SIMPLEX,MULTICAST>, metric=0, mtu=1500, options=c019b<RXCSUM, TXCSUM, VLAN_MTU, VLAN_HWTAGGING, VLAN_HWCSUM, TSO4, VLAN_HWTSO, LINKSTATE>, ether=8c:df:9d:81:8d:41, nd6_options=29<PERFORMNUD, IFDISABLED, AUTO_LINKLOCAL>, media=Ethernet autoselect
bge2	flags=8802<BROADCAST,SIMPLEX,MULTICAST>, metric=0, mtu=1500, options=c019b<RXCSUM, TXCSUM, VLAN_MTU, VLAN_HWTAGGING, VLAN_HWCSUM, TSO4, VLAN_HWTSO, LINKSTATE>, ether=8c:df:9d:81:8d:42, nd6_options=29<PERFORMNUD, IFDISABLED, AUTO_LINKLOCAL>, media=Ethernet autoselect
bge3	flags=8802<BROADCAST,SIMPLEX,MULTICAST>, metric=0, mtu=1500, options=c019b<RXCSUM, TXCSUM, VLAN_MTU, VLAN_HWTAGGING, VLAN_HWCSUM, TSO4, VLAN_HWTSO, LINKSTATE>, ether=8c:df:9d:81:8d:43, nd6_options=29<PERFORMNUD, IFDISABLED, AUTO_LINKLOCAL>, media=Ethernet autoselect
bge4	flags=8802<BROADCAST,SIMPLEX,MULTICAST>, metric=0, mtu=1500, options=c019b<RXCSUM, TXCSUM, VLAN_MTU, VLAN_HWTAGGING, VLAN_HWCSUM, TSO4, VLAN_HWTSO, LINKSTATE>, ether=8c:df:9d:81:8d:4c, nd6_options=29<PERFORMNUD, IFDISABLED, AUTO_LINKLOCAL>, media=Ethernet autoselect
bge5	flags=8802<BROADCAST,SIMPLEX,MULTICAST>, metric=0, mtu=1500, options=c019b<RXCSUM, TXCSUM, VLAN_MTU, VLAN_HWTAGGING, VLAN_HWCSUM, TSO4, VLAN_HWTSO, LINKSTATE>, ether=8c:df:9d:81:8d:4d, nd6_options=29<PERFORMNUD, IFDISABLED, AUTO_LINKLOCAL>, media=Ethernet autoselect
bge6	flags=8802<BROADCAST,SIMPLEX,MULTICAST>, metric=0, mtu=1500, options=c019b<RXCSUM, TXCSUM, VLAN_MTU, VLAN_HWTAGGING, VLAN_HWCSUM, TSO4, VLAN_HWTSO, LINKSTATE>, ether=8c:df:9d:81:8d:4e, nd6_options=29<PERFORMNUD, IFDISABLED, AUTO_LINKLOCAL>, media=Ethernet autoselect
bge7	flags=8802<BROADCAST,SIMPLEX,MULTICAST>, metric=0, mtu=1500, options=c019b<RXCSUM, TXCSUM, VLAN_MTU, VLAN_HWTAGGING, VLAN_HWCSUM, TSO4, VLAN_HWTSO, LINKSTATE>, ether=8c:df:9d:81:8d:4f, nd6_options=29<PERFORMNUD, IFDISABLED, AUTO_LINKLOCAL>, media=Ethernet autoselect
bge8	flags=8843<UP,BROADCAST,RUNNING,SIMPLEX,MULTICAST>, metric=0, mtu=1500, options=c019b<RXCSUM, TXCSUM, VLAN_MTU, VLAN_HWTAGGING, VLAN_HWCSUM, TSO4, VLAN_HWTSO, LINKSTATE>, ether=40:8d:5c:a9:e5:33, inet=192.168.10.103 netmask 0xfffff00 broadcast 192.168.10.255, nd6_options=29<PERFORMNUD, IFDISABLED, AUTO_LINKLOCAL>, media=Ethernet autoselect (1000baseT <full-duplex>), status=active
bge9	flags=8802<BROADCAST,SIMPLEX,MULTICAST>, metric=0, mtu=1500, options=c019b<RXCSUM, TXCSUM, VLAN_MTU, VLAN_HWTAGGING, VLAN_HWCSUM, TSO4, VLAN_HWTSO, LINKSTATE>, ether=40:8d:5c:a9:e5:34, nd6_options=29<PERFORMNUD, IFDISABLED, AUTO_LINKLOCAL>, media=Ethernet autoselect

## カーネルデバイス認識データ

acpi	apm	apmctl	audit
auditpipe	bpf	bpf0(bpf)	cd0
console	consolectl	ctty	cuau0
cuau0.init	cuau0.lock	cuau1	cuau1.init
cuau1.lock	da0	da0s1	da0s1a
da0s1b	devctl	devctl2	devstat
dumpdev(da0s1b)	fd	fd/0	fd/1
fd/2	fido	full	geom.ctl
hpet0	io	kbd1(kbdmux0)	kbd2(ukbd0)
kbd3(ukbd1)	kbdmux0	klog	kmem
led	led/ahci0.0.fault	led/ahci0.0.locate	led/ahci0.1.fault
led/ahci0.1.locate	led/ahci0.2.fault	led/ahci0.2.locate	led/ahci0.3.fault
led/ahci0.3.locate	led/ahci0.4.fault	led/ahci0.4.locate	log(/var/run/log)
mdctl	megaraid_sas_ioctl_node(mrsas0)	mem	midistat
mrsas0	netmap	nfslock	null
pass0	pass1	pass2	pci
random	reroot	reroot/reroot	ses0
sndstat	stderr(fd/2)	stdin(fd/0)	stdout(fd/1)
sysmouse	ttyu0	ttyu0.init	ttyu0.lock
ttyu1	ttyu1.init	ttyu1.lock	ttyv0
ttyv1	ttyv2	ttyv3	ttyv4
ttyv5	ttyv6	ttyv7	ttyv8
ttyv9	ttyva	ttyvb	ufssuspend
ugen0.1(usb/0.1.0)	ugen0.2(usb/0.2.0)	ugen0.3(usb/0.3.0)	ugen0.4(usb/0.4.0)
ukbd0	ukbd1	urandom(random)	usb

usb/0.1.0	usb/0.1.1	usb/0.2.0	usb/0.2.1
usb/0.3.0	usb/0.3.1	usb/0.4.0	usb/0.4.1
usb/0.4.2	usbctl	xpt0	zero

## PCI認識データ

1	pci0:0:0:0(hostb0), class=HOST-PCI, vendor='Intel Corporation', device='Skylake Host Bridge/DRAM Registers'
2	pci0:0:1:0(pci1), class=PCI-PCI, vendor='Intel Corporation', device='Skylake PCIe Controller (x16)'
3	pci0:0:20:0(xhci0), class=USB, vendor='Intel Corporation', device='Sunrise Point-H USB 3.0 xHCI Controller'
4	pci0:0:20:2(none0), class=dasp, vendor='Intel Corporation', device='Sunrise Point-H Thermal subsystem'
5	pci0:0:22:0(none1), class=simple comms, vendor='Intel Corporation', device='Sunrise Point-H CSME HECI'
6	pci0:0:22:1(none2), class=simple comms, vendor='Intel Corporation', device='Sunrise Point-H CSME HECI'
7	pci0:0:23:0(ahci0), class=SATA, vendor='Intel Corporation', device='Sunrise Point-H SATA controller [AHCI mode]'
8	pci0:0:27:0(pci2), class=PCI-PCI, vendor='Intel Corporation', device='Sunrise Point-H PCI Root Port'
9	pci0:0:27:3(pci3), class=PCI-PCI, vendor='Intel Corporation', device='Sunrise Point-H PCI Root Port'
10	pci0:0:28:0(pci4), class=PCI-PCI, vendor='Intel Corporation', device='Sunrise Point-H PCI Express Root Port'
11	pci0:0:28:3(pci5), class=PCI-PCI, vendor='Intel Corporation', device='Sunrise Point-H PCI Express Root Port'
12	pci0:0:28:4(pci6), class=PCI-PCI, vendor='Intel Corporation', device='Sunrise Point-H PCI Express Root Port'
13	pci0:0:29:0(pci7), class=PCI-PCI, vendor='Intel Corporation', device='Sunrise Point-H PCI Express Root Port'
14	pci0:0:31:0(isab0), class=PCI-ISA, vendor='Intel Corporation', device='Sunrise Point-H LPC Controller'
15	pci0:0:31:2(none3), class=memory, vendor='Intel Corporation', device='Sunrise Point-H PMC'
16	pci0:0:31:4(none4), class=SMBus, vendor='Intel Corporation', device='Sunrise Point-H SMBus'
17	pci0:48:0:0(bge0), class=ethernet, vendor='Broadcom Corporation', device='NetXtreme BCM5719 Gigabit Ethernet PCIe'
18	pci0:48:0:1(bge1), class=ethernet, vendor='Broadcom Corporation', device='NetXtreme BCM5719 Gigabit Ethernet PCIe'
19	pci0:48:0:2(bge2), class=ethernet, vendor='Broadcom Corporation', device='NetXtreme BCM5719 Gigabit Ethernet PCIe'
20	pci0:48:0:3(bge3), class=ethernet, vendor='Broadcom Corporation', device='NetXtreme BCM5719 Gigabit Ethernet PCIe'
21	pci0:95:0:0(vgapci0), class=VGA, vendor='Matrox Electronics Systems Ltd.', device='MGA G200e [Pilot] ServerEngines (SEP1)'
22	pci0:96:0:0(bge4), class=ethernet, vendor='Broadcom Corporation', device='NetXtreme BCM5719 Gigabit Ethernet PCIe'
23	pci0:96:0:1(bge5), class=ethernet, vendor='Broadcom Corporation', device='NetXtreme BCM5719 Gigabit Ethernet PCIe'
24	pci0:96:0:2(bge6), class=ethernet, vendor='Broadcom Corporation', device='NetXtreme BCM5719 Gigabit Ethernet PCIe'
25	pci0:96:0:3(bge7), class=ethernet, vendor='Broadcom Corporation', device='NetXtreme BCM5719 Gigabit Ethernet PCIe'
26	pci0:143:0:0(bge8), class=ethernet, vendor='Broadcom Corporation', device='NetXtreme BCM5720 Gigabit Ethernet PCIe'
27	pci0:143:0:1(bge9), class=ethernet, vendor='Broadcom Corporation', device='NetXtreme BCM5720 Gigabit Ethernet PCIe'
28	pci0:144:0:0(mrsas0), class=RAID, vendor='LSI Logic / Symbios Logic', device='MegaRAID SAS-3 3108 [Invader]'





55	0xde000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
56	0xdf000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
57	0xe0000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
58	0xe1000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
59	0xe2000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
60	0xe3000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
61	0xe4000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
62	0xe5000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
63	0xe6000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
64	0xe7000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
65	0xe8000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
66	0xe9000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
67	0xea000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
68	0xeb000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
69	0xec000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
70	0xed000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
71	0xee000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
72	0xef000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
73	0xf0000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
74	0xf1000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
75	0xf2000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
76	0xf3000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
77	0xf4000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
78	0xf5000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
79	0xf6000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
80	0xf7000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
81	0xf8000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
82	0xf9000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
83	0xfa000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
84	0xfb000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
85	0xfc000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
86	0xfd000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
87	0xfe000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
88	0xff000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
89	0x80000000/0x80000000 BIOS uncacheable set-by-firmware active

## 動作検証レポート 詳細データ

## 詳細データ

### カーネル環境変数データ詳細一覧

```
LINES="24"
acpi.oem="AMI "
acpi.revision="2"
acpi.rsdp="0x000f05b0"
acpi.rsdtr="0x7d1e7028"
acpi.xsdt="0x000000007d1e7080"
acpi.xsdt_length="36"
beansi_bootfs="^[1mbootfs: ^[m"
beansi_current="^[1mActive: ^[m"
beansi_page="^[1mP^[m]age: "
beansi_pageof=" of "
bemenu_bootfs="bootfs: "
bemenu_current="Active: "
bemenu_page="[P]age: "
bemenu_pageof=" of "
bootenv_autolist="YES"
bootenv_root[2]=""
bootenvmenu_command[1]="be_draw_screen 1 goto_menu"
bootenvmenu_command[2]="set_bootenv"
bootenvmenu_command[3]="set_be_page"
bootenvmenu_init="init_bootenv"
bootenvmenu_keycode[1]="8"
bootenvmenu_keycode[2]="97"
bootenvmenu_keycode[3]="112"
bootenvmenu_options="4"
bootenvmenu_optionstext="Boot Environments:"
bootfile="kernel"
comconsole_pcidev=""
comconsole_port="1016"
comconsole_speed="9600"
console="vidconsole"
currdev="disk0s1a:"
hint.acpi.0.oem="AMI "
hint.acpi.0.revision="2"
hint.acpi.0.rsdp="0x000f05b0"
hint.acpi.0.rsdtr="0x7d1e7028"
hint.acpi.0.xsdt="0x000000007d1e7080"
hint.acpi.0.xsdt_length="36"
hint.acpi_throttle.0.disabled="1"
hint.atkbd.0.at="atkbd"
hint.atkbd.0.irq="1"
hint.atkbd.0.at="isa"
hint.atkbd.0.port="0x060"
hint.atrtc.0.at="isa"
hint.atrtc.0.irq="8"
hint.atrtc.0.port="0x70"
hint.attimer.0.at="isa"
hint.attimer.0.irq="0"
hint.attimer.0.port="0x40"
hint.fd.0.at="fd0"
hint.fd.0.drive="0"
hint.fd.1.at="fd0"
hint.fd.1.drive="1"
hint.fdc.0.at="isa"
hint.fdc.0.drq="2"
hint.fdc.0.irq="6"
hint.fdc.0.port="0x3f0"
hint.p4tcc.0.disabled="1"
hint.ppc.0.at="isa"
hint.ppc.0.irq="7"
hint.psm.0.at="atkbd"
hint.psm.0.irq="12"
hint.sc.0.at="isa"
hint.sc.0.flags="0x100"
hint.uart.0.at="isa"
hint.uart.0.flags="0x10"
hint.uart.0.irq="4"
hint.uart.0.port="0x3f8"
hint.uart.1.at="isa"
hint.uart.1.irq="3"
hint.uart.1.port="0x2f8"
hw.mfi.mrsas_enable="1"
interpret="OK"
kernel="kernel"
kernel[0]="kernel"
kernel[1]="kernel.old"
kernel_options=""
kernelname="/boot/kernel/kernel"
loaddev="disk0s1a:"
loader_conf_files="/boot/device.hints /boot/loader.conf /boot/loader.conf.local"
mac_ifoff="NO"
module_path="/boot/kernel;/boot/modules"
pcibios.config1="1"
pcibios.config2="0"
pcibios.major="3"
pcibios.maxbus="192"
pcibios.minor="0"
smbios.bios.reldate="07/21/2016"
smbios.bios.vendor="American Megatrends Inc."
smbios.bios.version="S.0.5013"
smbios.chassis.maker="NEC"
smbios.chassis.serial="04"
smbios.chassis.tag=""
smbios.chassis.version="856-061964-001"
smbios.memory.enabled="16777216"
smbios.planar.maker="GIGABYTE"
smbios.planar.product="GA-6KASV3-NJ"
smbios.planar.serial="0FHCS1500200"
smbios.planar.version="1.0C"
smbios.socket.enabled="1"
smbios.socket.populated="1"
smbios.system.maker="NEC"
smbios.system.product="Express5800/T110h-S [N8100-2310Y]"
smbios.system.serial="0000003"
```

```
smbios.system.uuid="4c9dd732-6b77-11e6-8001-408d5ca9e535"  
smbios.system.version="FR1.5"  
smbios.version="3.0"  
temp_options=""  
twiddle_divisor="1"  
vfs.root.mountfrom="ufs:/dev/das1a"  
vfs.root.mountfrom.options="rw"  
zfs_be_currpage="1"
```

# カーネルステートデータ詳細一覧

```
kern.ostype: FreeBSD
kern.osrelease: 11.0-RELEASE-p1
kern.osrevision: 199506
kern.version: FreeBSD 11.0-RELEASE-p1 #0 r306420: Thu Sep 29 01:43:23 UTC 2016
  root@releeng2.nyi.freebsd.org:/usr/obj/usr/src/sys/GENERIC
kern.maxvnodes: 348543
kern.maxproc: 21684
kern.maxfiles: 521303
kern.argmax: 262144
kern.securelevel: -1
kern.hostname: verify.bsdconsulting.co.jp
kern.hostid: 1632784802
kern.clockrate: { hz = 1000, tick = 1000, profhz = 8128, stathz = 127 }
kern.posix1version: 200112
kern.ngroups: 1023
kern.job_control: 1
kern.saved_ids: 0
kern.boottime: { sec = 1486110363, usec = 980844 } Fri Feb 3 17:26:03 2017
kern.domainname:
kern.osreldate: 1100122
kern.bootfile: /boot/kernel/kernel
kern.maxfilesperproc: 469170
kern.maxprocpid: 19515
kern.ipc.maxsockbuf: 2097152
kern.ipc.sockbuf_waste_factor: 8
kern.ipc.max_linkhdr: 16
kern.ipc.max_protohdr: 60
kern.ipc.max_hdr: 76
kern.ipc.max_datalen: 92
kern.ipc.maxsockets: 521304
kern.ipc.numopensockets: 15
kern.ipc.soacceptqueue: 128
kern.ipc.shm_allow_removed: 1
kern.ipc.shm_use_phys: 0
kern.ipc.shmall: 131072
kern.ipc.shmseg: 128
kern.ipc.shmmni: 192
kern.ipc.shmin: 1
kern.ipc.shmax: 536870912
kern.ipc.semaem: 16384
kern.ipc.semvmx: 32767
kern.ipc.semusz: 632
kern.ipc.semume: 50
kern.ipc.semopm: 100
kern.ipc.semmsl: 340
kern.ipc.semnu: 150
kern.ipc.semms: 340
kern.ipc.semni: 50
kern.ipc.msgseg: 2048
kern.ipc.msgsz: 8
kern.ipc.msgttl: 40
kern.ipc.msgmb: 2048
kern.ipc.msgmni: 40
kern.ipc.msgmax: 16384
kern.ipc.aio_lifetime: 30000
kern.ipc.aio_target_procs: 4
kern.ipc.aio_num_procs: 4
kern.ipc.aio_max_procs: 32
kern.ipc.aio_empty_retries: 0
kern.ipc.aio_empty_results: 0
kern.ipc.pteresizeallowed: 1
kern.ipc.pteresizefail: 0
kern.ipc.ptevallocfail: 0
kern.ipc.pipfragretry: 0
kern.ipc.pipkva: 20480
kern.ipc.maxpipekva: 266903552
kern.ipc.umtx_max_robust: 1000
kern.ipc.umtx_vnode_persistent: 0
kern.ipc.nmbufs: 6480165
kern.ipc.nmbjumbo16: 337508
kern.ipc.nmbjumbo9: 450009
kern.ipc.nmbjumbo6: 506262
kern.ipc.nmbclusters: 1012524
kern.ipc.maxmbufmem: 8294602752
kern.dummy: 0
kern.ps_strings: 140737488351200
kern.usrstack: 140737488351232
kern.logsigexit: 1
kern.iov_max: 1024
kern.hostuuid: 4c9dd732-6b77-11e6-8001-408d5ca9e535
kern.cryptodevallowsoft: 0
kern.userasymcrypto: 1
kern.minvnodes: 87135
kern.metadelat: 28
kern.dirdelay: 29
kern.filedelay: 30
kern.tty_nout: 14402
kern.tty_nin: 332
kern.tty_inq_flush_secure: 1
kern.smp_forward_signal_enabled: 1
kern.smp_topology: 0
kern.smp_cpus: 8
kern.smp_disabled: 0
kern.smp_active: 1
kern.smp_maxcpu: 256
kern.smp_maxid: 7
kern.msdbuf_clear: 0
kern.msdbuf:
kern.always_console_output: 0
kern.log_console_add_linefeed: 0
kern.log_console_output: 1
kern.vm_guest: none
kern.sgrowsiz: 131072
kern.maxssiz: 536870912
kern.dflssiz: 8388608
kern.maxdsiz: 34359738368
kern.dfldsiz: 34359738368
kern.maxtsiz: 134217728
kern.bio_transient_maxcnt: 1024
kern.maxbcache: 0
kern.maxswzone: 0
kern.msdbufsize: 98304
kern.nsbuff: 256
kern.nbuf: 104925
```

```

kern.hz: 1000
kern.msbuf_show_timestamp: 0
kern.log_wakeups_per_second: 5
kern.kobj_methodcount: 261
kern.hintmode: 0
kern.devstat.version: 6
kern.devstat.generation: 210
kern.devstat.numdevs: 5
kern.ccpu: 0
kern.sched.topology_spec: <groups>
kern.sched.steal_thresh: 2
kern.sched.steal_idle: 1
kern.sched.balance_interval: 127
kern.sched.balance: 1
kern.sched.affinity: 1
kern.sched.idlespinthresh: 157
kern.sched.idlespins: 10000
kern.sched.static_boost: 152
kern.sched.preempt_thresh: 80
kern.sched.interact: 30
kern.sched.slice: 12
kern.sched.quantum: 94488
kern.sched.name: ULE
kern.sched.preemption: 1
kern.sched.cpusetsize: 32
kern.callout_stat: 0
kern.pin_pcpu_swi: 0
kern.pin_default_swi: 0
kern.ncallout: 18508
kern.threads.max_threads_hits: 0
kern.threads.max_threads_per_proc: 1500
kern.timecounter.tsc_shift: 1
kern.timecounter.smp_tsc_adjust: 0
kern.timecounter.smp_tsc: 1
kern.timecounter.invariant_tsc: 1
kern.timecounter.fast_gettime: 1
kern.timecounter.tick: 1
kern.timecounter.choice: TSC-low(1000) ACPI-fast(900) i8254(0) HPET(950) dummy(-1000000)
kern.timecounter.hardware: TSC-low
kern.timecounter.alloweddeviation: 5
kern.timecounter.stepwarnings: 0
kern.timecounter.tc.TSC-low.quality: 1000
kern.timecounter.tc.TSC-low.frequency: 1704060214
kern.timecounter.tc.TSC-low.counter: 1843462223
kern.timecounter.tc.TSC-low.mask: 4294967295
kern.timecounter.tc.ACPI-fast.quality: 900
kern.timecounter.tc.ACPI-fast.frequency: 3579545
kern.timecounter.tc.ACPI-fast.counter: 12679308
kern.timecounter.tc.ACPI-fast.mask: 16777215
kern.timecounter.tc.i8254.quality: 0
kern.timecounter.tc.i8254.frequency: 1193182
kern.timecounter.tc.i8254.counter: 29329
kern.timecounter.tc.i8254.mask: 65535
kern.timecounter.tc.HPET.quality: 950
kern.timecounter.tc.HPET.frequency: 24000000
kern.timecounter.tc.HPET.counter: 1583941605
kern.timecounter.tc.HPET.mask: 4294967295
kern.fscale: 2048
kern.corefile: %N.core
kern.coredump_devctl: 0
kern.nodump_coredump: 0
kern.coredump: 1
kern.cpmode_coredump: 0
kern.sugid_coredump: 0
kern.sigqueue_alloc_fail: 0
kern.sigqueue_overflow: 0
kern.sigqueue_preallocate: 1024
kern.sigqueue_max_pending_per_proc: 128
kern.forcesigexit: 1
kern.suspend_blocked: 0
kern.shutdown.dumpdevname: da0s1b
kern.shutdown.kproc_shutdown_wait: 60
kern.shutdown.poweroff_delay: 5000
kern.shutdown.show_busybufs: 0
kern.sync_on_panic: 0
kern.panic_reboot_wait_time: 15
kern.racct.rctl.throttle_pct2: 4294967295
kern.racct.rctl.throttle_pct: 4294967295
kern.racct.rctl.throttle_max: 4294967295
kern.racct.rctl.throttle_min: 4294967295
kern.racct.rctl.devctl_rate_limit: 10
kern.racct.rctl.log_rate_limit: 10
kern.racct.rctl.maxbufsize: 16777216
kern.racct.pcpu_threshold: 1
kern.racct.enable: 0
kern.proc_vmmmap_skip_resident_count: 0
kern.kstack_pages: 4
kern.hwpmc_software_events: 16
kern.fallback_elf_brand: -1
kern.pid_max: 99999
kern.features.ufs_quota64: 1
kern.features.ufs_quota: 1
kern.features.ufs_gjournal: 1
kern.features.ufs_acl: 1
kern.features.softupdates: 1
kern.features.ffs_snapshot: 1
kern.features.security_mac: 1
kern.features.audit: 1
kern.features.ipsec: 1
kern.features.inet6: 1
kern.features.sctp: 1
kern.features.inet: 1
kern.features.ato: 1
kern.features.posix_shm: 1
kern.features.sysv_shm: 1
kern.features.sysv_sem: 1
kern.features.sysv_msg: 1
kern.features.process_descriptors: 1
kern.features.security_capabilities: 1
kern.features.security_capability_mode: 1
kern.features.stack: 1
kern.features.rctl: 1
kern.features.racct: 1
kern.features.hwpmc_hooks: 1
kern.features.compat_freebsd7: 1
kern.features.compat_freebsd6: 1
kern.features.compat_freebsd5: 1
kern.features.compat_freebsd4: 1
kern.features.ktrace: 1
kern.features.kdtrace_hooks: 1

```

```

kern.features.kposix_priority_scheduling: 1
kern.features.geom_part_mbr: 1
kern.features.geom_part_gpt: 1
kern.features.geom_part_ebr_compat: 1
kern.features.geom_part_ebr: 1
kern.features.geom_part_bsd: 1
kern.features.geom_label: 1
kern.features.nfsd: 1
kern.features.nfscl: 1
kern.features.ata_cam: 1
kern.features.compat_freebsd_32bit: 1
kern.features.scbus: 1
kern.confctx: options CONFIG_AUTOGENERATED
kern.supported_archs: amd64 i386
kern.maxusers: 1354
kern.compiler_version: FreeBSD clang version 3.8.0 (tags/RELEASE_380/final 262564) (based on LLVM 3.8.0)
kern.ident: GENERIC
kern.malloc_count: 398
kern.module_path: /boot/kernel;/boot/modules
kern.ktrace.request_pool: 100
kern.ktrace.genio_size: 4096
kern.randompid: 0
kern.lastpid: 1818
kern.disallow_high_osrel: 0
kern.ps_arg_cache_limit: 256
kern.stackprot: 7
kern.coredump_pack_vmapinfo: 1
kern.coredump_pack_fileinfo: 1
kern.kq_calloutmax: 4096
kern.eventtimer.periodic: 0
kern.eventtimer.timer: LAPIC
kern.eventtimer.idletick: 0
kern.eventtimer.singlemul: 2
kern.eventtimer.choice: LAPIC(600) HPET(550) i8254(100) RTC(0)
kern.eventtimer.et.i8254.quality: 100
kern.eventtimer.et.i8254.frequency: 1193182
kern.eventtimer.et.i8254.flags: 1
kern.eventtimer.et.RTC.quality: 0
kern.eventtimer.et.RTC.frequency: 32768
kern.eventtimer.et.RTC.flags: 17
kern.eventtimer.et.HPET.quality: 550
kern.eventtimer.et.HPET.frequency: 24000000
kern.eventtimer.et.HPET.flags: 7
kern.eventtimer.et.LAPIC.quality: 600
kern.eventtimer.et.LAPIC.frequency: 3408120428
kern.eventtimer.et.LAPIC.flags: 7
kern.openfiles: 62
kern.chroot_allow_open_directories: 1
kern.vty: vt
kern.constty_wakeups_per_second: 5
kern.consmgbuf_size: 8192
kern.consmute: 0
kern.console: ttyv0,/uart,ttyv0,
kern.cp_times: 13 0 68 64 148984 10 0 26 1 149033 24 0 14 0 149031 8 0 14 0 149048 10 0 17 2 149042 16 0 36 1 149017 11 0 22 1 149035 11 0 19 2 149037
kern.cp_time: 103 0 216 71 1192227
kern.acct_suspended: 0
kern.acct_configured: 0
kern.acct_chkfreq: 15
kern.acct_resume: 4
kern.acct_suspend: 2
kern.init_shutdown_timeout: 120
kern.init_path: /sbin/init:/sbin/oinit:/sbin/init.bak:/rescue/init
kern.elf32.read_exec: 0
kern.elf32.nxstack: 1
kern.elf32.fallback_brand: -1
kern.elf64.nxstack: 1
kern.elf64.fallback_brand: -1
kern.geom.raid.raid5.enable: 1
kern.geom.raid.raidle.enable: 1
kern.geom.raid.raidle.rebuild_meta_update: 1024
kern.geom.raid.raidle.rebuild_cluster_idle: 100
kern.geom.raid.raidle.rebuild_fair_io: 20
kern.geom.raid.raidle.rebuild_slab_size: 1048576
kern.geom.raid.raid1.enable: 1
kern.geom.raid.raid1.rebuild_meta_update: 1024
kern.geom.raid.raid1.rebuild_cluster_idle: 100
kern.geom.raid.raid1.rebuild_fair_io: 20
kern.geom.raid.raid1.rebuild_slab_size: 1048576
kern.geom.raid.raid0.enable: 1
kern.geom.raid.concat.enable: 1
kern.geom.raid.sii.enable: 1
kern.geom.raid.promise.enable: 1
kern.geom.raid.nvidia.enable: 1
kern.geom.raid.jmicron.enable: 1
kern.geom.raid.intel.enable: 1
kern.geom.raid.ddf.enable: 1
kern.geom.raid.idle_threshold: 1000000
kern.geom.raid.name_format: 0
kern.geom.raid.disconnect_on_failure: 1
kern.geom.raid.clean_time: 5
kern.geom.raid.start_timeout: 30
kern.geom.raid.read_err_thresh: 10
kern.geom.raid.debug: 0
kern.geom.raid.aggressive_spare: 0
kern.geom.raid.enable: 1
kern.geom.part.mbr.enforce_chs: 0
kern.geom.part.check_integrity: 1
kern.geom.label.disk_ident.enable: 1
kern.geom.label.gptid.enable: 1
kern.geom.label.gpt.enable: 1
kern.geom.label.ufs.enable: 1
kern.geom.label.ufsid.enable: 1
kern.geom.label.reiserfs.enable: 1
kern.geom.label.ntfs.enable: 1
kern.geom.label.msdfs.enable: 1
kern.geom.label.iso9660.enable: 1
kern.geom.label.ext2fs.enable: 1
kern.geom.label.debug: 0
kern.geom.collectstats: 1
kern.geom.notaste: 0
kern.geom.debugflags: 0
kern.geom.confctx: 0 DISK cd0 0 2048 hd 0 sc 0
kern.geom.confdot: digraph geom {
kern.geom.confxml: <mesh>
kern.geom.inflight_transient_maps: 0
kern.geom.transient_map_soft_failures: 0
kern.geom.transient_map_hard_failures: 0
kern.geom.transient_map_retries: 10
kern.geom.transient_maps: 0
kern.geom.disk.cd0.led:

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kern.geom.disk.da0.led:
kern.geom.dev.delete_max_sectors: 262144
kern.disks: cd0 da0
kern.vt.splash_cpu_duration: 10
kern.vt.splash_cpu_style: 2
kern.vt.splash_ncpu: 0
kern.vt.splash_cpu: 0
kern.vt.kbd_panic: 0
kern.vt.kbd_debug: 1
kern.vt.kbd_reboot: 1
kern.vt.kbd_poweroff: 1
kern.vt.kbd_halt: 1
kern.vt.suspendswitch: 1
kern.vt.deadtimer: 15
kern.vt.debug: 0
kern.vt.enable_bell: 1
kern.vt.enable_altgr: 1
kern.random.fortuna.minpoolsize: 64
kern.random.harvest.mask_symbolic: [UMA],[FS_ATIME],[SWI,INTERRUPT,NET_NG,NET_ETHER,NET_TUN,MOUSE,KEYBOARD,ATTACH,CACHED]
kern.random.harvest.mask_bin: 00111111111
kern.random.harvest.mask: 511
kern.random.random_sources: 'Intel Secure Key RNG'
kern.cam.enc.emulate_array_devices: 1
kern.cam.sa.allow_io_split: 0
kern.cam.da.0.sort_io_queue: -1
kern.cam.da.0.rotating: 1
kern.cam.da.0.unmapped_io: 1
kern.cam.da.0.error_inject: 0
kern.cam.da.0.max_seq_zones: 0
kern.cam.da.0.optimal_noseq_zones: 0
kern.cam.da.0.optimal_seq_zones: 0
kern.cam.da.0.zone_support: None
kern.cam.da.0.zone_mode: Not Zoned
kern.cam.da.0.minimum_cmd_size: 6
kern.cam.da.0.delete_max: 131072
kern.cam.da.0.delete_method: NONE
kern.cam.da.default_softtimeout: 0
kern.cam.da.send_ordered: 1
kern.cam.da.default_timeout: 60
kern.cam.da.retry_count: 4
kern.cam.da.poll_period: 3
kern.cam.ada.write_cache: 1
kern.cam.ada.read_ahead: 1
kern.cam.ada.spindown_suspend: 1
kern.cam.ada.spindown_shutdown: 1
kern.cam.ada.send_ordered: 1
kern.cam.ada.default_timeout: 30
kern.cam.ada.retry_count: 4
kern.cam.cd.0.minimum_cmd_size: 6
kern.cam.cd.timeout: 30000
kern.cam.cd.retry_count: 4
kern.cam.cd.poll_period: 3
kern.cam.scsi_delay: 5000
kern.cam.cam_srch_hi: 0
kern.cam.pmp.hide_special: 1
kern.cam.pmp.default_timeout: 30
kern.cam.pmp.retry_count: 1
kern.cam.debug_delay: 0
kern.cam.dflags: 0
kern.cam.num_doneqs: 2
kern.cam.xpt_generation: 1045
kern.cam.boot_delay: 0
kern.cam.sort_io_queues: 1
vm.vmtotal:
System wide totals computed every five seconds: (values in kilobytes)
Processes: (RUNQ: 1 Disk Wait: 0 Page Wait: 0 Sleep: 20)
Virtual Memory: (Total: 349736K Active: 345132K)
Real Memory: (Total: 30716K Active: 30196K)
Shared Virtual Memory: (Total: 10784K Active: 6180K)
Shared Real Memory: (Total: 6268K Active: 5748K)
Free Memory: 15944532K
vm.loadavg: { 0.04 0.07 0.07 }
vm.v_free_min: 25572
vm.v_free_target: 86319
vm.v_free_reserved: 5323
vm.v_inactive_target: 129478
vm.v_pageout_free_min: 34
vm.swap_enabled: 1
vm.kvm_free: 2180169854976
vm.kvm_size: 2199023251456
vm.pmap.pdpe.demotions: 2
vm.pmap.pde.promotions: 189
vm.pmap.pde.p_failures: 1
vm.pmap.pde.mappings: 0
vm.pmap.pde.demotions: 103
vm.pmap.pcid_save_cnt: 16986
vm.pmap.invpcid_works: 1
vm.pmap.pcid_enabled: 1
vm.pmap.pg_ps_enabled: 1
vm.pmap.pat_works: 1
vm.idlezero_enable: 0
vm.reserv.reclaimed: 0
vm.reserv.partpop:
-1: 153008K, 87
vm.reserv.fullpop: 63
vm.reserv.freed: 4982
vm.reserv.broken: 1
vm.default_policy: rr
vm.ndomains: 1
vm.phys_segs:
SEGMENT 0:
start: 0x10000
end: 0x98000
domain: 0
free list: 0xffffffff81d78750
SEGMENT 1:
start: 0x100000
end: 0x200000
domain: 0
free list: 0xffffffff81d78750
SEGMENT 2:
start: 0x21b7000
end: 0x21e9000
domain: 0
free list: 0xffffffff81d784e0
SEGMENT 3:
start: 0x21ea000
end: 0x77050000
domain: 0
free list: 0xffffffff81d784e0

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SEGMENT 4:
start:    0x7709b000
end:      0x7c366000
domain:   0
free list: 0xffffffff81d784e0
SEGMENT 5:
start:    0x7faff000
end:      0x7fb00000
domain:   0
free list: 0xffffffff81d784e0
SEGMENT 6:
start:    0x10000000
end:      0x4629f9000
domain:   0
free list: 0xffffffff81d784e0
vm.phys_free:
DOMAIN 0:
FREE LIST 0:
FREE LIST 1:
vm.max_wired: 1333880
vm.pageout_oom_seq: 12
vm.pageout_lock_miss: 0
vm.disable_swapspace_pageouts: 0
vm.defer_swapspace_pageouts: 0
vm.swap_idle_enabled: 0
vm.lowmem_period: 10
vm.pageout_update_period: 600
vm.max_laundry: 32
vm.pageout_wakeup_thresh: 28127
vm.panic_on_oom: 0
vm.page_blacklist:
vm.tryrelock_restart: 0
vm.boot_pages: 64
vm.old_msync: 0
vm.old_mlock: 0
vm.stats.object.bypasses: 578
vm.stats.object.collapses: 4421
vm.stats.misc.cnt_prezero: 0
vm.stats.misc.zero_page_count: 32
vm.stats.vm.v_kthreadpages: 0
vm.stats.vm.v_rforkpages: 0
vm.stats.vm.v_vforkpages: 12871
vm.stats.vm.v_forkpages: 51544
vm.stats.vm.v_kthreads: 24
vm.stats.vm.v_rforks: 0
vm.stats.vm.v_vforks: 368
vm.stats.vm.v_forks: 1426
vm.stats.vm.v_interrupt_free_min: 2
vm.stats.vm.v_pageout_free_min: 34
vm.stats.vm.v_cache_count: 0
vm.stats.vm.v_inactive_count: 3435
vm.stats.vm.v_inactive_target: 129478
vm.stats.vm.v_active_count: 4401
vm.stats.vm.v_wire_count: 56132
vm.stats.vm.v_free_count: 3986130
vm.stats.vm.v_free_min: 25572
vm.stats.vm.v_free_target: 86319
vm.stats.vm.v_free_reserved: 5323
vm.stats.vm.v_page_count: 4050099
vm.stats.vm.v_page_size: 4096
vm.stats.vm.v_tfree: 197562
vm.stats.vm.v_pfree: 84252
vm.stats.vm.v_dfree: 0
vm.stats.vm.v_tcached: 0
vm.stats.vm.v_pdpages: 6595
vm.stats.vm.v_pdwakeup: 0
vm.stats.vm.v_reactivated: 0
vm.stats.vm.v_intrans: 0
vm.stats.vm.v_vnodepgsout: 0
vm.stats.vm.v_vnodepgsin: 3505
vm.stats.vm.v_vnodeout: 0
vm.stats.vm.v_vnodein: 458
vm.stats.vm.v_swappgsout: 0
vm.stats.vm.v_swappgsin: 0
vm.stats.vm.v_swapout: 0
vm.stats.vm.v_swapin: 0
vm.stats.vm.v_ozfod: 0
vm.stats.vm.v_zfod: 60272
vm.stats.vm.v_cow_optim: 167
vm.stats.vm.v_cow_faults: 52954
vm.stats.vm.v_io_faults: 338
vm.stats.vm.v_vm_faults: 129213
vm.stats.sys.v_soft: 9142
vm.stats.sys.v_intr: 14488
vm.stats.sys.v_syscall: 1046626
vm.stats.sys.v_trap: 130141
vm.stats.sys.v_swthc: 136691
vm.v_free_severe: 15447
vm.max_kernel_address: 18446744073709547520
vm.min_kernel_address: 18446741874686296064
vm.exec_map_entries: 16
vm.swap_idle_threshold2: 10
vm.swap_idle_threshold1: 2
vm.kstacks: 155
vm.kstack_cache_size: 128
vm.zone_warnings: 1
vm.zone_count: 113
vm.nswapdev: 1
vm.dmmax: 32
vm.swap_async_max: 4
vm.swap_maxpages: 64801568
vm.swapzone: 583214112
vm.overcommit: 0
vm.swap_reserved: 125628416
vm.swap_total: 4294930432
vm.kmem_map_free: 16542834688
vm.kmem_map_size: 46370816
vm.kmem_size_scale: 1
vm.kmem_size_max: 1319413950874
vm.kmem_size_min: 0
vm.kmem_zmax: 65536
vm.kmem_size: 16589205504
vm.md_malloc_wait: 0
vfs.ufs.rename_restarts: 0
vfs.ufs.dirhash_reclaimpercent: 10
vfs.ufs.dirhash_lowmemcount: 0
vfs.ufs.dirhash_docheck: 0
vfs.ufs.dirhash_mem: 48533
vfs.ufs.dirhash_maxmem: 26853376
vfs.ufs.dirhash_minsize: 2560

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vfs.nfs.downdelayinitial: 12  
vfs.nfs.downdelayinterval: 30  
vfs.nfs.defect: 0  
vfs.nfs.iodmax: 20  
vfs.nfs.iodmin: 0  
vfs.nfs.iodmaxidle: 120  
vfs.nfs.fileid\_maxwarnings: 10  
vfs.nfs.diskless\_rootpath:  
vfs.nfs.diskless\_valid: 0  
vfs.nfs.nfs\_ip\_paranoid: 1  
vfs.nfs.nfs\_directio\_allow\_mmap: 1  
vfs.nfs.nfs\_keep\_dirty\_on\_error: 0  
vfs.nfs.nfs\_directio\_enable: 0  
vfs.nfs.clean\_pages\_on\_close: 1  
vfs.nfs.commit\_on\_close: 0  
vfs.nfs.prime\_access\_cache: 0  
vfs.nfs.access\_cache\_timeout: 60  
vfs.nfs.ignore\_eexist: 0  
vfs.nfs.userhashsize: 100  
vfs.nfs.debuglevel: 0  
vfs.nfs.callback\_addr:  
vfs.nfs.realign\_count: 0  
vfs.nfs.realign\_test: 0  
vfs.nfs.skip\_wcc\_data\_onerr: 1  
vfs.nfs.nfs3\_jukebox\_delay: 10  
vfs.nfs.reconnects: 0  
vfs.nfs.bufpackets: 4  
vfs.devfs.dotime: 0  
vfs.devfs.rule\_depth: 1  
vfs.devfs.generation: 145  
vfs.ffs.compute\_summary\_at\_mount: 0  
vfs.ffs.maxclustersearch: 10  
vfs.ffs.dorealloclks: 1  
vfs.ffs.doasyncfree: 1  
vfs.worklist\_len: 6  
vfs.timestamp\_precision: 2  
vfs.free\_owe\_inact: 0  
vfs.reassignbufcalls: 592  
vfs.recycles: 0  
vfs.freevnodes: 33  
vfs.wantfreevnodes: 87135  
vfs.vnodes\_created: 587  
vfs.numvnodes: 545  
vfs.root\_mount\_hold:  
vfs.usermount: 0  
vfs.lookup\_shared: 1  
vfs.tyenumhash: 1  
vfs.read\_min: 1  
vfs.read\_max: 64  
vfs.write\_behind: 1  
vfs.cache.numupgrades: 1  
vfs.cache.numfullpathfound: 17  
vfs.cache.numfullpathfail4: 0  
vfs.cache.numfullpathfail2: 0  
vfs.cache.numfullpathfail1: 0  
vfs.cache.numfullpathcalls: 17  
vfs.cache.numneghits: 7084  
vfs.cache.numnegzaps: 0  
vfs.cache.numposhits: 43554  
vfs.cache.numposzaps: 84  
vfs.cache.nummisszap: 126  
vfs.cache.nummiss: 1565  
vfs.cache.numchecks: 54450  
vfs.cache.dotdohits: 15  
vfs.cache.dothits: 307  
vfs.cache.numcalls: 52735  
vfs.cache.numcache: 505  
vfs.cache.numneg: 31  
vfs.ncsizefactor: 2  
vfs.ncnegfactor: 16  
vfs.flushwithdeps: 0  
vfs.unmapped\_buf\_allowed: 1  
vfs.barrierwrites: 1  
vfs.notbufdflushes: 0  
vfs.flushbufqtarget: 100  
vfs.numbufallocfails: 0  
vfs.mappingrestarts: 0  
vfs.getnewbufrestarts: 0  
vfs.getnewbufcalls: 2375  
vfs.hifreebuffers: 1536  
vfs.lofreebuffers: 1024  
vfs.numfreebuffers: 104485  
vfs.dirtybufthresh: 23625  
vfs.hidirtybuffers: 26251  
vfs.lodirtybuffers: 13125  
vfs.numdirtybuffers: 23  
vfs.recursiveflushes: 0  
vfs.altbufferflushes: 0  
vfs.bdwriteskip: 0  
vfs.dirtybufferflushes: 0  
vfs.hirunningspace: 16777216  
vfs.lorunningspace: 11206656  
vfs.bufdefragcnt: 0  
vfs.buffreekvacnt: 1908  
vfs.bufspacethresh: 1675474944  
vfs.hibufspace: 1718435840  
vfs.lobufspace: 1632514048  
vfs.maxmalloccbufspace: 85921792  
vfs.bufmalloccspace: 0  
vfs.maxbufspace: 1719091200  
vfs.bufkvaspace: 4702208  
vfs.bufspace: 4857856  
vfs.runningbufspace: 0  
vfs.vmiotirenable: 1  
vfs.aio.max\_buf\_aio: 16  
vfs.aio.max\_aio\_queue\_per\_proc: 256  
vfs.aio.max\_aio\_per\_proc: 32  
vfs.aio.aiod\_lifetime: 30000  
vfs.aio.num\_buf\_aio: 0  
vfs.aio.num\_queue\_count: 0  
vfs.aio.max\_aio\_queue: 1024  
vfs.aio.target\_aio\_procs: 4  
vfs.aio.num\_aio\_procs: 0  
vfs.aio.max\_aio\_procs: 32  
vfs.aio.unsafe\_warningcnt: 1  
vfs.aio.enable\_unsafe: 0  
vfs.acl\_nfs4\_old\_semantics: 0  
vfs.pfs.vncache.misses: 0  
vfs.pfs.vncache.hits: 0  
vfs.pfs.vncache.maxentries: 0

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vfs.pfs.vncache_entries: 0
vfs.nfsd.fha.fha_stats: No file handle entries.
vfs.nfsd.fha.max_reqs_per_nfsd: 0
vfs.nfsd.fha.max_nfsds_per_fh: 8
vfs.nfsd.fha.bin_shift: 22
vfs.nfsd.fha.enable: 1
vfs.nfsd.request_space_throttle_count: 0
vfs.nfsd.request_space_throttled: 0
vfs.nfsd.request_space_low: 345608192
vfs.nfsd.request_space_high: 518412288
vfs.nfsd.request_space_used_highest: 0
vfs.nfsd.request_space_used: 0
vfs.nfsd.groups: 1
vfs.nfsd.threads: 0
vfs.nfsd.maxthreads: 1
vfs.nfsd.minthreads: 1
vfs.nfsd.cachetcp: 1
vfs.nfsd.tpcachetimeo: 43200
vfs.nfsd.udphighwater: 500
vfs.nfsd.tcphighwater: 0
vfs.nfsd.enable_stringtoid: 0
vfs.nfsd.debuglevel: 0
vfs.nfsd.enable_locallocks: 0
vfs.nfsd.issue_delegations: 0
vfs.nfsd.commit_miss: 0
vfs.nfsd.commit_blks: 0
vfs.nfsd.mirrormnt: 1
vfs.nfsd.async: 0
vfs.nfsd.server_max_nfsvers: 4
vfs.nfsd.server_min_nfsvers: 2
vfs.nfsd.nfs_privport: 0
vfs.nfsd.v4statelimit: 500000
vfs.nfsd.sessionhashsize: 20
vfs.nfsd.fhhashsize: 20
vfs.nfsd.clienthashsize: 20
vfs.nfsd.statehashsize: 10
vfs.nfsd.enable_nogroupcheck: 1
vfs.nfsd.enable_nobodycheck: 1
vfs.nfsd.enable_checkutf8: 1
net.local.stream.recvspace: 8192
net.local.stream.sendspace: 8192
net.local.dgram.recvspace: 4096
net.local.dgram.maxdgram: 2048
net.local.segpacket.recvspace: 8192
net.local.segpacket.maxsegpacket: 8192
net.local.taskcount: 0
net.local.recycled: 0
net.local.deferred: 0
net.local.inflight: 0
net.inet.ip.portrange.randomtime: 45
net.inet.ip.portrange.randomcps: 10
net.inet.ip.portrange.randomized: 1
net.inet.ip.portrange.reservedlow: 0
net.inet.ip.portrange.reservedhigh: 1023
net.inet.ip.portrange.hilast: 65535
net.inet.ip.portrange.hifirst: 49152
net.inet.ip.portrange.last: 65535
net.inet.ip.portrange.first: 10000
net.inet.ip.portrange.lowlast: 600
net.inet.ip.portrange.lowfirst: 1023
net.inet.ip.forwarding: 0
net.inet.ip.redirect: 1
net.inet.ip.ttl: 64
net.inet.ip.sourceroute: 0
net.inet.ip.intr_queue_maxlen: 256
net.inet.ip.intr_queue_drops: 0
net.inet.ip.accept_sourceroute: 0
net.inet.ip.gifttl: 30
net.inet.ip.maxfragsperpacket: 16
net.inet.ip.fragpackets: 0
net.inet.ip.maxfragpackets: 31666
net.inet.ip.process_options: 1
net.inet.ip.check_interface: 0
net.inet.ip.mcast.loop: 1
net.inet.ip.mcast.maxsocksrc: 128
net.inet.ip.mcast.maxgrpsrc: 512
net.inet.ip.random_id_total: 0
net.inet.ip.random_id_collisions: 0
net.inet.ip.random_id_period: 0
net.inet.ip.rfc6864: 1
net.inet.ip.random_id: 0
net.inet.ip.no_same_prefix: 0
net.inet.icmp.maskrepl: 0
net.inet.icmp.icmplim: 200
net.inet.icmp.tstamprepl: 1
net.inet.icmp.bmcastecho: 0
net.inet.icmp.quotelen: 8
net.inet.icmp.reply_from_interface: 0
net.inet.icmp.reply_src:
net.inet.icmp.log_redirect: 0
net.inet.icmp.drop_redirect: 0
net.inet.icmp.maskfake: 0
net.inet.icmp.icmplim_output: 1
net.inet.igmp.gsrdelay: 10
net.inet.igmp.default_version: 3
net.inet.igmp.legacysupp: 0
net.inet.igmp.v2enable: 1
net.inet.igmp.v1enable: 1
net.inet.igmp.sendlocal: 1
net.inet.igmp.sendra: 1
net.inet.igmp.recvifkludge: 1
net.inet.tcp.rfc1323: 1
net.inet.tcp.mssdflt: 536
net.inet.tcp.keepidle: 7200000
net.inet.tcp.keeptv1: 75000
net.inet.tcp.sendspace: 32768
net.inet.tcp.recvspace: 65536
net.inet.tcp.keeptv2: 75000
net.inet.tcp.delacktime: 100
net.inet.tcp.v6mssdflt: 1220
net.inet.tcp.nolocaltimewait: 0
net.inet.tcp.maxtcpt: 27767
net.inet.tcp.per_cpu_timers: 0
net.inet.tcp.v6pmtud_blackhole_mss: 1220
net.inet.tcp.pmtud_blackhole_mss: 1200
net.inet.tcp.pmtud_blackhole_failed: 0
net.inet.tcp.pmtud_blackhole_activated_min_mss: 0
net.inet.tcp.pmtud_blackhole_activated: 0
net.inet.tcp.pmtud_blackhole_detection: 0
net.inet.tcp.rexmit_drop_options: 0
```

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net.inet.tcp.keepcnt: 8
net.inet.tcp.finwait2_timeout: 60000
net.inet.tcp.fast_finwait2_recycle: 0
net.inet.tcp.always_keepalive: 1
net.inet.tcp.rexmit_slip: 200
net.inet.tcp.rexmit_min: 30
net.inet.tcp.msl: 30000
net.inet.tcp.persmax: 60000
net.inet.tcp.persmin: 5000
net.inet.tcp.syncache.rst_on_sock_fail: 1
net.inet.tcp.syncache.rexmtlimit: 3
net.inet.tcp.syncache.hashsize: 512
net.inet.tcp.syncache.count: 0
net.inet.tcp.syncache.cachelimit: 15364
net.inet.tcp.syncache.bucketlimit: 30
net.inet.tcp.syncookies_only: 0
net.inet.tcp.syncookies: 1
net.inet.tcp.functions_available:
net.inet.tcp.functions_default: default
net.inet.tcp.soreceive_stream: 0
net.inet.tcp.isn_reseed_interval: 0
net.inet.tcp.icmp_may_rst: 1
net.inet.tcp.pcbcount: 3
net.inet.tcp.do_tcpdrain: 1
net.inet.tcp.tcbhashsize: 131072
net.inet.tcp.log_debug: 0
net.inet.tcp.minms: 216
net.inet.tcp.sack.globalholes: 0
net.inet.tcp.sack.globalmaxholes: 65536
net.inet.tcp.sack.maxholes: 128
net.inet.tcp.sack.enable: 1
net.inet.tcp.reass.cursegments: 0
net.inet.tcp.reass.maxsegments: 63300
net.inet.tcp.sendbuf_max: 2097152
net.inet.tcp.sendbuf_inc: 8192
net.inet.tcp.sendbuf_auto: 1
net.inet.tcp.tso: 1
net.inet.tcp.path_mtu_discovery: 1
net.inet.tcp.recvbuf_max: 2097152
net.inet.tcp.recvbuf_inc: 16384
net.inet.tcp.recvbuf_auto: 1
net.inet.tcp.insecure_rst: 0
net.inet.tcp.insecure_syn: 0
net.inet.tcp.ecn.maxretries: 1
net.inet.tcp.ecn.enable: 2
net.inet.tcp.abc_l_var: 2
net.inet.tcp.rfc3465: 1
net.inet.tcp.initcwnd_segments: 10
net.inet.tcp.rfc3390: 1
net.inet.tcp.rfc3042: 1
net.inet.tcp.rfc6675_pipe: 0
net.inet.tcp.drop_synfin: 0
net.inet.tcp.delayed_ack: 1
net.inet.tcp.blackhole: 0
net.inet.tcp.log_in_vain: 0
net.inet.tcp.hostcache.purgenow: 0
net.inet.tcp.hostcache.purge: 0
net.inet.tcp.hostcache.prune: 300
net.inet.tcp.hostcache.expire: 3600
net.inet.tcp.hostcache.count: 0
net.inet.tcp.hostcache.bucketlimit: 30
net.inet.tcp.hostcache.hashsize: 512
net.inet.tcp.hostcache.cachelimit: 15360
net.inet.tcp.cc.available: newreno
net.inet.tcp.cc.algorithm: newreno
net.inet.udp.checksum: 1
net.inet.udp.maxdgram: 9216
net.inet.udp.recvspace: 42080
net.inet.udp.blackhole: 0
net.inet.udp.log_in_vain: 0
net.inet.esp.esp_enable: 1
net.inet.ah.ah_clearartos: 1
net.inet.ah.ah_enable: 1
net.inet.ipcomp.ipcomp_enable: 1
net.inet.sctp.diag_info_code: 0
net.inet.sctp.blackhole: 0
net.inet.sctp.use_dccecn: 1
net.inet.sctp.rttvar_steady_step: 20
net.inet.sctp.rttvar_eqret: 0
net.inet.sctp.rttvar_rtt: 5
net.inet.sctp.rttvar_bw: 4
net.inet.sctp.initial_cwnd: 3
net.inet.sctp.buffer_splitting: 0
net.inet.sctp.vtag_time_wait: 60
net.inet.sctp.nat_friendly_init: 0
net.inet.sctp.enable_sack_immediately: 1
net.inet.sctp.udp_tunneling_port: 0
net.inet.sctp.mobility_fasthandoff: 0
net.inet.sctp.mobility_base: 0
net.inet.sctp.default_frag_interleave: 1
net.inet.sctp.default_ss_module: 0
net.inet.sctp.default_cc_module: 0
net.inet.sctp.log_level: 0
net.inet.sctp.max_retran_chunk: 30
net.inet.sctp.min_residual: 1452
net.inet.sctp.abort_at_limit: 0
net.inet.sctp.hb_max_burst: 4
net.inet.sctp.do_sctp_drain: 1
net.inet.sctp.max_chained_mbufs: 5
net.inet.sctp.abc_l_var: 2
net.inet.sctp.nat_friendly: 1
net.inet.sctp.cwnd_maxburst: 1
net.inet.sctp.cmt_use_dac: 0
net.inet.sctp.cmt_on_off: 0
net.inet.sctp.outgoing_streams: 10
net.inet.sctp.incoming_streams: 2048
net.inet.sctp.add_more_on_output: 1452
net.inet.sctp.path_pf_threshold: 65535
net.inet.sctp.path_rtx_max: 5
net.inet.sctp.assoc_rtx_max: 10
net.inet.sctp.init_rtx_max: 8
net.inet.sctp.valid_cookie_life: 60000
net.inet.sctp.init_rto_max: 60000
net.inet.sctp.rto_initial: 3000
net.inet.sctp.rto_min: 1000
net.inet.sctp.rto_max: 60000
net.inet.sctp.secret_lifetime: 3600
net.inet.sctp.shutdown_guard_time: 0
net.inet.sctp.pmtu_raise_time: 600
net.inet.sctp.heartbeat_interval: 30000
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net.inet.sctp.asoc_resource: 10
net.inet.sctp.sys_resource: 1000
net.inet.sctp.sack_freq: 2
net.inet.sctp.delayed_sack_time: 200
net.inet.sctp.chunkscale: 10
net.inet.sctp.min_split_point: 2904
net.inet.sctp.pcbhashsize: 256
net.inet.sctp.tcbhashsize: 1024
net.inet.sctp.maxchunks: 126565
net.inet.sctp.fr_maxburst: 4
net.inet.sctp.maxburst: 4
net.inet.sctp.peer_chkoh: 256
net.inet.sctp.pkt_drop_enable: 0
net.inet.sctp.nrsack_enable: 0
net.inet.sctp.reconfig_enable: 1
net.inet.sctp.asconf_enable: 1
net.inet.sctp.auth_enable: 1
net.inet.sctp.pr_enable: 1
net.inet.sctp.ecn_enable: 1
net.inet.sctp.auto_asconf: 1
net.inet.sctp.recvspace: 1864135
net.inet.sctp.sendspace: 1864135
net.inet.ipsec.def_policy: 1
net.inet.ipsec.esp_trans_deflev: 1
net.inet.ipsec.esp_net_deflev: 1
net.inet.ipsec.ah_trans_deflev: 1
net.inet.ipsec.ah_net_deflev: 1
net.inet.ipsec.ah_clearartos: 1
net.inet.ipsec.ah_offsetmask: 0
net.inet.ipsec.dfbits: 0
net.inet.ipsec.ecn: 0
net.inet.ipsec.debug: 0
net.inet.ipsec.crypto_support: 50331648
net.inet.ipsec.filtertunnel: 0
net.inet.raw.recvspace: 9216
net.inet.raw.maxdgram: 9216
net.link.generic.system.ifcount: 11
net.link.ether.inet.allow_multicast: 0
net.link.ether.inet.log_arp_permanent_modify: 1
net.link.ether.inet.log_arp_movements: 1
net.link.ether.inet.log_arp_wrong_iface: 1
net.link.ether.inet.max_log_per_second: 1
net.link.ether.inet.maxhold: 1
net.link.ether.inet.wait: 20
net.link.ether.inet.proxyall: 0
net.link.ether.inet.maxtries: 5
net.link.ether.inet.max_age: 1200
net.link.vlan.mtag_pcp: 0
net.link.vlan.soft_pad: 0
net.link.gif.parallel_tunnels: 0
net.link.gif.max_nesting: 1
net.link.tun.devfs_cloning: 1
net.link.log_promisc_mode_change: 1
net.link.log_link_state_change: 1
net.link.ifqmaxlen: 50
net.key.debug: 0
net.key.spi_trycnt: 1000
net.key.spi_minval: 256
net.key.spi_maxval: 268435455
net.key.int_random: 60
net.key.larval_lifetime: 30
net.key.blockacq_count: 10
net.key.blockacq_lifetime: 20
net.key.esp_keymin: 256
net.key.esp_auth: 0
net.key.ah_keymin: 128
net.key.preferred_oldsa: 1
net.inet6.ip6.forwarding: 0
net.inet6.ip6.redirect: 1
net.inet6.ip6.hlim: 64
net.inet6.ip6.maxfrags: 253131
net.inet6.ip6.accept_rtadv: 0
net.inet6.ip6.log_interval: 5
net.inet6.ip6.hdrnestlimit: 15
net.inet6.ip6.dad_count: 1
net.inet6.ip6.auto_flowlabel: 1
net.inet6.ip6.defmcasthlim: 1
net.inet6.ip6.gifhlim: 30
net.inet6.ip6.kame_version: FreeBSD
net.inet6.ip6.use_deprecated: 1
net.inet6.ip6.rp_prune: 5
net.inet6.ip6.v6only: 1
net.inet6.ip6.use_tempaddr: 0
net.inet6.ip6.temppltime: 86400
net.inet6.ip6.tempvtime: 604800
net.inet6.ip6.auto_linklocal: 1
net.inet6.ip6.prefer_tempaddr: 0
net.inet6.ip6.use_defaultzone: 0
net.inet6.ip6.maxfrags: 253131
net.inet6.ip6.mcast_pmtu: 0
net.inet6.ip6.no_radv: 0
net.inet6.ip6.norbit_raif: 0
net.inet6.ip6.rfc6204w3: 0
net.inet6.ip6.deembed_scopeid: 1
net.inet6.ip6.dad_enhanced: 1
net.inet6.ip6.mcast_loop: 1
net.inet6.ip6.mcast_maxsocksrc: 128
net.inet6.ip6.mcast_maxgrpsrc: 512
net.inet6.ipsec6.def_policy: 1
net.inet6.ipsec6.esp_trans_deflev: 1
net.inet6.ipsec6.esp_net_deflev: 1
net.inet6.ipsec6.ah_trans_deflev: 1
net.inet6.ipsec6.ah_net_deflev: 1
net.inet6.ipsec6.ecn: 0
net.inet6.ipsec6.debug: 0
net.inet6.ipsec6.filtertunnel: 0
net.inet6.icmp6.rediraccept: 1
net.inet6.icmp6.redirtimeout: 600
net.inet6.icmp6.nd6_prune: 1
net.inet6.icmp6.nd6_delay: 5
net.inet6.icmp6.nd6_umaxtries: 3
net.inet6.icmp6.nd6_mmaxtries: 3
net.inet6.icmp6.nd6_useloopback: 1
net.inet6.icmp6.nodeinfo: 3
net.inet6.icmp6.errpslimit: 100
net.inet6.icmp6.nd6_maxnudhint: 0
net.inet6.icmp6.nd6_debug: 0
net.inet6.icmp6.nd6_maxqueueLen: 1
net.inet6.icmp6.nodeinfo_oldmcprefix: 1
net.inet6.icmp6.nd6_onlink_ns_rfc4861: 0
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net.inet6.icmp6.nd6_gctimer: 86400
net.inet6.mld.use_allow: 1
net.inet6.mld.vlenable: 1
net.inet6.mld.gsrdelay: 10
net.wlan.mesh.maxholding: 2
net.wlan.mesh.maxretries: 2
net.wlan.mesh.backofftimeout: 5000
net.wlan.mesh.confirmtimeout: 40
net.wlan.mesh.holdingtimeout: 40
net.wlan.mesh.retrytimeout: 40
net.wlan.mesh.gateint: 10000
net.wlan.hwmp.inact: 5000
net.wlan.hwmp.rootconfint: 2000
net.wlan.hwmp.rannint: 1000
net.wlan.hwmp.rootint: 2000
net.wlan.hwmp.roottimeout: 5000
net.wlan.hwmp.net_diameter_traversal_time: 512
net.wlan.hwmp.maxpreg_retries: 3
net.wlan.hwmp.pathlifetime: 5000
net.wlan.hwmp.targetonly: 0
net.wlan.addba_maxtries: 3
net.wlan.addba_backoff: 10000
net.wlan.addba_timeout: 250
net.wlan.recv_bar: 1
net.wlan.ampdu_age: 500
net.wlan.debug: 0
net.wlan.cac_timeout: 60
net.wlan.nol_timeout: 1800
net.wlan.devices:
net.route.netisr_maxqlen: 256
net.my_fibnum: 0
net.add_addr_allfibs: 1
net.fibs: 1
net.raw.recvspace: 8192
net.raw.sendspace: 8192
net.isr.numthreads: 1
net.isr.maxprot: 16
net.isr.defaultqlimit: 256
net.isr.maxqlimit: 10240
net.isr.bindthreads: 0
net.isr.maxthreads: 1
net.isr.dispatch: direct
net.iflib.min_tx_latency: 0
net.ifdescr_maxlen: 1024
net.bpf.maxbufsize: 524288
net.bpf.bufsize: 4096
net.bpf.optimize_writers: 0
net.bpf.zerocopy_enable: 0
net.bpf.maxinsns: 512
net.accf.unloadable: 0
debug.minidump: 1
debug.hwpstate_verbose: 0
debug.x86bios.int: 0
debug.x86bios.call: 0
debug.fdc.settle: 0
debug.fdc.spec2: 16
debug.fdc.spec1: 175
debug.fdc.retries: 10
debug.fdc.debugflags: 0
debug.fdc.fifo: 8
debug.vesa.shadow_ram: 0
debug.psm.pkterrthresh: 2
debug.psm.usecs: 500000
debug.psm.secs: 0
debug.psm.errusecs: 0
debug.psm.errsecs: 2
debug.psm.hz: 20
debug.psm.loglevel: 0
debug.dircheck: 0
debug.dobkgrdwrite: 1
debug.bigcgs: 0
debug.softdep.print_threads: 0
debug.softdep.emptyjblocks: 2
debug.softdep.flushcache: 0
debug.softdep.cleanup_failures: 0
debug.softdep.cleanup_retries: 0
debug.softdep.cleanup_high_delay: 0
debug.softdep.cleanup_inorequests: 0
debug.softdep.cleanup_blkrequests: 0
debug.softdep.jwait_newblk: 5
debug.softdep.jwait_inode: 2
debug.softdep.jwait_freeblks: 0
debug.softdep.jwait_filepage: 0
debug.softdep.journal_wait: 7
debug.softdep.journal_min: 0
debug.softdep.journal_low: 0
debug.softdep.jnewblk_rollback: 0
debug.softdep.jddref_rollback: 2
debug.softdep.dir_entry: 8
debug.softdep.direct_blk_ptrs: 17
debug.softdep.inode_bitmap: 8
debug.softdep.indir_blk_ptrs: 0
debug.softdep.sync_limit_hit: 0
debug.softdep.ino_limit_hit: 0
debug.softdep.blk_limit_hit: 0
debug.softdep.ino_limit_push: 0
debug.softdep.blk_limit_push: 0
debug.softdep.worklist_push: 0
debug.softdep.flush_threads: 1
debug.softdep.tickdelay: 2
debug.softdep.max_softdeps: 1394172
debug.softdep.write.jfsync: 0
debug.softdep.write.jtrunc: 0
debug.softdep.write.sbdp: 12
debug.softdep.write.jsegdep: 2
debug.softdep.write.jseg: 24
debug.softdep.write.jfreefrag: 0
debug.softdep.write.jfreeblk: 0
debug.softdep.write.jnewblk: 0
debug.softdep.write.jmvrref: 0
debug.softdep.write.jremref: 0
debug.softdep.write.jddref: 0
debug.softdep.write.freedeep: 0
debug.softdep.write.freework: 0
debug.softdep.write.newdirblk: 0
debug.softdep.write.dirrem: 0
debug.softdep.write.mkdir: 0
debug.softdep.write.diradd: 0
debug.softdep.write.freefile: 0
debug.softdep.write.freeblks: 9

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debug.softdep.write.freefrag: 0
debug.softdep.write.allocindir: 0
debug.softdep.write.indirdep: 0
debug.softdep.write.allocdirect: 62
debug.softdep.write.newblk: 0
debug.softdep.write.bmsafemap: 24
debug.softdep.write.inodedep: 189
debug.softdep.write.pagedep: 32
debug.softdep.current.jfsync: 0
debug.softdep.current.jtrunc: 0
debug.softdep.current.sbdep: 0
debug.softdep.current.jsegdep: 10
debug.softdep.current.jseg: 11
debug.softdep.current.jfreefrag: 0
debug.softdep.current.jfreeblk: 0
debug.softdep.current.jnewblk: 3
debug.softdep.current.jmhref: 0
debug.softdep.current.jremref: 0
debug.softdep.current.jaddrf: 1
debug.softdep.current.freedep: 0
debug.softdep.current.freework: 4
debug.softdep.current.newdirblk: 0
debug.softdep.current.dirrem: 3
debug.softdep.current.mkdir: 0
debug.softdep.current.diradd: 2
debug.softdep.current.freefile: 2
debug.softdep.current.freeblks: 4
debug.softdep.current.freefrag: 1
debug.softdep.current.allocindir: 0
debug.softdep.current.indirdep: 0
debug.softdep.current.allocdirect: 2
debug.softdep.current.newblk: 0
debug.softdep.current.bmsafemap: 3
debug.softdep.current.inodedep: 9
debug.softdep.current.pagedep: 4
debug.softdep.highuse.jfsync: 0
debug.softdep.highuse.jtrunc: 0
debug.softdep.highuse.sbdep: 1
debug.softdep.highuse.jsegdep: 165
debug.softdep.highuse.jseg: 11
debug.softdep.highuse.jfreefrag: 1
debug.softdep.highuse.jfreeblk: 0
debug.softdep.highuse.jnewblk: 33
debug.softdep.highuse.jmhref: 0
debug.softdep.highuse.jremref: 26
debug.softdep.highuse.jaddrf: 49
debug.softdep.highuse.freedep: 0
debug.softdep.highuse.freework: 23
debug.softdep.highuse.newdirblk: 12
debug.softdep.highuse.dirrem: 20
debug.softdep.highuse.mkdir: 24
debug.softdep.highuse.diradd: 61
debug.softdep.highuse.freefile: 5
debug.softdep.highuse.freeblks: 23
debug.softdep.highuse.freefrag: 1
debug.softdep.highuse.allocindir: 0
debug.softdep.highuse.indirdep: 0
debug.softdep.highuse.allocdirect: 64
debug.softdep.highuse.newblk: 1
debug.softdep.highuse.bmsafemap: 6
debug.softdep.highuse.inodedep: 84
debug.softdep.highuse.pagedep: 21
debug.softdep.total.jfsync: 0
debug.softdep.total.jtrunc: 0
debug.softdep.total.sbdep: 12
debug.softdep.total.jsegdep: 317
debug.softdep.total.jseg: 24
debug.softdep.total.jfreefrag: 4
debug.softdep.total.jfreeblk: 0
debug.softdep.total.jnewblk: 107
debug.softdep.total.jmhref: 0
debug.softdep.total.jremref: 64
debug.softdep.total.jaddrf: 142
debug.softdep.total.freedep: 0
debug.softdep.total.freework: 35
debug.softdep.total.newdirblk: 15
debug.softdep.total.dirrem: 50
debug.softdep.total.mkdir: 30
debug.softdep.total.diradd: 112
debug.softdep.total.freefile: 29
debug.softdep.total.freeblks: 35
debug.softdep.total.freefrag: 4
debug.softdep.total.allocindir: 0
debug.softdep.total.indirdep: 0
debug.softdep.total.allocdirect: 107
debug.softdep.total.newblk: 107
debug.softdep.total.bmsafemap: 109
debug.softdep.total.inodedep: 122
debug.softdep.total.pagedep: 35
debug.collectsnapstats: 0
debug.snapdebug: 0
debug.dopersistence: 0
debug.fsckcmds: 0
debug.crypto_timing: 0
debug.nlm_debug: 0
debug.if_tun_debug: 0
debug.vn_io_faults: 520
debug.vn_io_fault_prefault: 0
debug.vn_io_fault_enable: 1
debug.max_vnlru_free: 10000
debug.vnlru_nowhere: 0
debug.rush_requests: 0
debug.disablefullpath: 0
debug.disablecwd: 0
debug.vfscache: 1
debug.numcachehv: 50
debug.numcache: 505
debug.numneg: 31
debug.nchash: 524287
debug.devfs_iosize_max_clamp: 1
debug.iosize_max_clamp: 0
debug.rman_debug: 0
debug.kdb.alt_break_to_debugger: 0
debug.kdb.break_to_debugger: 0
debug.kdb.trap_code: 0
debug.kdb.trap: 0
debug.kdb.panic: 0
debug.kdb.enter: 0
debug.kdb.current:
debug.kdb.available:

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debug.clocktime: 0
debug.umtx.robust_faults_verbose: 1
debug.umtx.umtx_pi_allocated: 0
debug.sx.delay_max: 2000
debug.sx.delay_min: 40
debug.sx.delay_step: 100
debug.sx.delay_initial: 200
debug.sx.loops: 10000
debug.sx.retries: 10
debug.ncores: 5
debug.trace_on_panic: 1
debug.debugger_on_panic: 1
debug.rwlock.delay_max: 2000
debug.rwlock.delay_min: 40
debug.rwlock.delay_step: 100
debug.rwlock.delay_initial: 200
debug.rwlock.loops: 10000
debug.rwlock.retry: 10
debug.osd: 0
debug.mtx.delay_max: 2000
debug.mtx.delay_min: 40
debug.mtx.delay_step: 100
debug.mtx.delay_initial: 200
debug.adaptive_machine_arch: 1
debug.sizeof.namecache: 72
debug.sizeof.devstat: 288
debug.sizeof.pcb: 320
debug.sizeof.kinfo_proc: 1088
debug.sizeof.buf: 560
debug.sizeof.bio: 376
debug.sizeof.proc: 1320
debug.sizeof.vnode: 472
debug.sizeof.g_bioq: 56
debug.sizeof.g_consumer: 96
debug.sizeof.g_provider: 136
debug.sizeof.g_geom: 176
debug.sizeof.g_class: 176
debug.sizeof.cdev_priv: 376
debug.sizeof.cdev: 288
debug.fail_point.status_nlm_deny_grant: off
debug.fail_point.nlm_deny_grant: off
debug.fail_point.status_fill_kinfo_vnode__random_path: off
debug.fail_point.fill_kinfo_vnode__random_path: off
debug.fail_point.status_sysctl_running: off
debug.fail_point.sysctl_running: off
debug.fail_point.status_test_fail_point: off
debug.fail_point.test_fail_point: off
debug.fail_point.test_trigger_fail_point: off
debug.fail_point.status_nfsc_force_fileid_warning: off
debug.fail_point.nfsc_force_fileid_warning: off
debug.cpubreq.verbose: 0
debug.cpubreq.lowest: 0
debug.bootverbose: 0
debug.boothowto: 0
debug.elf32_legacy_core_dump: 0
debug.elf64_legacy_core_dump: 0
debug.uart_force_poll: 0
debug.uart_poll_freq: 50
debug.mddebug: 0
debug.iwi: 0
debug.ipw: 0
debug.acpi.resume_beeper: 0
debug.acpi.batt.batt_sleep_ms: 0
debug.acpi.ec.timeout: 750
debug.acpi.ec.pollled: 0
debug.acpi.ec.burst: 0
debug.acpi.cpu_unordered: 0
debug.acpi.suspend_bounce: 0
debug.acpi.reset_clock: 1
debug.acpi.default_register_width: 1
debug.acpi.interpreter_slack: 1
debug.acpi.enable_debug_objects: 0
debug.acpi.acpi_ca_version: 20160527
debug.acpi.max_threads: 3
debug.acpi.tasks_hiwater: 8
debug.acpi.max_tasks: 1024
hw.machine: amd64
hw.model: Intel(R) Xeon(R) CPU E3-1230 v5 @ 3.40GHz
hw.ncpu: 8
hw.byteorder: 1234
hw.physmem: 17082064896
hw.usermem: 16852123648
hw.pagesize: 4096
hw.floatingpoint: 1
hw.machine_arch: amd64
hw.realmem: 17179869184
hw.acpi.cpu.cx_lowest: C2
hw.acpi.reset_video: 0
hw.acpi.handle_reboot: 1
hw.acpi.disable_on_reboot: 0
hw.acpi.verbose: 0
hw.acpi.s4bios: 0
hw.acpi.sleep_delay: 1
hw.acpi.suspend_state: NONE
hw.acpi.standby_state: NONE
hw.acpi.lid_switch_state: NONE
hw.acpi.sleep_button_state: S4
hw.acpi.power_button_state: S5
hw.acpi.supported_sleep_state: S4 S5
hw.mca.cmc_throttle: 60
hw.mca.force_scan: 0
hw.mca.interval: 3600
hw.mca.count: 0
hw.mca.erratum383: 0
hw.mca.intel6h_HSD131: 0
hw.mca.amd10h_L1TP: 1
hw.mca.enabled: 1
hw.apic.timer_tsc_deadline: 1
hw.apic.eoi_suppression: 1
hw.apic.x2apic_mode: 0
hw.apic.enable_extint: 0
hw.hv_vendor:
hw.clockrate: 3408
hw.via_feature_xcrypt: 0
hw.via_feature_rng: 0
hw.busdma.zone1.alignment: 4096
hw.busdma.zone1.lowaddr: 0xffffffff
hw.busdma.zone1.total_deferred: 0
hw.busdma.zone1.total_bounced: 0
hw.busdma.zone1.active_bpages: 0
```



```
hw.busdma.zone1.reserved_bpages: 0
hw.busdma.zone1.free_bpages: 231
hw.busdma.zone1.total_bpages: 231
hw.busdma.zone0.alignment: 4096
hw.busdma.zone0.lowaddr: 0xffffffffffffff
hw.busdma.zone0.total_deferred: 0
hw.busdma.zone0.total_bounced: 0
hw.busdma.zone0.active_bpages: 0
hw.busdma.zone0.reserved_bpages: 0
hw.busdma.zone0.free_bpages: 8196
hw.busdma.zone0.total_bpages: 8196
hw.busdma.total_bpages: 8427
hw.dmar.batch_coalesce: 100
hw.dmar.match_verbos: 0
hw.dmar.tbl_pagecnt: 0
hw.nvd.delete_max: 1073741824
hw.hn.tx_swq_depth: 0
hw.hn.tx_ring_cnt: 0
hw.hn.chan_cnt: 0
hw.hn.use_if_start: 0
hw.hn.bind_tx_taskq: -1
hw.hn.use_txdesc_bufiring: 1
hw.hn.share_tx_taskq: 0
hw.hn.lro_entry_count: 128
hw.hn.direct_tx_size: 128
hw.hn.tx_chimney_size: 0
hw.hn.tso_maxlen: 0
hw.hn.trust_hostip: 1
hw.hn.trust_hostudp: 1
hw.hn.trust_hosttcp: 1
hw.ixlv.tx_itr: 122
hw.ixlv.rx_itr: 62
hw.ixlv.dynamic_tx_itr: 0
hw.ixlv.dynamic_rx_itr: 0
hw.ixlv.txbr_size: 4096
hw.ixlv.max_queues: 0
hw.ixlv.ring_size: 1024
hw.ixl.tx_itr: 122
hw.ixl.rx_itr: 62
hw.ixl.dynamic_tx_itr: 1
hw.ixl.dynamic_rx_itr: 1
hw.ixl.shared_debug_mask: 0
hw.ixl.core_debug_mask: 0
hw.ixl.enable_tx_fc_filter: 1
hw.ixl.max_queues: 0
hw.ixl.ring_size: 1024
hw.ixl.enable_msix: 1
hw.bxe.udp_rss: 0
hw.bxe.autogreen: 0
hw.bxe.mrrs: -1
hw.bxe.max_aggregation_size: 0
hw.bxe.rx_budget: -1
hw.bxe.hc_tx_ticks: 50
hw.bxe.hc_rx_ticks: 25
hw.bxe.max_rx_bufs: 0
hw.bxe.queue_count: 4
hw.bxe.interrupt_mode: 2
hw.bxe.debug: 0
hw.psm.tap_timeout: 125000
hw.psm.tap_threshold: 25
hw.psm.tap_enabled: -1
hw.instruction_sse: 1
hw.bus.devctl_queue: 1000
hw.bus.devctl_disable: 0
hw.pagesizes: 4096 2097152 0
hw.availpages: 4170426
hw.intr_storm_threshold: 1000
hw.xbd.xbd_enable_indirect: 1
hw.wi.debug: 0
hw.wi.txerate: 0
hw.watchdog.wd_last_u_secs: 0
hw.watchdog.wd_last_u: 0
hw.vtnet.rx_process_limit: 512
hw.vtnet.mq_max_pairs: 8
hw.vtnet.mq_disable: 0
hw.vtnet.lro_disable: 0
hw.vtnet.tso_disable: 0
hw.vtnet.csum_disable: 0
hw.usb.ums.debug: 0
hw.usb.ukbd.pollrate: 0
hw.usb.ukbd.no_leds: 0
hw.usb.ukbd.debug: 0
hw.usb.full_ddesc: 0
hw.usb.no_cs_fail: 0
hw.usb.proc.debug: 0
hw.usb.disable_port_power: 0
hw.usb.disable_enumeration: 0
hw.usb.power_timeout: 30
hw.usb.uhub.debug: 0
hw.usb.uhub.debug: 0
hw.usb.uhub.debug: 0
hw.usb.usb_lang_mask: 255
hw.usb.usb_lang_id: 9
hw.usb.template: 0
hw.usb.dev.debug: 0
hw.usb.timings.extra_power_up_time: 20
hw.usb.timings.resume_recovery: 50
hw.usb.timings.resume_wait: 50
hw.usb.timings.resume_delay: 250
hw.usb.timings.set_address_settle: 10
hw.usb.timings.port_resume_delay: 40
hw.usb.timings.port_powerup_delay: 300
hw.usb.timings.port_reset_recovery: 250
hw.usb.timings.port_root_reset_delay: 200
hw.usb.timings.port_reset_delay: 50
hw.usb.debug: 0
hw.usb.umass.throttle: 0
hw.usb.umass.debug: 0
hw.usb.no_shutdown_wait: 0
hw.usb.no_suspend_wait: 0
hw.usb.no_boot_wait: 0
hw.usb.ctrl.debug: 0
hw.usb.xhci.dma32: 0
hw.usb.xhci.use_polling: 0
hw.usb.xhci.xhci_port_route: 0
hw.usb.xhci.debug: 0
hw.usb.xhci.streams: 0
hw.usb.uhci.loop: 0
hw.usb.uhci.debug: 0
hw.usb.ohci.debug: 0
hw.usb.ehci.lostintrbug: 0
```

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hw.usb_ehci.iadbug: 0
hw.usb_ehci.no_hs: 0
hw.usb_ehci.debug: 0
hw.broken_txfifo: 0
hw.syscons.sc_no_suspend_vtswitch: 0
hw.syscons.kbd_debug: 1
hw.syscons.kbd_reboot: 1
hw.syscons.bell: 1
hw.syscons.saver.keybonly: 1
hw.midi.seq.debug: 0
hw.midi.instrhoff: 0
hw.midi.dumpraw: 0
hw.midi.debug: 0
hw.midi.stat.verbose: 0
hw.snd.maxautovchans: 16
hw.snd.default_unit: -1
hw.snd.version: 2009061500/amd64
hw.snd.default_auto: -1
hw.snd.verbose: 0
hw.snd.vpc_mixer_bypass: 1
hw.snd.feeder_rate_quality: 1
hw.snd.feeder_rate_round: 25
hw.snd.feeder_rate_max: 2016000
hw.snd.feeder_rate_min: 1
hw.snd.feeder_rate_polyphase_max: 183040
hw.snd.feeder_rate_presets: 100:8:0.85 100:36:0.92 100:164:0.97
hw.snd.feeder_eq_exact_rate: 0
hw.snd.feeder_eq_presets: PEQ:16000,0.2500,62,0.2500:-9,9,1.0:44100,48000,88200,96000,176400,192000
hw.snd.basename_clone: 1
hw.snd.compat_linux_mmap: 0
hw.snd.syncdelay: -1
hw.snd.usefrags: 0
hw.snd.vpc_reset: 0
hw.snd.vpc_0db: 45
hw.snd.vpc_autoreset: 1
hw.snd.timeout: 5
hw.snd.latency_profile: 1
hw.snd.latency: 5
hw.snd.report_soft_matrix: 1
hw.snd.report_soft_formats: 1
hw.sdhci.enable_msi: 1
hw.sdhci.debug: 0
hw.puc.msi_disable: 0
hw.pci.host_mem_start: 2147483648
hw.pci.mcfg: 1
hw.pci.default_vgapci_unit: 0
hw.pci.enable_pcie_hp: 1
hw.pci.clear_pcie: 0
hw.pci.iov_max_config: 1048576
hw.pci.enable_ari: 1
hw.pci.clear_buses: 0
hw.pci.clearBars: 0
hw.pci.usb_early_takeover: 1
hw.pci.honor_msi_blacklist: 1
hw.pci.enable_msix: 1
hw.pci.enable_msi: 1
hw.pci.do_power_suspend: 1
hw.pci.do_power_resume: 1
hw.pci.do_power_nodriver: 0
hw.pci.realloc_bars: 0
hw.pci.enable_io_modes: 1
hw.pciic.pd6722_vsense: 1
hw.pciic.intr_mask: 57016
hw.cbb.debug: 0
hw.cbb.start_32_io: 4096
hw.cbb.start_16_io: 256
hw.cbb.start_memory: 2281701376
hw.pccard.cis_debug: 0
hw.pccard.debug: 0
hw.mwl.rxdma_low: 3
hw.mwl.rxquota: 640
hw.mwl.txcoalesce: 8
hw.mwl.txbuf: 256
hw.mwl.rxbuf: 640
hw.mwl.rxdesc: 256
hw.mmc.debug: 0
hw.mfi.mrsas_enable: 1
hw.mfi.msi: 1
hw.mfi.cmd_timeout: 30
hw.mfi.poll_cmd_timeout: 60
hw.mfi.detect_jbod_change: 1
hw.mfi.max_cmds: 128
hw.mfi.event_class: 0
hw.mfi.event_locale: 65535
hw.malo.pci.msi_disable: 0
hw.malo.txbuf: 256
hw.malo.rxquota: 256
hw.malo.rxbuf: 256
hw.malo.txcoalesce: 8
hw.kbd.keymap_restrict_change: 0
hw.ix.rxd: 2048
hw.ix.txd: 2048
hw.ix.num_queues: 0
hw.ix.enable_msix: 1
hw.ix.advertise_speed: 0
hw.ix.flow_control: 3
hw.ix.tx_process_limit: 256
hw.ix.rx_process_limit: 256
hw.ix.max_interrupt_rate: 31250
hw.ix.enable_aim: 1
hw.igb.tx_process_limit: -1
hw.igb.rx_process_limit: 100
hw.igb.num_queues: 0
hw.igb.header_split: 0
hw.igb.buf_ring_size: 4096
hw.igb.max_interrupt_rate: 8000
hw.igb.enable_msix: 1
hw.igb.enable_aim: 1
hw.igb.txd: 1024
hw.igb.rxd: 1024
hw.em.eee_setting: 1
hw.em.rx_process_limit: 100
hw.em.enable_msix: 1
hw.em.sbp: 0
hw.em.smart_pwr_down: 0
hw.em.txd: 1024
hw.em.rxd: 1024
hw.em.rx_abs_int_delay: 66
hw.em.tx_abs_int_delay: 66
hw.em.rx_int_delay: 0
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hw.em.tx_int_delay: 66
hw.em.disable_crc_stripping: 0
hw.cardbus.cis_debug: 0
hw.cardbus.debug: 0
hw.bge.allow_asf: 1
hw.bce.rx_ticks: 18
hw.bce.rx_ticks_int: 18
hw.bce.rx_quick_cons_trip: 6
hw.bce.rx_quick_cons_trip_int: 6
hw.bce.tx_ticks: 80
hw.bce.tx_ticks_int: 80
hw.bce.tx_quick_cons_trip: 20
hw.bce.tx_quick_cons_trip_int: 20
hw.bce.strict_rx_mtu: 0
hw.bce.hdr_split: 1
hw.bce.tx_pages: 2
hw.bce.rx_pages: 2
hw.bce.msi_enable: 1
hw.bce.tso_enable: 1
hw.bce.verbose: 1
hw.ath.bstuck: 4
hw.ath.txbuf_mgmt: 32
hw.ath.txbuf: 512
hw.ath.rxbuf: 512
hw.ath.anical: 100
hw.ath.resetcal: 1200
hw.ath.shortcal: 100
hw.ath.longcal: 30
hw.ata.ata_dma_check_80pin: 1
hw.an.an_cache_iponly: 1
hw.an.an_cache_mcastonly: 0
hw.an.an_cache_mode: dbm
hw.an.an_dump: off
hw.amr.force_sg32: 0
hw.aac.enable_msi: 1
machdep.tsc_freq: 3408120428
machdep.disable_tsc_calibration: 0
machdep.disable_tsc: 0
machdep.disable_msix_migration: 0
machdep.hyperthreading_allowed: 1
machdep.idle: acpi
machdep.idle_available: spin, mwait, hlt, acpi
machdep.idle_mwait: 1
machdep.i8254_freq: 1193182
machdep.acpi_root: 984496
machdep.uprintf_signal: 0
machdep.prot_fault_translation: 0
machdep.panic_on_nmi: 1
machdep.kdb_on_nmi: 1
machdep.max_ldt_segment: 1024
machdep.nkpt: 50
machdep.smap:
machdep.bootmethod: BIOS
machdep.disable_mtrrs: 0
machdep.disable_rtc_set: 0
machdep.wall_cmos_clock: 1
machdep.adjkerntz: -32400
machdep.rtc_save_period: 1800
machdep.enable_panic_key: 0
machdep.acpi_timer_freq: 3579545
user.cs_path: /usr/bin:/bin:/usr/sbin:/sbin
user.bc_base_max: 99
user.bc_dim_max: 2048
user.bc_scale_max: 99
user.bc_string_max: 1000
user.coll_weights_max: 10
user.expr_nest_max: 32
user.line_max: 2048
user.re_dup_max: 255
user.posix2_version: 199212
user.posix2_c_bind: 0
user.posix2_c_dev: 0
user.posix2_char_term: 0
user.posix2_fort_dev: 0
user.posix2_fort_run: 0
user.posix2_localedef: 0
user.posix2_sw_dev: 0
user.posix2_upe: 0
user.stream_max: 20
user.tzname_max: 255
p1003_1b.asynchronous_io: 200112
p1003_1b.mapped_files: 200112
p1003_1b.memlock: 0
p1003_1b.memlock_range: 0
p1003_1b.memory_protection: 0
p1003_1b.message_passing: 0
p1003_1b.prioritized_io: 0
p1003_1b.priority_scheduling: 200112
p1003_1b.realtime_signals: 200112
p1003_1b.semaphores: 0
p1003_1b.fsync: 200112
p1003_1b.shared_memory_objects: 200112
p1003_1b.synchronized_io: 0
p1003_1b.timers: 200112
p1003_1b.aio_listio_max: 16
p1003_1b.aio_max: 1024
p1003_1b.aio_prio_delta_max: 0
p1003_1b.delaytimer_max: 2147483647
p1003_1b.mq_open_max: 0
p1003_1b.pagesize: 4096
p1003_1b.rtsig_max: 62
p1003_1b.sem_nsems_max: 0
p1003_1b.sem_value_max: 0
p1003_1b.sigqueue_max: 128
p1003_1b.timer_max: 32
hptmv.status: RocketRAID 18xx SATA Controller driver Version v1.16
dev.umass.%parent:
dev.ums.0.parseinfo: i1: Z:r0, p40, s8; B1:r0, p0, s1; B2:r0, p1, s1; B3:r0, p2, s1;
dev.ums.0.%parent: uhub0
dev.ums.0.%pnpinfo: vendor=0x046b product=0xff10 devclass=0x00 devsubclass=0x00 devproto=0x00 sernum="" release=0x0100 mode=host intclass=0x03 intsubclass=0x01 intrprotocol=0x02
dev.ums.0.%location: bus=0 hubaddr=1 port=10 devaddr=4 interface=1 ugen=ugen0.4
dev.ums.0.%driver: ums
dev.ums.0.%desc: Mouse Interface
dev.ums.%parent:
dev.ukbd.1.%parent: uhub0
dev.ukbd.1.%pnpinfo: vendor=0x046b product=0xff10 devclass=0x00 devsubclass=0x00 devproto=0x00 sernum="" release=0x0100 mode=host intclass=0x03 intsubclass=0x01 intrprotocol=0x01
dev.ukbd.1.%location: bus=0 hubaddr=1 port=10 devaddr=4 interface=0 ugen=ugen0.4
dev.ukbd.1.%driver: ukbd
dev.ukbd.1.%desc: Keyboard Interface
dev.ukbd.0.%parent: uhub1

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dev.ukbd.0.%pnpinf: vendor=0x0853 product=0x0100 devclass=0x00 devsubclass=0x00 devproto=0x00 sernum="" release=0x0102 mode=host intclass=0x03 intsubclass=0x01 intrprotocol=0x01
dev.ukbd.0.%location: bus=0 hubaddr=2 port=1 devaddr=3 interface=0 ugen=ugen0.3
dev.ukbd.0.%driver: ukbd
dev.ukbd.0.%desc: Topre Corporation HHKB Professional, class 0/0, rev 1.10/1.02, addr 2
dev.ukbd.%parent:
dev.uhub.1.disable_port_power: 0
dev.uhub.1.disable_enumeration: 0
dev.uhub.1.%parent: uhub0
dev.uhub.1.%pnpinf: vendor=0x0409 product=0x005a devclass=0x09 devsubclass=0x00 devproto=0x01 sernum="" release=0x0100 mode=host intclass=0x09 intsubclass=0x00 intrprotocol=0x00
dev.uhub.1.%location: bus=0 hubaddr=1 port=4 devaddr=2 interface=0 ugen=ugen0.2
dev.uhub.1.%driver: uhub
dev.uhub.1.%desc: vendor 0x0409 product 0x005a, class 9/0, rev 2.00/1.00, addr 1
dev.uhub.0.disable_port_power: 0
dev.uhub.0.disable_enumeration: 0
dev.uhub.0.%parent: usb0
dev.uhub.0.%pnpinf:
dev.uhub.0.%location:
dev.uhub.0.%driver: uhub
dev.uhub.0.%desc: 0x8086 XHCI root HUB, class 9/0, rev 3.00/1.00, addr 1
dev.uhub.%parent:
dev.cpubreq.7.%parent: cpu7
dev.cpubreq.7.%pnpinf:
dev.cpubreq.7.%location:
dev.cpubreq.7.%driver: cpufreq
dev.cpubreq.7.%desc:
dev.cpubreq.6.%parent: cpu6
dev.cpubreq.6.%pnpinf:
dev.cpubreq.6.%location:
dev.cpubreq.6.%driver: cpufreq
dev.cpubreq.6.%desc:
dev.cpubreq.5.%parent: cpu5
dev.cpubreq.5.%pnpinf:
dev.cpubreq.5.%location:
dev.cpubreq.5.%driver: cpufreq
dev.cpubreq.5.%desc:
dev.cpubreq.4.%parent: cpu4
dev.cpubreq.4.%pnpinf:
dev.cpubreq.4.%location:
dev.cpubreq.4.%driver: cpufreq
dev.cpubreq.4.%desc:
dev.cpubreq.3.%parent: cpu3
dev.cpubreq.3.%pnpinf:
dev.cpubreq.3.%location:
dev.cpubreq.3.%driver: cpufreq
dev.cpubreq.3.%desc:
dev.cpubreq.2.%parent: cpu2
dev.cpubreq.2.%pnpinf:
dev.cpubreq.2.%location:
dev.cpubreq.2.%driver: cpufreq
dev.cpubreq.2.%desc:
dev.cpubreq.1.%parent: cpu1
dev.cpubreq.1.%pnpinf:
dev.cpubreq.1.%location:
dev.cpubreq.1.%driver: cpufreq
dev.cpubreq.1.%desc:
dev.cpubreq.0.%parent: cpu0
dev.cpubreq.0.%pnpinf:
dev.cpubreq.0.%location:
dev.cpubreq.0.%driver: cpufreq
dev.cpubreq.0.%desc:
dev.cpubreq.%parent:
dev.est.7.freq_settings: 3401/80000 3400/80000 3200/73495 3000/67226 2800/61202 2700/58269 2500/52572 2300/47109 2100/41853 1900/36813 1700/32645 1500/28007 1400/25765 1200/21437
1000/17302 800/13362
dev.est.7.%parent: cpu7
dev.est.7.%pnpinf:
dev.est.7.%location:
dev.est.7.%driver: est
dev.est.7.%desc: Enhanced SpeedStep Frequency Control
dev.est.6.freq_settings: 3401/80000 3400/80000 3200/73495 3000/67226 2800/61202 2700/58269 2500/52572 2300/47109 2100/41853 1900/36813 1700/32645 1500/28007 1400/25765 1200/21437
1000/17302 800/13362
dev.est.6.%parent: cpu6
dev.est.6.%pnpinf:
dev.est.6.%location:
dev.est.6.%driver: est
dev.est.6.%desc: Enhanced SpeedStep Frequency Control
dev.est.5.freq_settings: 3401/80000 3400/80000 3200/73495 3000/67226 2800/61202 2700/58269 2500/52572 2300/47109 2100/41853 1900/36813 1700/32645 1500/28007 1400/25765 1200/21437
1000/17302 800/13362
dev.est.5.%parent: cpu5
dev.est.5.%pnpinf:
dev.est.5.%location:
dev.est.5.%driver: est
dev.est.5.%desc: Enhanced SpeedStep Frequency Control
dev.est.4.freq_settings: 3401/80000 3400/80000 3200/73495 3000/67226 2800/61202 2700/58269 2500/52572 2300/47109 2100/41853 1900/36813 1700/32645 1500/28007 1400/25765 1200/21437
1000/17302 800/13362
dev.est.4.%parent: cpu4
dev.est.4.%pnpinf:
dev.est.4.%location:
dev.est.4.%driver: est
dev.est.4.%desc: Enhanced SpeedStep Frequency Control
dev.est.3.freq_settings: 3401/80000 3400/80000 3200/73495 3000/67226 2800/61202 2700/58269 2500/52572 2300/47109 2100/41853 1900/36813 1700/32645 1500/28007 1400/25765 1200/21437
1000/17302 800/13362
dev.est.3.%parent: cpu3
dev.est.3.%pnpinf:
dev.est.3.%location:
dev.est.3.%driver: est
dev.est.3.%desc: Enhanced SpeedStep Frequency Control
dev.est.2.freq_settings: 3401/80000 3400/80000 3200/73495 3000/67226 2800/61202 2700/58269 2500/52572 2300/47109 2100/41853 1900/36813 1700/32645 1500/28007 1400/25765 1200/21437
1000/17302 800/13362
dev.est.2.%parent: cpu2
dev.est.2.%pnpinf:
dev.est.2.%location:
dev.est.2.%driver: est
dev.est.2.%desc: Enhanced SpeedStep Frequency Control
dev.est.1.freq_settings: 3401/80000 3400/80000 3200/73495 3000/67226 2800/61202 2700/58269 2500/52572 2300/47109 2100/41853 1900/36813 1700/32645 1500/28007 1400/25765 1200/21437
1000/17302 800/13362
dev.est.1.%parent: cpu1
dev.est.1.%pnpinf:
dev.est.1.%location:
dev.est.1.%driver: est
dev.est.1.%desc: Enhanced SpeedStep Frequency Control
dev.est.0.freq_settings: 3401/80000 3400/80000 3200/73495 3000/67226 2800/61202 2700/58269 2500/52572 2300/47109 2100/41853 1900/36813 1700/32645 1500/28007 1400/25765 1200/21437
1000/17302 800/13362
dev.est.0.%parent: cpu0
dev.est.0.%pnpinf:
dev.est.0.%location:
dev.est.0.%driver: est
dev.est.0.%desc: Enhanced SpeedStep Frequency Control
dev.est.%parent:
dev.acpi_perf.7.%parent: cpu7
dev.acpi_perf.7.%pnpinf:

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dev.acpi_perf.7.%location:
dev.acpi_perf.7.%driver: acpi_perf
dev.acpi_perf.7.%desc:
dev.acpi_perf.6.%parent: cpu6
dev.acpi_perf.6.%pnpinfo:
dev.acpi_perf.6.%location:
dev.acpi_perf.6.%driver: acpi_perf
dev.acpi_perf.6.%desc:
dev.acpi_perf.5.%parent: cpu5
dev.acpi_perf.5.%pnpinfo:
dev.acpi_perf.5.%location:
dev.acpi_perf.5.%driver: acpi_perf
dev.acpi_perf.5.%desc:
dev.acpi_perf.4.%parent: cpu4
dev.acpi_perf.4.%pnpinfo:
dev.acpi_perf.4.%location:
dev.acpi_perf.4.%driver: acpi_perf
dev.acpi_perf.4.%desc:
dev.acpi_perf.3.%parent: cpu3
dev.acpi_perf.3.%pnpinfo:
dev.acpi_perf.3.%location:
dev.acpi_perf.3.%driver: acpi_perf
dev.acpi_perf.3.%desc:
dev.acpi_perf.2.%parent: cpu2
dev.acpi_perf.2.%pnpinfo:
dev.acpi_perf.2.%location:
dev.acpi_perf.2.%driver: acpi_perf
dev.acpi_perf.2.%desc:
dev.acpi_perf.1.%parent: cpu1
dev.acpi_perf.1.%pnpinfo:
dev.acpi_perf.1.%location:
dev.acpi_perf.1.%driver: acpi_perf
dev.acpi_perf.1.%desc:
dev.acpi_perf.0.%parent: cpu0
dev.acpi_perf.0.%pnpinfo:
dev.acpi_perf.0.%location:
dev.acpi_perf.0.%driver: acpi_perf
dev.acpi_perf.0.%desc:
dev.acpi_perf.%parent:
dev.orm.0.%parent: isa0
dev.orm.0.%pnpinfo:
dev.orm.0.%location:
dev.orm.0.%driver: orm
dev.orm.0.%desc: ISA Option ROM
dev.orm.%parent:
dev.apic.0.%parent: nexus0
dev.apic.0.%pnpinfo:
dev.apic.0.%location:
dev.apic.0.%driver: apic
dev.apic.0.%desc: APIC resources
dev.apic.%parent:
dev.fpu_pnp.0.%parent: acpi0
dev.fpu_pnp.0.%pnpinfo: _HID=PNP0C04 _UID=0
dev.fpu_pnp.0.%location: handle=\_SB_.PCI0.LPCB.MATH
dev.fpu_pnp.0.%driver: fpu_pnp
dev.fpu_pnp.0.%desc: Legacy ISA coprocessor support
dev.fpu_pnp.%parent:
dev.uart.1.pps_mode: 2
dev.uart.1.%parent: acpi0
dev.uart.1.%pnpinfo: _HID=PNP0501 _UID=1
dev.uart.1.%location: handle=\_SB_.PCI0.LPCB.UAR2
dev.uart.1.%driver: uart
dev.uart.1.%desc: 16550 or compatible
dev.uart.0.pps_mode: 2
dev.uart.0.%parent: acpi0
dev.uart.0.%pnpinfo: _HID=PNP0501 _UID=0
dev.uart.0.%location: handle=\_SB_.PCI0.LPCB.UAR1
dev.uart.0.%driver: uart
dev.uart.0.%desc: 16550 or compatible
dev.uart.%parent:
dev.psmc_pnp.0.%parent: acpi0
dev.psmc_pnp.0.%pnpinfo: _HID=MSFT0003 _UID=0
dev.psmc_pnp.0.%location: handle=\_SB_.PCI0.LPCB.P52M
dev.psmc_pnp.0.%driver: psmc_pnp
dev.psmc_pnp.0.%desc: PS/2 mouse port
dev.psmc_pnp.%parent:
dev.acpi_button.1.%parent: acpi0
dev.acpi_button.1.%pnpinfo: _HID=PNP0C0C _UID=0
dev.acpi_button.1.%location: handle=\_SB_.PWRB
dev.acpi_button.1.%driver: acpi_button
dev.acpi_button.1.%desc: Power Button
dev.acpi_button.0.%parent: acpi0
dev.acpi_button.0.%pnpinfo: _HID=PNP0C0E _UID=0
dev.acpi_button.0.%location: handle=\_SB_.SLPB
dev.acpi_button.0.%driver: acpi_button
dev.acpi_button.0.%desc: Sleep Button
dev.acpi_button.%parent:
dev.isa.0.%parent: isa0
dev.isa.0.%pnpinfo:
dev.isa.0.%location:
dev.isa.0.%driver: isa
dev.isa.0.%desc: ISA bus
dev.isa.%parent:
dev.isab.0.%parent: pci0
dev.isab.0.%pnpinfo: vendor=0x8086 device=0xa149 subvendor=0x1bcf subdevice=0x8069 class=0x060100
dev.isab.0.%location: slot=31 function=0 dbsf=pci0:0:31:0 handle=\_SB_.PCI0.LPCB
dev.isab.0.%driver: isab
dev.isab.0.%desc: PCI-ISA bridge
dev.isab.%parent:
dev.mrsas.0.wake: 0
dev.mrsas.0.reset_in_progress: 0
dev.mrsas.0.mrsas_fw_fault_check_delay: 1
dev.mrsas.0.mrsas_io_timeout: 180000
dev.mrsas.0.mrsas_debug: 4
dev.mrsas.0.io_cmds_highwater: 37
dev.mrsas.0.fw_outstanding: 0
dev.mrsas.0.reset_count: 0
dev.mrsas.0.driver_version: 06.709.07.00-fbsd
dev.mrsas.0.disable_ocr: 0
dev.mrsas.0.%parent: pci6
dev.mrsas.0.%pnpinfo: vendor=0x1000 device=0x005d subvendor=0x1000 subdevice=0x9364 class=0x010400
dev.mrsas.0.%location: slot=0 function=0 dbsf=pci0:144:0:0 handle=\_SB_.PCI0.RP05.PXSX
dev.mrsas.0.%driver: mrsas
dev.mrsas.0.%desc: AVAGO Invader SAS Controller
dev.mrsas.%parent:
dev.vgapci.0.wake: 0
dev.vgapci.0.%parent: pci3
dev.vgapci.0.%pnpinfo: vendor=0x102b device=0x0522 subvendor=0x1bcf subdevice=0x8069 class=0x030000
dev.vgapci.0.%location: slot=0 function=0 dbsf=pci0:95:0:0 handle=\_SB_.PCI0.RP20.PXSX
dev.vgapci.0.%driver: vgapci

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dev.vgapci.0.%desc: VGA-compatible display
dev.vgapci.%parent:
dev.brgphy.9.%parent: miibus9
dev.brgphy.9.%npninfo: oui=0x1be9 model=0x36 rev=0x0
dev.brgphy.9.%location: phyno=2
dev.brgphy.9.%driver: brgphy
dev.brgphy.9.%desc: BCM5720C 1000BASE-T media interface
dev.brgphy.8.%parent: miibus8
dev.brgphy.8.%npninfo: oui=0x1be9 model=0x36 rev=0x0
dev.brgphy.8.%location: phyno=1
dev.brgphy.8.%driver: brgphy
dev.brgphy.8.%desc: BCM5720C 1000BASE-T media interface
dev.brgphy.7.%parent: miibus7
dev.brgphy.7.%npninfo: oui=0x1be9 model=0x22 rev=0x0
dev.brgphy.7.%location: phyno=4
dev.brgphy.7.%driver: brgphy
dev.brgphy.7.%desc: BCM5719C 1000BASE-T media interface
dev.brgphy.6.%parent: miibus6
dev.brgphy.6.%npninfo: oui=0x1be9 model=0x22 rev=0x0
dev.brgphy.6.%location: phyno=3
dev.brgphy.6.%driver: brgphy
dev.brgphy.6.%desc: BCM5719C 1000BASE-T media interface
dev.brgphy.5.%parent: miibus5
dev.brgphy.5.%npninfo: oui=0x1be9 model=0x22 rev=0x0
dev.brgphy.5.%location: phyno=2
dev.brgphy.5.%driver: brgphy
dev.brgphy.5.%desc: BCM5719C 1000BASE-T media interface
dev.brgphy.4.%parent: miibus4
dev.brgphy.4.%npninfo: oui=0x1be9 model=0x22 rev=0x0
dev.brgphy.4.%location: phyno=1
dev.brgphy.4.%driver: brgphy
dev.brgphy.4.%desc: BCM5719C 1000BASE-T media interface
dev.brgphy.3.%parent: miibus3
dev.brgphy.3.%npninfo: oui=0x1be9 model=0x22 rev=0x0
dev.brgphy.3.%location: phyno=4
dev.brgphy.3.%driver: brgphy
dev.brgphy.3.%desc: BCM5719C 1000BASE-T media interface
dev.brgphy.2.%parent: miibus2
dev.brgphy.2.%npninfo: oui=0x1be9 model=0x22 rev=0x0
dev.brgphy.2.%location: phyno=3
dev.brgphy.2.%driver: brgphy
dev.brgphy.2.%desc: BCM5719C 1000BASE-T media interface
dev.brgphy.1.%parent: miibus1
dev.brgphy.1.%npninfo: oui=0x1be9 model=0x22 rev=0x0
dev.brgphy.1.%location: phyno=2
dev.brgphy.1.%driver: brgphy
dev.brgphy.1.%desc: BCM5719C 1000BASE-T media interface
dev.brgphy.0.%parent: miibus0
dev.brgphy.0.%npninfo: oui=0x1be9 model=0x22 rev=0x0
dev.brgphy.0.%location: phyno=1
dev.brgphy.0.%driver: brgphy
dev.brgphy.0.%desc: BCM5719C 1000BASE-T media interface
dev.brgphy.%parent:
dev.miibus.9.%parent: bge9
dev.miibus.9.%npninfo:
dev.miibus.9.%location:
dev.miibus.9.%driver: miibus
dev.miibus.9.%desc: MII bus
dev.miibus.8.%parent: bge8
dev.miibus.8.%npninfo:
dev.miibus.8.%location:
dev.miibus.8.%driver: miibus
dev.miibus.8.%desc: MII bus
dev.miibus.7.%parent: bge7
dev.miibus.7.%npninfo:
dev.miibus.7.%location:
dev.miibus.7.%driver: miibus
dev.miibus.7.%desc: MII bus
dev.miibus.6.%parent: bge6
dev.miibus.6.%npninfo:
dev.miibus.6.%location:
dev.miibus.6.%driver: miibus
dev.miibus.6.%desc: MII bus
dev.miibus.5.%parent: bge5
dev.miibus.5.%npninfo:
dev.miibus.5.%location:
dev.miibus.5.%driver: miibus
dev.miibus.5.%desc: MII bus
dev.miibus.4.%parent: bge4
dev.miibus.4.%npninfo:
dev.miibus.4.%location:
dev.miibus.4.%driver: miibus
dev.miibus.4.%desc: MII bus
dev.miibus.3.%parent: bge3
dev.miibus.3.%npninfo:
dev.miibus.3.%location:
dev.miibus.3.%driver: miibus
dev.miibus.3.%desc: MII bus
dev.miibus.2.%parent: bge2
dev.miibus.2.%npninfo:
dev.miibus.2.%location:
dev.miibus.2.%driver: miibus
dev.miibus.2.%desc: MII bus
dev.miibus.1.%parent: bge1
dev.miibus.1.%npninfo:
dev.miibus.1.%location:
dev.miibus.1.%driver: miibus
dev.miibus.1.%desc: MII bus
dev.miibus.0.%parent: bge0
dev.miibus.0.%npninfo:
dev.miibus.0.%location:
dev.miibus.0.%driver: miibus
dev.miibus.0.%desc: MII bus
dev.miibus.%parent:
dev.bge.9.stats.tx.BroadcastPkts: 0
dev.bge.9.stats.tx.MulticastPkts: 0
dev.bge.9.stats.tx.UnicastPkts: 0
dev.bge.9.stats.tx.LateCollisions: 0
dev.bge.9.stats.tx.ExcessiveCollisions: 0
dev.bge.9.stats.tx.DeferredTransmissions: 0
dev.bge.9.stats.tx.MultipleCollisionFrames: 0
dev.bge.9.stats.tx.SingleCollisionFrames: 0
dev.bge.9.stats.tx.InternalMacTransmitErrors: 0
dev.bge.9.stats.tx.XoffSent: 0
dev.bge.9.stats.tx.XonSent: 0
dev.bge.9.stats.tx.Collisions: 0
dev.bge.9.stats.tx.ifHCOutOctets: 0
dev.bge.9.stats.rx.UndersizePkts: 0
dev.bge.9.stats.rx.Jabbers: 0
dev.bge.9.stats.rx.FramesTooLong: 0

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dev.bge.9.stats.rx.xoffStateEntered: 0
dev.bge.9.stats.rx.ControlFramesReceived: 0
dev.bge.9.stats.rx.xoffPauseFramesReceived: 0
dev.bge.9.stats.rx.xonPauseFramesReceived: 0
dev.bge.9.stats.rx.AlignmentErrors: 0
dev.bge.9.stats.rx.FCSErrors: 0
dev.bge.9.stats.rx.BroadcastPkts: 0
dev.bge.9.stats.rx.MulticastPkts: 0
dev.bge.9.stats.rx.UnicastPkts: 0
dev.bge.9.stats.rx.Fragments: 0
dev.bge.9.stats.rx.ifHCInOctets: 0
dev.bge.9.stats.RecvThresholdHit: 0
dev.bge.9.stats.InputErrors: 0
dev.bge.9.stats.InputDiscards: 0
dev.bge.9.stats.NoMoreRxBDs: 0
dev.bge.9.stats.DmaWriteHighPriQueueFull: 0
dev.bge.9.stats.DmaWriteQueueFull: 0
dev.bge.9.stats.FramesDroppedDueToFilters: 0
dev.bge.9.forced_udpcsum: 0
dev.bge.9.msi: 1
dev.bge.9.forced_collapse: 0
dev.bge.9.%parent: pci5
dev.bge.9.%pnpinf: vendor=0x14e4 device=0x165f subvendor=0x1bcf subdevice=0x8069 class=0x020000
dev.bge.9.%location: slot=0 function=1 dbsf=pci0:143:0:1 handle=_SB_.PCI0.RP04.LAN2
dev.bge.9.%driver: bge
dev.bge.9.%desc: Broadcom NetXtreme Gigabit Ethernet Controller, ASIC rev. 0x5720000
dev.bge.8.wake: 0
dev.bge.8.stats.tx.BroadcastPkts: 4
dev.bge.8.stats.tx.MulticastPkts: 0
dev.bge.8.stats.tx.UnicastPkts: 13
dev.bge.8.stats.tx.LateCollisions: 0
dev.bge.8.stats.tx.ExcessiveCollisions: 0
dev.bge.8.stats.tx.DeferredTransmissions: 0
dev.bge.8.stats.tx.MultipleCollisionFrames: 0
dev.bge.8.stats.tx.SingleCollisionFrames: 0
dev.bge.8.stats.tx.InternalMacTransmitErrors: 0
dev.bge.8.stats.tx.XoffSent: 0
dev.bge.8.stats.tx.XonSent: 0
dev.bge.8.stats.tx.Collisions: 0
dev.bge.8.stats.tx.ifHCOutOctets: 2095
dev.bge.8.stats.rx.UndersizePkts: 0
dev.bge.8.stats.rx.Jabbers: 0
dev.bge.8.stats.rx.FramesTooLong: 0
dev.bge.8.stats.rx.xoffStateEntered: 0
dev.bge.8.stats.rx.ControlFramesReceived: 0
dev.bge.8.stats.rx.xoffPauseFramesReceived: 0
dev.bge.8.stats.rx.xonPauseFramesReceived: 0
dev.bge.8.stats.rx.AlignmentErrors: 0
dev.bge.8.stats.rx.FCSErrors: 0
dev.bge.8.stats.rx.BroadcastPkts: 3786
dev.bge.8.stats.rx.MulticastPkts: 4678
dev.bge.8.stats.rx.UnicastPkts: 16
dev.bge.8.stats.rx.Fragments: 0
dev.bge.8.stats.rx.ifHCInOctets: 1417766
dev.bge.8.stats.RecvThresholdHit: 0
dev.bge.8.stats.InputErrors: 0
dev.bge.8.stats.InputDiscards: 0
dev.bge.8.stats.NoMoreRxBDs: 0
dev.bge.8.stats.DmaWriteHighPriQueueFull: 0
dev.bge.8.stats.DmaWriteQueueFull: 0
dev.bge.8.stats.FramesDroppedDueToFilters: 0
dev.bge.8.forced_udpcsum: 0
dev.bge.8.msi: 1
dev.bge.8.forced_collapse: 0
dev.bge.8.%parent: pci5
dev.bge.8.%pnpinf: vendor=0x14e4 device=0x165f subvendor=0x1bcf subdevice=0x8069 class=0x020000
dev.bge.8.%location: slot=0 function=0 dbsf=pci0:143:0:0 handle=_SB_.PCI0.RP04.PXSX
dev.bge.8.%driver: bge
dev.bge.8.%desc: Broadcom NetXtreme Gigabit Ethernet Controller, ASIC rev. 0x5720000
dev.bge.7.stats.tx.BroadcastPkts: 0
dev.bge.7.stats.tx.MulticastPkts: 0
dev.bge.7.stats.tx.UnicastPkts: 0
dev.bge.7.stats.tx.LateCollisions: 0
dev.bge.7.stats.tx.ExcessiveCollisions: 0
dev.bge.7.stats.tx.DeferredTransmissions: 0
dev.bge.7.stats.tx.MultipleCollisionFrames: 0
dev.bge.7.stats.tx.SingleCollisionFrames: 0
dev.bge.7.stats.tx.InternalMacTransmitErrors: 0
dev.bge.7.stats.tx.XoffSent: 0
dev.bge.7.stats.tx.XonSent: 0
dev.bge.7.stats.tx.Collisions: 0
dev.bge.7.stats.tx.ifHCOutOctets: 0
dev.bge.7.stats.rx.UndersizePkts: 0
dev.bge.7.stats.rx.Jabbers: 0
dev.bge.7.stats.rx.FramesTooLong: 0
dev.bge.7.stats.rx.xoffStateEntered: 0
dev.bge.7.stats.rx.ControlFramesReceived: 0
dev.bge.7.stats.rx.xoffPauseFramesReceived: 0
dev.bge.7.stats.rx.xonPauseFramesReceived: 0
dev.bge.7.stats.rx.AlignmentErrors: 0
dev.bge.7.stats.rx.FCSErrors: 0
dev.bge.7.stats.rx.BroadcastPkts: 0
dev.bge.7.stats.rx.MulticastPkts: 0
dev.bge.7.stats.rx.UnicastPkts: 0
dev.bge.7.stats.rx.Fragments: 0
dev.bge.7.stats.rx.ifHCInOctets: 0
dev.bge.7.stats.RecvThresholdHit: 0
dev.bge.7.stats.InputErrors: 0
dev.bge.7.stats.InputDiscards: 0
dev.bge.7.stats.NoMoreRxBDs: 0
dev.bge.7.stats.DmaWriteHighPriQueueFull: 0
dev.bge.7.stats.DmaWriteQueueFull: 0
dev.bge.7.stats.FramesDroppedDueToFilters: 0
dev.bge.7.forced_udpcsum: 0
dev.bge.7.msi: 1
dev.bge.7.forced_collapse: 0
dev.bge.7.%parent: pci4
dev.bge.7.%pnpinf: vendor=0x14e4 device=0x1657 subvendor=0x1bcf subdevice=0x8064 class=0x020000
dev.bge.7.%location: slot=0 function=3 dbsf=pci0:96:0:3
dev.bge.7.%driver: bge
dev.bge.7.%desc: 1000BASE-T Adapter(4ch), ASIC rev. 0x5719001
dev.bge.6.stats.tx.BroadcastPkts: 0
dev.bge.6.stats.tx.MulticastPkts: 0
dev.bge.6.stats.tx.UnicastPkts: 0
dev.bge.6.stats.tx.LateCollisions: 0
dev.bge.6.stats.tx.ExcessiveCollisions: 0
dev.bge.6.stats.tx.DeferredTransmissions: 0
dev.bge.6.stats.tx.MultipleCollisionFrames: 0
dev.bge.6.stats.tx.SingleCollisionFrames: 0
dev.bge.6.stats.tx.InternalMacTransmitErrors: 0

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dev.bge.6.stats.tx.XoffSent: 0
dev.bge.6.stats.tx.XonSent: 0
dev.bge.6.stats.tx.Collisions: 0
dev.bge.6.stats.tx.ifHCOutOctets: 0
dev.bge.6.stats.rx.UndersizePkts: 0
dev.bge.6.stats.rx.Jabbers: 0
dev.bge.6.stats.rx.FramesTooLong: 0
dev.bge.6.stats.rx.xoffStateEntered: 0
dev.bge.6.stats.rx.ControlFramesReceived: 0
dev.bge.6.stats.rx.xoffPauseFramesReceived: 0
dev.bge.6.stats.rx.xonPauseFramesReceived: 0
dev.bge.6.stats.rx.AlignmentErrors: 0
dev.bge.6.stats.rx.FCSErrors: 0
dev.bge.6.stats.rx.BroadcastPkts: 0
dev.bge.6.stats.rx.MulticastPkts: 0
dev.bge.6.stats.rx.UnicastPkts: 0
dev.bge.6.stats.rx.Fragments: 0
dev.bge.6.stats.rx.ifHCInOctets: 0
dev.bge.6.stats.RecvThresholdHit: 0
dev.bge.6.stats.InputErrors: 0
dev.bge.6.stats.InputDiscards: 0
dev.bge.6.stats.NoMoreRxBDs: 0
dev.bge.6.stats.DmaWriteHighPriQueueFull: 0
dev.bge.6.stats.DmaWriteQueueFull: 0
dev.bge.6.stats.FramesDroppedDueToFilters: 0
dev.bge.6.forced_udpcsum: 0
dev.bge.6.msi: 1
dev.bge.6.forced_collapse: 0
dev.bge.6.%parent: pci4
dev.bge.6.%pnpinfo: vendor=0x14e4 device=0x1657 subvendor=0x1bcf subdevice=0x8064 class=0x020000
dev.bge.6.%location: slot=0 function=2 dbsf=pci0:96:0:2
dev.bge.6.%driver: bge
dev.bge.6.%desc: 1000BASE-T_Adapter(4ch), ASIC rev. 0x5719001
dev.bge.5.stats.tx.BroadcastPkts: 0
dev.bge.5.stats.tx.MulticastPkts: 0
dev.bge.5.stats.tx.UnicastPkts: 0
dev.bge.5.stats.tx.LateCollisions: 0
dev.bge.5.stats.tx.ExcessiveCollisions: 0
dev.bge.5.stats.tx.DeferredTransmissions: 0
dev.bge.5.stats.tx.MultipleCollisionFrames: 0
dev.bge.5.stats.tx.SingleCollisionFrames: 0
dev.bge.5.stats.tx.InternalMacTransmitErrors: 0
dev.bge.5.stats.tx.XoffSent: 0
dev.bge.5.stats.tx.XonSent: 0
dev.bge.5.stats.tx.Collisions: 0
dev.bge.5.stats.tx.ifHCOutOctets: 0
dev.bge.5.stats.rx.UndersizePkts: 0
dev.bge.5.stats.rx.Jabbers: 0
dev.bge.5.stats.rx.FramesTooLong: 0
dev.bge.5.stats.rx.xoffStateEntered: 0
dev.bge.5.stats.rx.ControlFramesReceived: 0
dev.bge.5.stats.rx.xoffPauseFramesReceived: 0
dev.bge.5.stats.rx.xonPauseFramesReceived: 0
dev.bge.5.stats.rx.AlignmentErrors: 0
dev.bge.5.stats.rx.FCSErrors: 0
dev.bge.5.stats.rx.BroadcastPkts: 0
dev.bge.5.stats.rx.MulticastPkts: 0
dev.bge.5.stats.rx.UnicastPkts: 0
dev.bge.5.stats.rx.Fragments: 0
dev.bge.5.stats.rx.ifHCInOctets: 0
dev.bge.5.stats.RecvThresholdHit: 0
dev.bge.5.stats.InputErrors: 0
dev.bge.5.stats.InputDiscards: 0
dev.bge.5.stats.NoMoreRxBDs: 0
dev.bge.5.stats.DmaWriteHighPriQueueFull: 0
dev.bge.5.stats.DmaWriteQueueFull: 0
dev.bge.5.stats.FramesDroppedDueToFilters: 0
dev.bge.5.forced_udpcsum: 0
dev.bge.5.msi: 1
dev.bge.5.forced_collapse: 0
dev.bge.5.%parent: pci4
dev.bge.5.%pnpinfo: vendor=0x14e4 device=0x1657 subvendor=0x1bcf subdevice=0x8064 class=0x020000
dev.bge.5.%location: slot=0 function=1 dbsf=pci0:96:0:1
dev.bge.5.%driver: bge
dev.bge.5.%desc: 1000BASE-T_Adapter(4ch), ASIC rev. 0x5719001
dev.bge.4.wake: 0
dev.bge.4.stats.tx.BroadcastPkts: 0
dev.bge.4.stats.tx.MulticastPkts: 0
dev.bge.4.stats.tx.UnicastPkts: 0
dev.bge.4.stats.tx.LateCollisions: 0
dev.bge.4.stats.tx.ExcessiveCollisions: 0
dev.bge.4.stats.tx.DeferredTransmissions: 0
dev.bge.4.stats.tx.MultipleCollisionFrames: 0
dev.bge.4.stats.tx.SingleCollisionFrames: 0
dev.bge.4.stats.tx.InternalMacTransmitErrors: 0
dev.bge.4.stats.tx.XoffSent: 0
dev.bge.4.stats.tx.XonSent: 0
dev.bge.4.stats.tx.Collisions: 0
dev.bge.4.stats.tx.ifHCOutOctets: 0
dev.bge.4.stats.rx.UndersizePkts: 0
dev.bge.4.stats.rx.Jabbers: 0
dev.bge.4.stats.rx.FramesTooLong: 0
dev.bge.4.stats.rx.xoffStateEntered: 0
dev.bge.4.stats.rx.ControlFramesReceived: 0
dev.bge.4.stats.rx.xoffPauseFramesReceived: 0
dev.bge.4.stats.rx.xonPauseFramesReceived: 0
dev.bge.4.stats.rx.AlignmentErrors: 0
dev.bge.4.stats.rx.FCSErrors: 0
dev.bge.4.stats.rx.BroadcastPkts: 0
dev.bge.4.stats.rx.MulticastPkts: 0
dev.bge.4.stats.rx.UnicastPkts: 0
dev.bge.4.stats.rx.Fragments: 0
dev.bge.4.stats.rx.ifHCInOctets: 0
dev.bge.4.stats.RecvThresholdHit: 0
dev.bge.4.stats.InputErrors: 0
dev.bge.4.stats.InputDiscards: 0
dev.bge.4.stats.NoMoreRxBDs: 0
dev.bge.4.stats.DmaWriteHighPriQueueFull: 0
dev.bge.4.stats.DmaWriteQueueFull: 0
dev.bge.4.stats.FramesDroppedDueToFilters: 0
dev.bge.4.forced_udpcsum: 0
dev.bge.4.msi: 1
dev.bge.4.forced_collapse: 0
dev.bge.4.%parent: pci4
dev.bge.4.%pnpinfo: vendor=0x14e4 device=0x1657 subvendor=0x1bcf subdevice=0x8064 class=0x020000
dev.bge.4.%location: slot=0 function=0 dbsf=pci0:96:0:0 handle=_SB_.PCI0.RP01.PXSX
dev.bge.4.%driver: bge
dev.bge.4.%desc: 1000BASE-T_Adapter(4ch), ASIC rev. 0x5719001
dev.bge.3.stats.tx.BroadcastPkts: 0
dev.bge.3.stats.tx.MulticastPkts: 0

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dev.bge.3.stats.tx.UnicastPkts: 0
dev.bge.3.stats.tx.LateCollisions: 0
dev.bge.3.stats.tx.ExcessiveCollisions: 0
dev.bge.3.stats.tx.DeferredTransmissions: 0
dev.bge.3.stats.tx.MultipleCollisionFrames: 0
dev.bge.3.stats.tx.SingleCollisionFrames: 0
dev.bge.3.stats.tx.InternalMacTransmitErrors: 0
dev.bge.3.stats.tx.XoffSent: 0
dev.bge.3.stats.tx.XonSent: 0
dev.bge.3.stats.tx.Collisions: 0
dev.bge.3.stats.tx.ifHCOutOctets: 0
dev.bge.3.stats.rx.UndersizePkts: 0
dev.bge.3.stats.rx.Jabbers: 0
dev.bge.3.stats.rx.FramesTooLong: 0
dev.bge.3.stats.rx.xoffStateEntered: 0
dev.bge.3.stats.rx.ControlFramesReceived: 0
dev.bge.3.stats.rx.xoffPauseFramesReceived: 0
dev.bge.3.stats.rx.xonPauseFramesReceived: 0
dev.bge.3.stats.rx.AlignmentErrors: 0
dev.bge.3.stats.rx.FCSErrors: 0
dev.bge.3.stats.rx.BroadcastPkts: 0
dev.bge.3.stats.rx.MulticastPkts: 0
dev.bge.3.stats.rx.UnicastPkts: 0
dev.bge.3.stats.rx.Fragments: 0
dev.bge.3.stats.rx.ifHCInOctets: 0
dev.bge.3.stats.RecvThresholdHit: 0
dev.bge.3.stats.InputErrors: 0
dev.bge.3.stats.InputDiscards: 0
dev.bge.3.stats.NoMoreRxBDs: 0
dev.bge.3.stats.DmaWriteHighPriQueueFull: 0
dev.bge.3.stats.DmaWriteQueueFull: 0
dev.bge.3.stats.FramesDroppedDueToFilters: 0
dev.bge.3.forced_udpcsum: 0
dev.bge.3.msi: 1
dev.bge.3.forced_collapse: 0
dev.bge.3.%parent: pci2
dev.bge.3.%npinfo: vendor=0x14e4 device=0x1657 subvendor=0x1bcf subdevice=0x8064 class=0x020000
dev.bge.3.%location: slot=0 function=3 dbsf=pci0:48:0:3
dev.bge.3.%driver: bge
dev.bge.3.%desc: 1000BASE-T Adapter(4ch), ASIC rev. 0x5719001
dev.bge.2.stats.tx.BroadcastPkts: 0
dev.bge.2.stats.tx.MulticastPkts: 0
dev.bge.2.stats.tx.UnicastPkts: 0
dev.bge.2.stats.tx.LateCollisions: 0
dev.bge.2.stats.tx.ExcessiveCollisions: 0
dev.bge.2.stats.tx.DeferredTransmissions: 0
dev.bge.2.stats.tx.MultipleCollisionFrames: 0
dev.bge.2.stats.tx.SingleCollisionFrames: 0
dev.bge.2.stats.tx.InternalMacTransmitErrors: 0
dev.bge.2.stats.tx.XoffSent: 0
dev.bge.2.stats.tx.XonSent: 0
dev.bge.2.stats.tx.Collisions: 0
dev.bge.2.stats.tx.ifHCOutOctets: 0
dev.bge.2.stats.rx.UndersizePkts: 0
dev.bge.2.stats.rx.Jabbers: 0
dev.bge.2.stats.rx.FramesTooLong: 0
dev.bge.2.stats.rx.xoffStateEntered: 0
dev.bge.2.stats.rx.ControlFramesReceived: 0
dev.bge.2.stats.rx.xoffPauseFramesReceived: 0
dev.bge.2.stats.rx.xonPauseFramesReceived: 0
dev.bge.2.stats.rx.AlignmentErrors: 0
dev.bge.2.stats.rx.FCSErrors: 0
dev.bge.2.stats.rx.BroadcastPkts: 0
dev.bge.2.stats.rx.MulticastPkts: 0
dev.bge.2.stats.rx.UnicastPkts: 0
dev.bge.2.stats.rx.Fragments: 0
dev.bge.2.stats.rx.ifHCInOctets: 0
dev.bge.2.stats.RecvThresholdHit: 0
dev.bge.2.stats.InputErrors: 0
dev.bge.2.stats.InputDiscards: 0
dev.bge.2.stats.NoMoreRxBDs: 0
dev.bge.2.stats.DmaWriteHighPriQueueFull: 0
dev.bge.2.stats.DmaWriteQueueFull: 0
dev.bge.2.stats.FramesDroppedDueToFilters: 0
dev.bge.2.forced_udpcsum: 0
dev.bge.2.msi: 1
dev.bge.2.forced_collapse: 0
dev.bge.2.%parent: pci2
dev.bge.2.%npinfo: vendor=0x14e4 device=0x1657 subvendor=0x1bcf subdevice=0x8064 class=0x020000
dev.bge.2.%location: slot=0 function=2 dbsf=pci0:48:0:2
dev.bge.2.%driver: bge
dev.bge.2.%desc: 1000BASE-T Adapter(4ch), ASIC rev. 0x5719001
dev.bge.1.stats.tx.BroadcastPkts: 0
dev.bge.1.stats.tx.MulticastPkts: 0
dev.bge.1.stats.tx.UnicastPkts: 0
dev.bge.1.stats.tx.LateCollisions: 0
dev.bge.1.stats.tx.ExcessiveCollisions: 0
dev.bge.1.stats.tx.DeferredTransmissions: 0
dev.bge.1.stats.tx.MultipleCollisionFrames: 0
dev.bge.1.stats.tx.SingleCollisionFrames: 0
dev.bge.1.stats.tx.InternalMacTransmitErrors: 0
dev.bge.1.stats.tx.XoffSent: 0
dev.bge.1.stats.tx.XonSent: 0
dev.bge.1.stats.tx.Collisions: 0
dev.bge.1.stats.tx.ifHCOutOctets: 0
dev.bge.1.stats.rx.UndersizePkts: 0
dev.bge.1.stats.rx.Jabbers: 0
dev.bge.1.stats.rx.FramesTooLong: 0
dev.bge.1.stats.rx.xoffStateEntered: 0
dev.bge.1.stats.rx.ControlFramesReceived: 0
dev.bge.1.stats.rx.xoffPauseFramesReceived: 0
dev.bge.1.stats.rx.xonPauseFramesReceived: 0
dev.bge.1.stats.rx.AlignmentErrors: 0
dev.bge.1.stats.rx.FCSErrors: 0
dev.bge.1.stats.rx.BroadcastPkts: 0
dev.bge.1.stats.rx.MulticastPkts: 0
dev.bge.1.stats.rx.UnicastPkts: 0
dev.bge.1.stats.rx.Fragments: 0
dev.bge.1.stats.rx.ifHCInOctets: 0
dev.bge.1.stats.RecvThresholdHit: 0
dev.bge.1.stats.InputErrors: 0
dev.bge.1.stats.InputDiscards: 0
dev.bge.1.stats.NoMoreRxBDs: 0
dev.bge.1.stats.DmaWriteHighPriQueueFull: 0
dev.bge.1.stats.DmaWriteQueueFull: 0
dev.bge.1.stats.FramesDroppedDueToFilters: 0
dev.bge.1.forced_udpcsum: 0
dev.bge.1.msi: 1
dev.bge.1.forced_collapse: 0
dev.bge.1.%parent: pci2

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dev.bge.1.%pnpinf: vendor=0x14e4 device=0x1657 subvendor=0x1bcf subdevice=0x8064 class=0x020000
dev.bge.1.%location: slot=0 function=1 dbsf=pci0:48:0:1
dev.bge.1.%driver: bge
dev.bge.1.%desc: 1000BASE-T_Adapter(4ch), ASIC rev. 0x5719001
dev.bge.0.wake: 0
dev.bge.0.stats.tx.BroadcastPkts: 0
dev.bge.0.stats.tx.MulticastPkts: 0
dev.bge.0.stats.tx.UnicastPkts: 0
dev.bge.0.stats.tx.LateCollisions: 0
dev.bge.0.stats.tx.ExcessiveCollisions: 0
dev.bge.0.stats.tx.DeferredTransmissions: 0
dev.bge.0.stats.tx.MultipleCollisionFrames: 0
dev.bge.0.stats.tx.SingleCollisionFrames: 0
dev.bge.0.stats.tx.InternalMacTransmitErrors: 0
dev.bge.0.stats.tx.XoffSent: 0
dev.bge.0.stats.tx.XonSent: 0
dev.bge.0.stats.tx.Collisions: 0
dev.bge.0.stats.tx.ifHCOutOctets: 0
dev.bge.0.stats.rx.UndersizePkts: 0
dev.bge.0.stats.rx.Jabbers: 0
dev.bge.0.stats.rx.FramesTooLong: 0
dev.bge.0.stats.rx.xoffStateEntered: 0
dev.bge.0.stats.rx.ControlFramesReceived: 0
dev.bge.0.stats.rx.xoffPauseFramesReceived: 0
dev.bge.0.stats.rx.xonPauseFramesReceived: 0
dev.bge.0.stats.rx.AlignmentErrors: 0
dev.bge.0.stats.rx.FCSErrors: 0
dev.bge.0.stats.rx.BroadcastPkts: 0
dev.bge.0.stats.rx.MulticastPkts: 0
dev.bge.0.stats.rx.UnicastPkts: 0
dev.bge.0.stats.rx.Fragments: 0
dev.bge.0.stats.rx.ifHCInOctets: 0
dev.bge.0.stats.RecvThresholdHit: 0
dev.bge.0.stats.InputErrors: 0
dev.bge.0.stats.InputDiscards: 0
dev.bge.0.stats.NoMoreRxBDs: 0
dev.bge.0.stats.DmaWriteHighPriQueueFull: 0
dev.bge.0.stats.DmaWriteQueueFull: 0
dev.bge.0.stats.FramesDroppedDueToFilters: 0
dev.bge.0.forced_udpcsum: 0
dev.bge.0.msi: 1
dev.bge.0.forced_collapse: 0
dev.bge.0.%parent: pci2
dev.bge.0.%pnpinf: vendor=0x14e4 device=0x1657 subvendor=0x1bcf subdevice=0x8064 class=0x020000
dev.bge.0.%location: slot=0 function=0 dbsf=pci0:48:0:0 handle=\_SB_.PCI0.RP18.PXSX
dev.bge.0.%driver: bge
dev.bge.0.%desc: 1000BASE-T_Adapter(4ch), ASIC rev. 0x5719001
dev.bge.%parent:
dev.ahciem.0.%parent: ahci0
dev.ahciem.0.%pnpinf:
dev.ahciem.0.%location:
dev.ahciem.0.%driver: ahciem
dev.ahciem.0.%desc: AHCI enclosure management bridge
dev.ahciem.%parent:
dev.ahcich.4.%parent: ahci0
dev.ahcich.4.%pnpinf:
dev.ahcich.4.%location: channel=4
dev.ahcich.4.%driver: ahcich
dev.ahcich.4.%desc: AHCI channel
dev.ahcich.3.%parent: ahci0
dev.ahcich.3.%pnpinf:
dev.ahcich.3.%location: channel=3
dev.ahcich.3.%driver: ahcich
dev.ahcich.3.%desc: AHCI channel
dev.ahcich.2.%parent: ahci0
dev.ahcich.2.%pnpinf:
dev.ahcich.2.%location: channel=2
dev.ahcich.2.%driver: ahcich
dev.ahcich.2.%desc: AHCI channel
dev.ahcich.1.%parent: ahci0
dev.ahcich.1.%pnpinf:
dev.ahcich.1.%location: channel=1
dev.ahcich.1.%driver: ahcich
dev.ahcich.1.%desc: AHCI channel
dev.ahcich.0.%parent: ahci0
dev.ahcich.0.%pnpinf:
dev.ahcich.0.%location: channel=0
dev.ahcich.0.%driver: ahcich
dev.ahcich.0.%desc: AHCI channel
dev.ahcich.%parent:
dev.ahci.0.%parent: pci0
dev.ahci.0.%pnpinf: vendor=0x8086 device=0xa102 subvendor=0x1bcf subdevice=0x8069 class=0x010601
dev.ahci.0.%location: slot=23 function=0 dbsf=pci0:0:23:0 handle=\_SB_.PCI0.SAT0
dev.ahci.0.%driver: ahci
dev.ahci.0.%desc: Intel Sunrise Point AHCI SATA controller
dev.ahci.%parent:
dev.usbus.0.%parent: xhci0
dev.usbus.0.%pnpinf:
dev.usbus.0.%location:
dev.usbus.0.%driver: usbuhci
dev.usbus.0.%desc:
dev.usbus.%parent:
dev.xhci.0.wake: 0
dev.xhci.0.%parent: pci0
dev.xhci.0.%pnpinf: vendor=0x8086 device=0xa12f subvendor=0x1bcf subdevice=0x8069 class=0x0c0330
dev.xhci.0.%location: slot=20 function=0 dbsf=pci0:0:20:0 handle=\_SB_.PCI0.XHC_
dev.xhci.0.%driver: xhci
dev.xhci.0.%desc: Intel Sunrise Point USB 3.0 controller
dev.xhci.%parent:
dev.hostb.0.%parent: pci0
dev.hostb.0.%pnpinf: vendor=0x8086 device=0x1918 subvendor=0x1bcf subdevice=0x8069 class=0x060000
dev.hostb.0.%location: slot=0 function=0 dbsf=pci0:0:0:0 handle=\_SB_.PCI0.DSC0
dev.hostb.0.%driver: hostb
dev.hostb.0.%desc: Host to PCI bridge
dev.hostb.%parent:
dev.pci.7.wake: 0
dev.pci.7.%parent: pci7
dev.pci.7.%pnpinf:
dev.pci.7.%location:
dev.pci.7.%driver: pci
dev.pci.7.%desc: ACPI PCI bus
dev.pci.6.wake: 0
dev.pci.6.%parent: pci6
dev.pci.6.%pnpinf:
dev.pci.6.%location:
dev.pci.6.%driver: pci
dev.pci.6.%desc: ACPI PCI bus
dev.pci.5.wake: 0
dev.pci.5.%parent: pci5
dev.pci.5.%pnpinf:

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dev.pci.5.%location:
dev.pci.5.%driver: pci
dev.pci.5.%desc: ACPI PCI bus
dev.pci.4.wake: 0
dev.pci.4.%parent: pcib4
dev.pci.4.%pnpinfo:
dev.pci.4.%location:
dev.pci.4.%driver: pci
dev.pci.4.%desc: ACPI PCI bus
dev.pci.3.wake: 0
dev.pci.3.%parent: pcib3
dev.pci.3.%pnpinfo:
dev.pci.3.%location:
dev.pci.3.%driver: pci
dev.pci.3.%desc: ACPI PCI bus
dev.pci.2.wake: 0
dev.pci.2.%parent: pcib2
dev.pci.2.%pnpinfo:
dev.pci.2.%location:
dev.pci.2.%driver: pci
dev.pci.2.%desc: ACPI PCI bus
dev.pci.1.wake: 0
dev.pci.1.%parent: pcib1
dev.pci.1.%pnpinfo:
dev.pci.1.%location:
dev.pci.1.%driver: pci
dev.pci.1.%desc: ACPI PCI bus
dev.pci.0.%parent: pcib0
dev.pci.0.%pnpinfo:
dev.pci.0.%location:
dev.pci.0.%driver: pci
dev.pci.0.%desc: ACPI PCI bus
dev.pci.%parent:
dev.pci.7.wake: 0
dev.pci.7.subbus: 192
dev.pci.7.secbus: 192
dev.pci.7.pribus: 0
dev.pci.7.domain: 0
dev.pci.7.%parent: pci0
dev.pci.7.%pnpinfo: vendor=0x8086 device=0xa118 subvendor=0x1bcf subdevice=0x8069 class=0x060400
dev.pci.7.%location: slot=29 function=0 dbsf=pci0:0:29:0 handle=\_SB_.PCI0.RP09
dev.pci.7.%driver: pcib
dev.pci.7.%desc: ACPI PCI-PCI bridge
dev.pci.6.wake: 0
dev.pci.6.subbus: 144
dev.pci.6.secbus: 144
dev.pci.6.pribus: 0
dev.pci.6.domain: 0
dev.pci.6.%parent: pci0
dev.pci.6.%pnpinfo: vendor=0x8086 device=0xa114 subvendor=0x1bcf subdevice=0x8069 class=0x060400
dev.pci.6.%location: slot=28 function=4 dbsf=pci0:0:28:4 handle=\_SB_.PCI0.RP05
dev.pci.6.%driver: pcib
dev.pci.6.%desc: ACPI PCI-PCI bridge
dev.pci.5.wake: 0
dev.pci.5.subbus: 143
dev.pci.5.secbus: 143
dev.pci.5.pribus: 0
dev.pci.5.domain: 0
dev.pci.5.%parent: pci0
dev.pci.5.%pnpinfo: vendor=0x8086 device=0xa113 subvendor=0x1bcf subdevice=0x8069 class=0x060400
dev.pci.5.%location: slot=28 function=3 dbsf=pci0:0:28:3 handle=\_SB_.PCI0.RP04
dev.pci.5.%driver: pcib
dev.pci.5.%desc: ACPI PCI-PCI bridge
dev.pci.4.wake: 0
dev.pci.4.subbus: 96
dev.pci.4.secbus: 96
dev.pci.4.pribus: 0
dev.pci.4.domain: 0
dev.pci.4.%parent: pci0
dev.pci.4.%pnpinfo: vendor=0x8086 device=0xa110 subvendor=0x1bcf subdevice=0x8069 class=0x060400
dev.pci.4.%location: slot=28 function=0 dbsf=pci0:0:28:0 handle=\_SB_.PCI0.RP01
dev.pci.4.%driver: pcib
dev.pci.4.%desc: ACPI PCI-PCI bridge
dev.pci.3.wake: 0
dev.pci.3.subbus: 95
dev.pci.3.secbus: 95
dev.pci.3.pribus: 0
dev.pci.3.domain: 0
dev.pci.3.%parent: pci0
dev.pci.3.%pnpinfo: vendor=0x8086 device=0xa16a subvendor=0x1bcf subdevice=0x8069 class=0x060400
dev.pci.3.%location: slot=27 function=3 dbsf=pci0:0:27:3 handle=\_SB_.PCI0.RP20
dev.pci.3.%driver: pcib
dev.pci.3.%desc: ACPI PCI-PCI bridge
dev.pci.2.wake: 0
dev.pci.2.subbus: 48
dev.pci.2.secbus: 48
dev.pci.2.pribus: 0
dev.pci.2.domain: 0
dev.pci.2.%parent: pci0
dev.pci.2.%pnpinfo: vendor=0x8086 device=0xa168 subvendor=0x1bcf subdevice=0x8069 class=0x060400
dev.pci.2.%location: slot=27 function=0 dbsf=pci0:0:27:0 handle=\_SB_.PCI0.RP18
dev.pci.2.%driver: pcib
dev.pci.2.%desc: ACPI PCI-PCI bridge
dev.pci.1.wake: 0
dev.pci.1.subbus: 1
dev.pci.1.secbus: 1
dev.pci.1.pribus: 0
dev.pci.1.domain: 0
dev.pci.1.%parent: pci0
dev.pci.1.%pnpinfo: vendor=0x8086 device=0x1901 subvendor=0x1bcf subdevice=0x8069 class=0x060400
dev.pci.1.%location: slot=1 function=0 dbsf=pci0:0:1:0 handle=\_SB_.PCI0.PEG0
dev.pci.1.%driver: pcib
dev.pci.1.%desc: ACPI PCI-PCI bridge
dev.pci.0.%parent: acpi0
dev.pci.0.%pnpinfo: _HID=PNP0A08 _UID=0
dev.pci.0.%location: handle=\_SB_.PCI0
dev.pci.0.%driver: pcib
dev.pci.0.%desc: ACPI Host-PCI bridge
dev.pci.%parent:
dev.pci_link.7.%parent: acpi0
dev.pci_link.7.%pnpinfo: _HID=PNP0C0F _UID=8
dev.pci_link.7.%location: handle=\_SB_.LNKH
dev.pci_link.7.%driver: pci_link
dev.pci_link.7.%desc: ACPI PCI Link LNKH
dev.pci_link.6.%parent: acpi0
dev.pci_link.6.%pnpinfo: _HID=PNP0C0F _UID=7
dev.pci_link.6.%location: handle=\_SB_.LNKG
dev.pci_link.6.%driver: pci_link
dev.pci_link.6.%desc: ACPI PCI Link LNKG
dev.pci_link.5.%parent: acpi0

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dev.pci_link.5.%pnpinfo: _HID=PNP0C0F _UID=6
dev.pci_link.5.%location: handle=\_SB_.LNKF
dev.pci_link.5.%driver: pci_link
dev.pci_link.5.%desc: ACPI PCI Link LNKF
dev.pci_link.4.%parent: acpi0
dev.pci_link.4.%pnpinfo: _HID=PNP0C0F _UID=5
dev.pci_link.4.%location: handle=\_SB_.LNKE
dev.pci_link.4.%driver: pci_link
dev.pci_link.4.%desc: ACPI PCI Link LNKE
dev.pci_link.3.%parent: acpi0
dev.pci_link.3.%pnpinfo: _HID=PNP0C0F _UID=4
dev.pci_link.3.%location: handle=\_SB_.LNKD
dev.pci_link.3.%driver: pci_link
dev.pci_link.3.%desc: ACPI PCI Link LNKD
dev.pci_link.2.%parent: acpi0
dev.pci_link.2.%pnpinfo: _HID=PNP0C0F _UID=3
dev.pci_link.2.%location: handle=\_SB_.LNKC
dev.pci_link.2.%driver: pci_link
dev.pci_link.2.%desc: ACPI PCI Link LNKC
dev.pci_link.1.%parent: acpi0
dev.pci_link.1.%pnpinfo: _HID=PNP0C0F _UID=2
dev.pci_link.1.%location: handle=\_SB_.LNKB
dev.pci_link.1.%driver: pci_link
dev.pci_link.1.%desc: ACPI PCI Link LNKB
dev.pci_link.0.%parent: acpi0
dev.pci_link.0.%pnpinfo: _HID=PNP0C0F _UID=1
dev.pci_link.0.%location: handle=\_SB_.LNKA
dev.pci_link.0.%driver: pci_link
dev.pci_link.0.%desc: ACPI PCI Link LNKA
dev.pci_link.%parent:
dev.acpi_timer.0.%parent: acpi0
dev.acpi_timer.0.%pnpinfo: unknown
dev.acpi_timer.0.%location: unknown
dev.acpi_timer.0.%driver: acpi_timer
dev.acpi_timer.0.%desc: 24-bit timer at 3.579545MHz
dev.acpi_timer.%parent:
dev.attimer.0.%parent: acpi0
dev.attimer.0.%pnpinfo: _HID=PNP0100 _UID=0
dev.attimer.0.%location: handle=\_SB_.PCI0.LPCB.TIMR
dev.attimer.0.%driver: attimer
dev.attimer.0.%desc: AT timer
dev.attimer.%parent:
dev.atrtc.0.%parent: acpi0
dev.atrtc.0.%pnpinfo: _HID=PNP0B00 _UID=0
dev.atrtc.0.%location: handle=\_SB_.PCI0.LPCB.RTC_
dev.atrtc.0.%driver: atrtc
dev.atrtc.0.%desc: AT realtime clock
dev.atrtc.%parent:
dev.hpet.0.%mmap_allow_write: 1
dev.hpet.0.%mmap_allow: 1
dev.hpet.0.%parent: acpi0
dev.hpet.0.%pnpinfo: _HID=PNP0103 _UID=0
dev.hpet.0.%location: handle=\_SB_.PCI0.LPCB.HPET
dev.hpet.0.%driver: hpet
dev.hpet.0.%desc: High Precision Event Timer
dev.hpet.%parent:
dev.cpu.7.cx_method: C1/mwait/hwc C2/mwait/hwc
dev.cpu.7.cx_usage_counters: 1414 5402
dev.cpu.7.cx_usage: 20.74% 79.25% last 769us
dev.cpu.7.cx_lowest: C2
dev.cpu.7.cx_supported: C1/1/1 C2/2/120
dev.cpu.7.%parent: acpi0
dev.cpu.7.%pnpinfo: _HID=none _UID=0
dev.cpu.7.%location: handle=\_PR_.CPU7
dev.cpu.7.%driver: cpu
dev.cpu.7.%desc: ACPI CPU
dev.cpu.6.cx_method: C1/mwait/hwc C2/mwait/hwc
dev.cpu.6.cx_usage_counters: 945 5473
dev.cpu.6.cx_usage: 14.72% 85.27% last 1573us
dev.cpu.6.cx_lowest: C2
dev.cpu.6.cx_supported: C1/1/1 C2/2/120
dev.cpu.6.%parent: acpi0
dev.cpu.6.%pnpinfo: _HID=none _UID=0
dev.cpu.6.%location: handle=\_PR_.CPU6
dev.cpu.6.%driver: cpu
dev.cpu.6.%desc: ACPI CPU
dev.cpu.5.cx_method: C1/mwait/hwc C2/mwait/hwc
dev.cpu.5.cx_usage_counters: 613 4978
dev.cpu.5.cx_usage: 10.96% 89.03% last 2514us
dev.cpu.5.cx_lowest: C2
dev.cpu.5.cx_supported: C1/1/1 C2/2/120
dev.cpu.5.%parent: acpi0
dev.cpu.5.%pnpinfo: _HID=none _UID=0
dev.cpu.5.%location: handle=\_PR_.CPU5
dev.cpu.5.%driver: cpu
dev.cpu.5.%desc: ACPI CPU
dev.cpu.4.cx_method: C1/mwait/hwc C2/mwait/hwc
dev.cpu.4.cx_usage_counters: 799 5982
dev.cpu.4.cx_usage: 11.78% 88.21% last 857us
dev.cpu.4.cx_lowest: C2
dev.cpu.4.cx_supported: C1/1/1 C2/2/120
dev.cpu.4.%parent: acpi0
dev.cpu.4.%pnpinfo: _HID=none _UID=0
dev.cpu.4.%location: handle=\_PR_.CPU4
dev.cpu.4.%driver: cpu
dev.cpu.4.%desc: ACPI CPU
dev.cpu.3.cx_method: C1/mwait/hwc C2/mwait/hwc
dev.cpu.3.cx_usage_counters: 871 5548
dev.cpu.3.cx_usage: 13.56% 86.43% last 627us
dev.cpu.3.cx_lowest: C2
dev.cpu.3.cx_supported: C1/1/1 C2/2/120
dev.cpu.3.%parent: acpi0
dev.cpu.3.%pnpinfo: _HID=none _UID=0
dev.cpu.3.%location: handle=\_PR_.CPU3
dev.cpu.3.%driver: cpu
dev.cpu.3.%desc: ACPI CPU
dev.cpu.2.cx_method: C1/mwait/hwc C2/mwait/hwc
dev.cpu.2.cx_usage_counters: 1253 7322
dev.cpu.2.cx_usage: 14.61% 85.38% last 102us
dev.cpu.2.cx_lowest: C2
dev.cpu.2.cx_supported: C1/1/1 C2/2/120
dev.cpu.2.%parent: acpi0
dev.cpu.2.%pnpinfo: _HID=none _UID=0
dev.cpu.2.%location: handle=\_PR_.CPU2
dev.cpu.2.%driver: cpu
dev.cpu.2.%desc: ACPI CPU
dev.cpu.1.cx_method: C1/mwait/hwc C2/mwait/hwc
dev.cpu.1.cx_usage_counters: 784 5615
dev.cpu.1.cx_usage: 12.25% 87.74% last 1293us
dev.cpu.1.cx_lowest: C2

```

```

dev.cpu.1.cx_supported: C1/1/1 C2/2/120
dev.cpu.1.%parent: acpi0
dev.cpu.1.%pnpinfo: _HID=none _UID=0
dev.cpu.1.%location: handle=\_PR_.CPU1
dev.cpu.1.%driver: cpu
dev.cpu.1.%desc: ACPI CPU
dev.cpu.0.cx_method: C1/mwait/hwc C2/mwait/hwc
dev.cpu.0.cx_usage_counters: 769 13074
dev.cpu.0.cx_usage: 5.55% 94.44% last 838us
dev.cpu.0.cx_lowest: C2
dev.cpu.0.cx_supported: C1/1/1 C2/2/120
dev.cpu.0.Freq_Levels: 3401/80000 3400/80000 3200/73495 3000/67226 2800/61202 2700/58269 2500/52572 2300/47109 2100/41853 1900/36813 1700/32645 1500/28007 1400/25765 1200/21437
1000/17302 800/13362
dev.cpu.0.Freq: 3400
dev.cpu.0.%parent: acpi0
dev.cpu.0.%pnpinfo: _HID=none _UID=0
dev.cpu.0.%location: handle=\_PR_.CPU0
dev.cpu.0.%driver: cpu
dev.cpu.0.%desc: ACPI CPU
dev.cpu.%parent:
dev.acpi.sysresource.6.%parent: acpi0
dev.acpi.sysresource.6.%pnpinfo: _HID=PNP0C02 _UID=0
dev.acpi.sysresource.6.%location: handle=\_SB_.PCI0.IOTR
dev.acpi.sysresource.6.%driver: acpi_sysresource
dev.acpi.sysresource.6.%desc: System Resource
dev.acpi.sysresource.5.%parent: acpi0
dev.acpi.sysresource.5.%pnpinfo: _HID=PNP0C02 _UID=1
dev.acpi.sysresource.5.%location: handle=\_SB_.PCI0.PDR3
dev.acpi.sysresource.5.%driver: acpi_sysresource
dev.acpi.sysresource.5.%desc: System Resource
dev.acpi.sysresource.4.%parent: acpi0
dev.acpi.sysresource.4.%pnpinfo: _HID=PNP0C01 _UID=241
dev.acpi.sysresource.4.%location: handle=\_SB_.PCI0.RP20.PPF1
dev.acpi.sysresource.4.%driver: acpi_sysresource
dev.acpi.sysresource.4.%desc: System Resource
dev.acpi.sysresource.3.%parent: acpi0
dev.acpi.sysresource.3.%pnpinfo: _HID=INT3F00 _UID=0
dev.acpi.sysresource.3.%location: handle=\_SB_.PCI0.LPCB.CWDT
dev.acpi.sysresource.3.%driver: acpi_sysresource
dev.acpi.sysresource.3.%desc: System Resource
dev.acpi.sysresource.2.%parent: acpi0
dev.acpi.sysresource.2.%pnpinfo: _HID=PNP0C02 _UID=0
dev.acpi.sysresource.2.%location: handle=\_SB_.PCI0.LPCB.LDR2
dev.acpi.sysresource.2.%driver: acpi_sysresource
dev.acpi.sysresource.2.%desc: System Resource
dev.acpi.sysresource.1.%parent: acpi0
dev.acpi.sysresource.1.%pnpinfo: _HID=PNP0C02 _UID=2
dev.acpi.sysresource.1.%location: handle=\_SB_.PCI0.LPCB.LDR3
dev.acpi.sysresource.1.%driver: acpi_sysresource
dev.acpi.sysresource.1.%desc: System Resource
dev.acpi.sysresource.0.%parent: acpi0
dev.acpi.sysresource.0.%pnpinfo: _HID=PNP0C02 _UID=0
dev.acpi.sysresource.0.%location: handle=\_SB_.PCI0.LPCB.SIO1
dev.acpi.sysresource.0.%driver: acpi_sysresource
dev.acpi.sysresource.0.%desc: System Resource
dev.acpi.sysresource.%parent:
dev.acpi.0.%parent: nexus0
dev.acpi.0.%pnpinfo:
dev.acpi.0.%location:
dev.acpi.0.%driver: acpi
dev.acpi.0.%desc: AMI 0707
dev.acpi.%parent:
dev.cryptosoft.0.%parent: nexus0
dev.cryptosoft.0.%pnpinfo:
dev.cryptosoft.0.%location:
dev.cryptosoft.0.%driver: cryptosoft
dev.cryptosoft.0.%desc: software crypto
dev.cryptosoft.%parent:
dev.ram.0.%parent: nexus0
dev.ram.0.%pnpinfo:
dev.ram.0.%location:
dev.ram.0.%driver: ram
dev.ram.0.%desc: System RAM
dev.ram.%parent:
dev.vtga.0.%parent: nexus0
dev.vtga.0.%pnpinfo:
dev.vtga.0.%location:
dev.vtga.0.%driver: vtga
dev.vtga.0.%desc: VT VGA driver
dev.vtga.%parent:
dev.nexus.0.%parent: root0
dev.nexus.0.%pnpinfo:
dev.nexus.0.%location:
dev.nexus.0.%driver: nexus
dev.nexus.0.%desc:
dev.nexus.%parent:
dev.xen.xsd_kva: 0
dev.xen.xsd_port: 0
dev.xen.balloon.high_mem: 0
dev.xen.balloon.low_mem: 0
dev.xen.balloon.hard_limit: 0
dev.xen.balloon.driver_pages: 0
dev.xen.balloon.target: 0
dev.xen.balloon.current: 0
dev.netmap.ixl_rx_miss_bufs: 0
dev.netmap.iflib_rx_miss_bufs: 0
dev.netmap.iflib_rx_miss: 0
dev.netmap.iflib_crcstrip: 1
dev.netmap.bridge_batch: 1024
dev.netmap.default_pipes: 0
dev.netmap.priv_buf_num: 4098
dev.netmap.priv_buf_size: 2048
dev.netmap.buf_curr_num: 0
dev.netmap.buf_num: 163840
dev.netmap.buf_curr_size: 0
dev.netmap.buf_size: 2048
dev.netmap.priv_ring_num: 4
dev.netmap.priv_ring_size: 20480
dev.netmap.ring_curr_num: 0
dev.netmap.ring_num: 200
dev.netmap.ring_curr_size: 0
dev.netmap.ring_size: 36864
dev.netmap.priv_if_num: 1
dev.netmap.priv_if_size: 1024
dev.netmap.if_curr_num: 0
dev.netmap.if_num: 100
dev.netmap.if_curr_size: 0
dev.netmap.if_size: 1024
dev.netmap.generic_rings: 1

```

```
dev.netmap.generic_ringsize: 1024
dev.netmap.generic_mit: 100000
dev.netmap.admode: 0
dev.netmap.fwd: 0
dev.netmap.flags: 0
dev.netmap.adaptive_io: 0
dev.netmap.txsync_retry: 2
dev.netmap.no_pendintr: 1
dev.netmap.mitigate: 1
dev.netmap.no_timestamp: 0
dev.netmap.verbose: 0
dev.netmap.ix_rx_miss_bufs: 0
dev.netmap.ix_rx_miss: 0
dev.netmap.ix_crcstrip: 0
security.mac.mmap_revocation_via_cow: 0
security.mac.mmap_revocation: 1
security.mac.labeled: 0
security.mac.max_slots: 4
security.mac.version: 4
security.bsd.stack_guard_page: 0
security.bsd.unprivileged_get_quota: 0
security.bsd.hardlink_check_gid: 0
security.bsd.hardlink_check_uid: 0
security.bsd.unprivileged_read_msgbuf: 1
security.bsd.unprivileged_idprio: 0
security.bsd.unprivileged_proc_debug: 1
security.bsd.conservative_signals: 1
security.bsd.see_other_gids: 1
security.bsd.see_other_uids: 1
security.bsd.unprivileged_mlock: 1
security.bsd.suser_enabled: 1
security.bsd.map_at_zero: 0
security.jail.param.sysvshm.: 0
security.jail.param.sysvsem.: 0
security.jail.param.sysvmsg.: 0
security.jail.param.allow.mount.zfs: 0
security.jail.param.allow.mount.tmpfs: 0
security.jail.param.allow.mount.linsysfs: 0
security.jail.param.allow.mount.linprocfs: 0
security.jail.param.allow.mount.procfs: 0
security.jail.param.allow.mount.nullfs: 0
security.jail.param.allow.mount.fdescfs: 0
security.jail.param.allow.mount.devfs: 0
security.jail.param.allow.mount.: 0
security.jail.param.allow.socket.af: 0
security.jail.param.allow.quotas: 0
security.jail.param.allow.chflags: 0
security.jail.param.allow.raw_sockets: 0
security.jail.param.allow.sysvipc: 0
security.jail.param.allow.set_hostname: 0
security.jail.param.ip6.saddrsel: 0
security.jail.param.ip6.: 0
security.jail.param.ip4.saddrsel: 0
security.jail.param.ip4.: 0
security.jail.param.cpuset.id: 0
security.jail.param.host.hostid: 0
security.jail.param.host.hostuid: 64
security.jail.param.host.domainname: 256
security.jail.param.host.hostname: 256
security.jail.param.host.: 0
security.jail.param.children.max: 0
security.jail.param.children.cur: 0
security.jail.param.dying: 0
security.jail.param.persist: 0
security.jail.param.devfs_ruleset: 0
security.jail.param.enforce_statfs: 0
security.jail.param.osrelease: 32
security.jail.param.osreldate: 0
security.jail.param.securelevel: 0
security.jail.param.path: 1024
security.jail.param.name: 256
security.jail.param.parent: 0
security.jail.param.jid: 0
security.jail.devfs_ruleset: 0
security.jail.enforce_statfs: 2
security.jail.mount_zfs_allowed: 0
security.jail.mount_tmpfs_allowed: 0
security.jail.mount_linsysfs_allowed: 0
security.jail.mount_linprocfs_allowed: 0
security.jail.mount_procfs_allowed: 0
security.jail.mount_nullfs_allowed: 0
security.jail.mount_fdescfs_allowed: 0
security.jail.mount_devfs_allowed: 0
security.jail.mount_allowed: 0
security.jail.chflags_allowed: 0
security.jail.allow_raw_sockets: 0
security.jail.sysvipc_allowed: 0
security.jail.socket_unixiproute_only: 1
security.jail.set_hostname_allowed: 1
security.jail.jail_max_af_ips: 255
security.jail.vnet: 0
security.jail.jailed: 0
compat.io32.maxvmem: 0
compat.io32.maxssiz: 67108864
compat.io32.maxdsiz: 536870912
```

# システム起動時メッセージ

```
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FreeBSD is a registered trademark of The FreeBSD Foundation.
FreeBSD 11.0-RELEASE-p1 #0 r306420: Thu Sep 29 01:43:23 UTC 2016
root@releeng2.nyi.freebsd.org:/usr/obj/usr/src/sys/GENERIC amd64
FreeBSD clang version 3.8.0 (tags/RELEASE_380/final 262564) (based on LLVM 3.8.0)
VT(vga): resolution 640x480
CPU: Intel(R) Xeon(R) CPU E3-1230 v5 @ 3.40GHz (3408.12-MHz K8-class CPU)
Origin="GenuineIntel" Id=0x506e3 Family=0x6 Model=0x5e Stepping=3
Features=0xbfebfbff<FPU,VME,DE,PSE,TSC,MSR,MCE,CX8,APIC,SEP,MTRR,PGE,MCA,CMOV,PAT,PSE36,CLFLUSH,DTS,ACPI,MMX,FXSR,SSE,SSE2,SS,HTT,TM,PBE>

Features2=0x7ffafbff<SSE3,PCLMULQDQ,DTES64,MON,DS_CPL,VMX,SMX,EST,TM2,SSSE3,SDBG,FMA,CX16,xTPR,PDCM,PCID,SSE4.1,SSE4.2,x2APIC,MOVBE,POPCNT,TSCDLT,AESNI,XSAVE,OSXSAVE,AVX,F16C,RDRAND>
AMD Features=0x2c100800<SYSCALL,NX,Page1GB,RTSCLP,LM>
AMD Features2=0x121<LAHF,ABM,Prefetch>
Structured Extended Features=0x29c6fbff<FSGSBASE,TSCADJ,SGX,BMI1,HLE,AVX2,SMEP,BMI2,ERMS,INVPCID,RTM,NFPUSG,MPX,RDSEED,ADX,SMAP,CLFLUSHOPT,PROCTRACE>
XSAVE Features=0xf<XSAVEOPT,XSAVEC,XINUSE,XSAVES>
VT-x: PAT,HLT,MTF,PAUSE,EPT,UG,VPID
TSC: P-state invariant, performance statistics
real memory = 17179869184 (16384 MB)
avail memory = 16484663296 (15721 MB)
Event timer "LAPIC" quality 600
ACPI APIC Table: <AMI 0707 >
FreeBSD/SMP: Multiprocessor System Detected: 8 CPUs
FreeBSD/SMP: 1 package(s) x 4 core(s) x 2 hardware threads
random: unblocking device.
ioapic0 <Version 2.0> irqs 0-119 on motherboard
random: entropy device external interface
kbd1 at kbdmux0
netmap: loaded module
module_register_init: MOD_LOAD (vesa, 0xffffffffff810c950, 0) error 19
random: registering fast source Intel Secure Key RNG
random: fast provider: "Intel Secure Key RNG"
vtvga0: <VT VGA driver> on motherboard
cryptosoft0: <software crypto> on motherboard
acpi0: <AMI 0707> on motherboard
acpi0: Power Button (fixed)
cpu0: <ACPI CPU> on acpi0
cpu1: <ACPI CPU> on acpi0
cpu2: <ACPI CPU> on acpi0
cpu3: <ACPI CPU> on acpi0
cpu4: <ACPI CPU> on acpi0
cpu5: <ACPI CPU> on acpi0
cpu6: <ACPI CPU> on acpi0
cpu7: <ACPI CPU> on acpi0
hpet0: <High Precision Event Timer> iomem 0xfed00000-0xfed003ff on acpi0
Timecounter "HPET" frequency 24000000 Hz quality 950
Event timer "HPET" frequency 24000000 Hz quality 550
atrtc0: <AT realtime clock> port 0x70-0x77 irq 8 on acpi0
atrtc0: Warning: Couldn't map I/O.
Event timer "RTC" frequency 32768 Hz quality 0
attimer0: <AT timer> port 0x40-0x43,0x50-0x53 irq 0 on acpi0
Timecounter "i8254" frequency 1193182 Hz quality 0
Event timer "i8254" frequency 1193182 Hz quality 100
Timecounter "ACPI-fast" frequency 3579545 Hz quality 900
acpi_timer0: <24-bit timer at 3.579545MHz> port 0x1808-0x180b on acpi0
pcib0: <ACPI Host-PCI bridge> port 0xcfc8-0xcfff on acpi0
pcib0: _OSC returned error 0x10
pcio: <ACPI PCI bus> on pcib0
pcib1: <ACPI PCI-PCI bridge> irq 16 at device 1.0 on pci0
pci1: <ACPI PCI bus> on pcib1
xhci0: <Intel Sunrise Point USB 3.0 controller> mem 0xdf000000-0xdf0fffff irq 16 at device 20.0 on pci0
xhci0: 32 bytes context size, 64-bit DMA
usb0 at xhci0
pcio: <simple comms> at device 22.0 (no driver attached)
pcio: <simple comms> at device 22.1 (no driver attached)
ahci0: <Intel Sunrise Point AHCI SATA controller> port 0xf050-0xf057,0xf040-0xf043,0xf020-0xf03f mem 0xdf10000-0xdf11fff,0xdf1e000-0xdf1e0ff,0xdf1d000-0xdf1d7ff irq 16 at device 23.0 on pci0
ahci0: AHCI v1.31 with 5 6Gbps ports, Port Multiplier not supported
ahcich0: <AHCI channel> at channel 0 on ahci0
ahcich1: <AHCI channel> at channel 1 on ahci0
ahcich2: <AHCI channel> at channel 2 on ahci0
ahcich3: <AHCI channel> at channel 3 on ahci0
ahcich4: <AHCI channel> at channel 4 on ahci0
ahciem0: <AHCI enclosure management bridge> on ahci0
pcib2: <ACPI PCI-PCI bridge> irq 17 at device 27.0 on pci0
pci2: <ACPI PCI bus> on pcib2
bge0: <1000BASE-T Adapter(4ch), ASIC rev. 0x5719001> mem 0xddfb0000-0xddfbffff,0xddfa0000-0xddfaffff,0xddf90000-0xddf9ffff irq 17 at device 0.0 on pci2
bge0: CHIP ID 0x05719001; ASIC REV 0x5719; CHIP REV 0x57190; PCI-E
miibus0: <MII bus> on bge0
brgphy0: <BCM5719C 1000BASE-T media interface> PHY 1 on miibus0
brgphy0: 10baseT, 10baseT-FDX, 100baseTX, 100baseTX-FDX, 1000baseT, 1000baseT-master, 1000baseT-FDX, 1000baseT-FDX-master, auto, auto-flow
bge0: Using defaults for TSO: 65518/35/2048
bge0: Ethernet address: 8c:df:9d:81:8d:40
bge1: <1000BASE-T Adapter(4ch), ASIC rev. 0x5719001> mem 0xddf80000-0xddf8ffff,0xddf70000-0xddf7ffff,0xddf60000-0xddf6ffff irq 18 at device 0.1 on pci2
bge1: CHIP ID 0x05719001; ASIC REV 0x5719; CHIP REV 0x57190; PCI-E
miibus1: <MII bus> on bge1
brgphy1: <BCM5719C 1000BASE-T media interface> PHY 2 on miibus1
brgphy1: 10baseT, 10baseT-FDX, 100baseTX, 100baseTX-FDX, 1000baseT, 1000baseT-master, 1000baseT-FDX, 1000baseT-FDX-master, auto, auto-flow
bge1: Using defaults for TSO: 65518/35/2048
bge1: Ethernet address: 8c:df:9d:81:8d:41
bge2: <1000BASE-T Adapter(4ch), ASIC rev. 0x5719001> mem 0xddf50000-0xddf5ffff,0xddf40000-0xddf4ffff,0xddf30000-0xddf3ffff irq 17 at device 0.2 on pci2
bge2: CHIP ID 0x05719001; ASIC REV 0x5719; CHIP REV 0x57190; PCI-E
miibus2: <MII bus> on bge2
brgphy2: <BCM5719C 1000BASE-T media interface> PHY 3 on miibus2
brgphy2: 10baseT, 10baseT-FDX, 100baseTX, 100baseTX-FDX, 1000baseT, 1000baseT-master, 1000baseT-FDX, 1000baseT-FDX-master, auto, auto-flow
bge2: Using defaults for TSO: 65518/35/2048
bge2: Ethernet address: 8c:df:9d:81:8d:42
bge3: <1000BASE-T Adapter(4ch), ASIC rev. 0x5719001> mem 0xddf20000-0xddf2ffff,0xddf10000-0xddf1ffff,0xddf00000-0xddf0ffff irq 18 at device 0.3 on pci2
bge3: CHIP ID 0x05719001; ASIC REV 0x5719; CHIP REV 0x57190; PCI-E
miibus3: <MII bus> on bge3
brgphy3: <BCM5719C 1000BASE-T media interface> PHY 4 on miibus3
brgphy3: 10baseT, 10baseT-FDX, 100baseTX, 100baseTX-FDX, 1000baseT, 1000baseT-master, 1000baseT-FDX, 1000baseT-FDX-master, auto, auto-flow
bge3: Using defaults for TSO: 65518/35/2048
bge3: Ethernet address: 8c:df:9d:81:8d:43
pcib3: <ACPI PCI-PCI bridge> irq 19 at device 27.3 on pci0
pcib3: failed to allocate initial memory window: 0xde000000-0xdf9fffff
pci3: <ACPI PCI bus> on pcib3
vgapci0: <VGA-compatible display> mem 0xde000000-0xdeffffff,0xdf000000-0xdf7fffff irq 19 at device 0.0 on pci3
vgapci0: Boot video device
pcib4: <ACPI PCI-PCI bridge> irq 16 at device 28.0 on pci0
pci4: <ACPI PCI bus> on pcib4
bge4: <1000BASE-T Adapter(4ch), ASIC rev. 0x5719001> mem 0xddeb0000-0xddebffff,0xddea0000-0xddeaffff,0xdde90000-0xdde9ffff irq 16 at device 0.0 on pci4
bge4: CHIP ID 0x05719001; ASIC REV 0x5719; CHIP REV 0x57190; PCI-E
```

```
miibus4: <MII bus> on bge4
brgphy4: <BCM5719C 1000BASE-T media interface> PHY 1 on miibus4
brgphy4: 10baseT, 10baseT-FDX, 100baseTX, 100baseTX-FDX, 1000baseT, 1000baseT-master, 1000baseT-FDX, 1000baseT-FDX-master, auto, auto-flow
bge4: Using defaults for TSO: 65518/35/2048
bge4: Ethernet address: 8c:df:9d:81:8d:4c
bge5: <1000BASE-T-Adapter(4ch), ASIC rev. 0x5719001> mem 0xdde80000-0xdde8ffff,0xdde70000-0xdde7ffff,0xdde60000-0xdde6ffff irq 17 at device 0.1 on pci4
bge5: CHIP ID 0x05719001; ASIC REV 0x5719; CHIP REV 0x57190; PCI-E
miibus5: <MII bus> on bge5
brgphy5: <BCM5719C 1000BASE-T media interface> PHY 2 on miibus5
brgphy5: 10baseT, 10baseT-FDX, 100baseTX, 100baseTX-FDX, 1000baseT, 1000baseT-master, 1000baseT-FDX, 1000baseT-FDX-master, auto, auto-flow
bge5: Using defaults for TSO: 65518/35/2048
bge5: Ethernet address: 8c:df:9d:81:8d:4d
bge6: <1000BASE-T-Adapter(4ch), ASIC rev. 0x5719001> mem 0xdde50000-0xdde5ffff,0xdde40000-0xdde4ffff,0xdde30000-0xdde3ffff irq 16 at device 0.2 on pci4
bge6: CHIP ID 0x05719001; ASIC REV 0x5719; CHIP REV 0x57190; PCI-E
miibus6: <MII bus> on bge6
brgphy6: <BCM5719C 1000BASE-T media interface> PHY 3 on miibus6
brgphy6: 10baseT, 10baseT-FDX, 100baseTX, 100baseTX-FDX, 1000baseT, 1000baseT-master, 1000baseT-FDX, 1000baseT-FDX-master, auto, auto-flow
bge6: Using defaults for TSO: 65518/35/2048
bge6: Ethernet address: 8c:df:9d:81:8d:4e
bge7: <1000BASE-T-Adapter(4ch), ASIC rev. 0x5719001> mem 0xdde20000-0xdde2ffff,0xdde10000-0xdde1ffff,0xdde00000-0xdde0ffff irq 17 at device 0.3 on pci4
bge7: CHIP ID 0x05719001; ASIC REV 0x5719; CHIP REV 0x57190; PCI-E
miibus7: <MII bus> on bge7
brgphy7: <BCM5719C 1000BASE-T media interface> PHY 4 on miibus7
brgphy7: 10baseT, 10baseT-FDX, 100baseTX, 100baseTX-FDX, 1000baseT, 1000baseT-master, 1000baseT-FDX, 1000baseT-FDX-master, auto, auto-flow
bge7: Using defaults for TSO: 65518/35/2048
bge7: Ethernet address: 8c:df:9d:81:8d:4f
pcib5: <ACPI PCI-PCI bridge> irq 19 at device 28.3 on pci0
pci5: <ACPI PCI bus> on pcib5
bge8: <Broadcom NetXtreme Gigabit Ethernet Controller, ASIC rev. 0x5720000> mem 0xddd50000-0xddd5ffff,0xddd40000-0xddd4ffff,0xddd30000-0xddd3ffff irq 19 at device 0.0 on pci5
bge8: APE FW version: NCSI v1.3.12.0
bge8: CHIP ID 0x05720000; ASIC REV 0x5720; CHIP REV 0x57200; PCI-E
miibus8: <MII bus> on bge8
brgphy8: <BCM5720C 1000BASE-T media interface> PHY 1 on miibus8
brgphy8: 10baseT, 10baseT-FDX, 100baseTX, 100baseTX-FDX, 1000baseT, 1000baseT-master, 1000baseT-FDX, 1000baseT-FDX-master, auto, auto-flow
bge8: Using defaults for TSO: 65518/35/2048
bge8: Ethernet address: 40:8d:5c:a9:e5:33
bge9: <Broadcom NetXtreme Gigabit Ethernet Controller, ASIC rev. 0x5720000> mem 0xddd20000-0xddd2ffff,0xddd10000-0xddd1ffff,0xddd00000-0xddd0ffff irq 16 at device 0.1 on pci5
bge9: APE FW version: NCSI v1.3.12.0
bge9: CHIP ID 0x05720000; ASIC REV 0x5720; CHIP REV 0x57200; PCI-E
miibus9: <MII bus> on bge9
brgphy9: <BCM5720C 1000BASE-T media interface> PHY 2 on miibus9
brgphy9: 10baseT, 10baseT-FDX, 100baseTX, 100baseTX-FDX, 1000baseT, 1000baseT-master, 1000baseT-FDX, 1000baseT-FDX-master, auto, auto-flow
bge9: Using defaults for TSO: 65518/35/2048
bge9: Ethernet address: 40:8d:5c:a9:e5:34
pcib6: <ACPI PCI-PCI bridge> irq 16 at device 28.4 on pci0
pci6: <ACPI PCI bus> on pcib6
AVAG0 MegaRAID SAS FreeBSD mrsas driver version: 06.709.07.00-fbsd
mrsas0: <AVAGO Invader SAS Controller> port 0xe000-0xe0ff mem 0xdfc00000-0xdfc0ffff,0xdfbf0000-0xdfbfffff irq 16 at device 0.0 on pci6
mrsas0: Using MSI-X with 8 number of vectors
mrsas0: FW supports <96> MSIX vector,Online CPU 8 Current MSIX <8>
mrsas0: MSI-x interrupts setup success
pcib7: <ACPI PCI-PCI bridge> irq 16 at device 29.0 on pci0
pci7: <ACPI PCI bus> on pcib7
isa0: <PCI-ISA bridge> at device 31.0 on pci0
isa0: <ISA bus> on isa0
pci0: <memory> at device 31.2 (no driver attached)
acpi_button0: <Sleep Button> on acpi0
acpi_button1: <Power Button> on acpi0
uart0: <16550 or compatible> port 0x3f8-0x3ff irq 4 flags 0x10 on acpi0
uart1: <16550 or compatible> port 0x2f8-0x2ff irq 3 on acpi0
orm0: <ISA Option ROMs> at iomem 0xc0000-0xc7fff on isa0
ppc0: cannot reserve I/O port range
est0: <Enhanced SpeedStep Frequency Control> on cpu0
est1: <Enhanced SpeedStep Frequency Control> on cpu1
est2: <Enhanced SpeedStep Frequency Control> on cpu2
est3: <Enhanced SpeedStep Frequency Control> on cpu3
est4: <Enhanced SpeedStep Frequency Control> on cpu4
est5: <Enhanced SpeedStep Frequency Control> on cpu5
est6: <Enhanced SpeedStep Frequency Control> on cpu6
est7: <Enhanced SpeedStep Frequency Control> on cpu7
Timecounters tick every 1.000 msec
nvme cam probe device init
usb0: 5.0Gbps Super Speed USB v3.0
ugen0.1: <0x8086> at usb0
uhub0: <0x8086 XHCI root HUB, class 9/0, rev 3.00/1.00, addr 1> on usb0
mrsas0: Disestablish mrsas intr hook
(da0:mrsas0:0:0:0): UNMAPPED
da0 at mrsas0 bus 0 scbus6 target 0 lun 0
da0: <LSI MR9362-8i 4.22> Fixed Direct Access SPC-3 SCSI device
da0: Serial Number 002db1f032b303e81fe04a570bb00506
da0: 150,000MB/s transfers
da0: 2143200MB (4389273600 512 byte sectors)
ses0 at ahciem0 bus 0 scbus5 target 0 lun 0
ses0: <AHCI SGPIO Enclosure 1.00 0001> SEMB S-E-S 2.00 device
ses0: SEMB SES Device
cd0 at ahcich4 bus 0 scbus4 target 0 lun 0
cd0: <HL-DT-ST DVDROM DUD0N EV01> Removable CD-ROM SCSI device
cd0: Serial Number BGARVN3006339
cd0: 150,000MB/s transfers (SATA 1.x, UDMA6, ATAPI 12bytes, PIO 8192bytes)
cd0: Attempt to query device size failed: NOT READY, Medium not present - tray closed
SMP: AP CPU #1 Launched!
SMP: AP CPU #2 Launched!
SMP: AP CPU #5 Launched!
SMP: AP CPU #6 Launched!
SMP: AP CPU #7 Launched!
SMP: AP CPU #3 Launched!
SMP: AP CPU #4 Launched!
Timecounter "TSC-low" frequency 1704060214 Hz quality 1000
Trying to mount root from ufs:/dev/da0s1a [rw]...
bge8: link state changed to DOWN
uhub0: 26 ports with 26 removable, self powered
ugen0.2: <vendor 0x0409> at usb0
uhub1: <vendor 0x0409 product 0x005a, class 9/0, rev 2.00/1.00, addr 1> on usb0
uhub1: 3 ports with 2 removable, bus powered
ugen0.3: <Topre Corporation> at usb0
ukbd0: <Topre Corporation HHKB Professional, class 0/0, rev 1.10/1.02, addr 2> on usb0
kbd2 at ukbd0
ugen0.4: <American Megatrends Inc.> at usb0
ukbd1: <Keyboard Interface> on usb0
kbd3 at ukbd1
ums0: <Mouse Interface> on usb0
ums0: 3 buttons and [Z] coordinates ID=0
bge8: link state changed to UP
```



# カーネルデバイスツリー

```
nexus0
vtvga0
  I/O memory addresses:
    0xa0000-0xffff
cryptosoft0
apic0
  I/O memory addresses:
    0xfec00000-0xfec0001f
ram0
  I/O memory addresses:
    0x0-0x9b7ff
    0x100000-0x7704ffff
    0x7709b000-0x7c365fff
    0x7faf000-0x7fafffff
    0x10000000-0x47ffffff
acpi0
  Interrupt request lines:
    0x9
  I/O ports:
    0x2e-0x2f
    0x4e-0x4f
    0x61
    0x63
    0x65
    0x67
    0x70
    0x80
    0x92
    0xb2-0xb3
    0x680-0x69f
    0x800-0x87f
    0x164e-0x164f
    0x1800-0x18fe
    0xf00-0xfffe
    0xffff
  I/O memory addresses:
    0xdf800000-0xdf8fffff
    0xdf914000-0xdf914fff
    0xdffe0000-0xdfffffff
    0xe0000000-0xffffffff
    0xfed10000-0xfed17fff
    0xfed18000-0xfed18fff
    0xfed19000-0xfed19fff
    0xfed20000-0xfed3ffff
    0xfed45000-0xfed8ffff
    0xfed90000-0xfed93fff
    0xfee00000-0xfefeffff
    0xff000000-0xffffffff
cpu0
  acpi_perf0
  est0
  cpufreq0
cpu1
  acpi_perf1
  est1
  cpufreq1
cpu2
  acpi_perf2
  est2
  cpufreq2
cpu3
  acpi_perf3
  est3
  cpufreq3
cpu4
  acpi_perf4
  est4
  cpufreq4
cpu5
  acpi_perf5
  est5
  cpufreq5
cpu6
  acpi_perf6
  est6
  cpufreq6
cpu7
  acpi_perf7
  est7
  cpufreq7
pcib0
  I/O ports:
    0xcf8-0xcff
pci0
  PCI domain 0 bus numbers:
    0
  hostb0
  pcib1
  PCI domain 0 bus numbers:
    1
  pci1
  pcib1 bus numbers:
    1
xhci0
  Interrupt request lines:
    0x108
  I/O memory addresses:
    0xdf00000-0xdf0fffff
usb0
  uhub0
  uhub1
  ukbd0
  ukbd1
  ums0
ahci0
  Interrupt request lines:
    0x109
  I/O ports:
    0xf020-0xf03f
    0xf040-0xf043
    0xf050-0xf057
  I/O memory addresses:
    0xdf10000-0xdf11fff
    0xdf1d000-0xdf1d7ff
```

```

0xdf1e000-0xdf1e0ff
ahcich0
I/O memory addresses:
0xdf1d100-0xdf1d17f
ahcich1
I/O memory addresses:
0xdf1d180-0xdf1d1ff
ahcich2
I/O memory addresses:
0xdf1d200-0xdf1d27f
ahcich3
I/O memory addresses:
0xdf1d280-0xdf1d2ff
ahcich4
I/O memory addresses:
0xdf1d300-0xdf1d37f
ahciem0
I/O memory addresses:
0xdf1d020-0xdf1d023
0xdf1d580-0xdf1d587
pcib2
I/O memory addresses:
0xddf00000-0xddfffff
0xdfe00000-0xdfefffff
PCI domain 0 bus numbers:
48
pci2
pci2 bus numbers:
48
bge0
Interrupt request lines:
0x10a
pci2 prefetch window:
0xddf90000-0xddf9ffff
0xddfa0000-0xddfaffff
0xddfb0000-0xddfbffff
miibus0
brgphy0
bge1
Interrupt request lines:
0x10b
pci2 prefetch window:
0xddf60000-0xddf6ffff
0xddf70000-0xddf7ffff
0xddf80000-0xddf8ffff
miibus1
brgphy1
bge2
Interrupt request lines:
0x10c
pci2 prefetch window:
0xddf30000-0xddf3ffff
0xddf40000-0xddf4ffff
0xddf50000-0xddf5ffff
miibus2
brgphy2
bge3
Interrupt request lines:
0x10d
pci2 prefetch window:
0xddf00000-0xddf0ffff
0xddf10000-0xddf1ffff
0xddf20000-0xddf2ffff
miibus3
brgphy3
pcib3
I/O memory addresses:
0xde000000-0xdeffffff
0xdf000000-0xdf7fffff
PCI domain 0 bus numbers:
95
pci3
pci3 bus numbers:
95
vgapci0
pci3 memory window:
0xdf000000-0xdf7fffff
pci3 prefetch window:
0xde000000-0xdeffffff
pcib4
I/O memory addresses:
0xdde00000-0xddeffffff
0xdfd00000-0xdfdfffff
PCI domain 0 bus numbers:
96
pci4
pci4 bus numbers:
96
bge4
Interrupt request lines:
0x10e
pci4 prefetch window:
0xdde90000-0xdde9ffff
0xddea0000-0xddeaffff
0xddeb0000-0xddebffff
miibus4
brgphy4
bge5
Interrupt request lines:
0x10f
pci4 prefetch window:
0xdde60000-0xdde6ffff
0xdde70000-0xdde7ffff
0xdde80000-0xdde8ffff
miibus5
brgphy5
bge6
Interrupt request lines:
0x110
pci4 prefetch window:
0xdde30000-0xdde3ffff
0xdde40000-0xdde4ffff
0xdde50000-0xdde5ffff
miibus6
brgphy6
bge7
Interrupt request lines:
0x111
pci4 prefetch window:
0xdde00000-0xdde0ffff

```

```

        0xdde10000-0xdde1ffff
        0xdde20000-0xdde2ffff
miibus7
  brgphy7
pcib5
  I/O memory addresses:
    0xdd000000-0xddffffff
  PCI domain 0 bus numbers:
    143
pci5
  pcib5 bus numbers:
    143
bge8
  Interrupt request lines:
    0x112
  pcib5 prefetch window:
    0xdd300000-0xdd3fffff
    0xdd400000-0xdd4fffff
    0xdd500000-0xdd5fffff
miibus8
  brgphy8
bge9
  Interrupt request lines:
    0x113
  pcib5 prefetch window:
    0xdd000000-0xdd0fffff
    0xdd100000-0xdd1fffff
    0xdd200000-0xdd2fffff
miibus9
  brgphy9
pcib6
  I/O ports:
    0xe000-0xefff
  I/O memory addresses:
    0xdfa00000-0xdfcfffff
  PCI domain 0 bus numbers:
    144
pci6
  pcib6 bus numbers:
    144
mrsas0
  Interrupt request lines:
    0x114
    0x115
    0x116
    0x117
    0x118
    0x119
    0x11a
    0x11b
  pcib6 I/O port window:
    0xe000-0xe0ff
  pcib6 memory window:
    0xdfb00000-0xdfbfffff
    0xdfc00000-0xdfc0fffff
pcib7
  PCI domain 0 bus numbers:
    192
pci7
  pcib7 bus numbers:
    192
isab0
isa0
orm0
  I/O memory addresses:
    0xc0000-0xc7fff
psmcpnp0
acpi_sysresource0
uart0
  Interrupt request lines:
    0x4
  I/O ports:
    0x3f8-0x3ff
uart1
  Interrupt request lines:
    0x3
  I/O ports:
    0x2f8-0x2ff
hpet0
  Interrupt request lines:
    0x100
    0x101
    0x102
    0x103
    0x104
    0x105
    0x106
    0x107
  I/O memory addresses:
    0xfed00000-0xfed003ff
fpupnp0
  I/O ports:
    0xf0
acpi_sysresource1
acpi_sysresource2
atrtc0
  Interrupt request lines:
    0x8
attimer0
  Interrupt request lines:
    0x0
  I/O ports:
    0x40-0x43
    0x50-0x53
acpi_sysresource3
acpi_sysresource4
acpi_sysresource5
acpi_sysresource6
pci_link0
pci_link1
pci_link2
pci_link3
pci_link4
pci_link5
pci_link6
pci_link7
acpi_button0
acpi_button1
acpi_timer0
  ACPI I/O ports:

```



# PCIバスデータ詳細一覧

```
hostb0@pci:0:0:0:      class=0x060000 card=0x80691bcf chip=0x19188086 rev=0x07 hdr=0x00
vendor                = 'Intel Corporation'
device                = 'Skylake Host Bridge/DRAM Registers'
class                 = bridge
subclass              = HOST-PCI
cap 09[e0]            = vendor (length 16) Intel cap 0 version 1
pcib1@pci:0:1:0:      class=0x060400 card=0x80691bcf chip=0x19018086 rev=0x07 hdr=0x01
vendor                = 'Intel Corporation'
device                = 'Skylake PCIe Controller (x16)'
class                 = bridge
subclass              = PCI-PCI
cap 0d[88]            = PCI Bridge card=0x80691bcf
cap 01[80]            = powerspec 3 supports D0 D3 current D0
cap 05[90]            = MSI supports 1 message
cap 10[a0]            = PCI-Express 2 root port max data 256(256)
                      link x0(x16) speed 0.0(8.0) ASPM disabled(L0s/L1)
                      slot 3 power limit 75000 mW
ecap 0002[100]        = VC 1 max VC0
ecap 0005[140]        = Root Complex Link Declaration 1
ecap 0001[1c0]        = AER 1 0 fatal 0 non-fatal 0 corrected
ecap 0019[d94]        = PCIe Sec 1 lane errors 0
xhci0@pci:0:20:0:     class=0x0c0330 card=0x80691bcf chip=0xa12f8086 rev=0x31 hdr=0x00
vendor                = 'Intel Corporation'
device                = 'Sunrise Point-H USB 3.0 xHCI Controller'
class                 = serial bus
subclass              = USB
cap 01[70]            = powerspec 2 supports D0 D3 current D0
cap 05[80]            = MSI supports 8 messages, 64 bit enabled with 1 message
none0@pci:0:20:2:     class=0x118000 card=0x80691bcf chip=0xa1318086 rev=0x31 hdr=0x00
vendor                = 'Intel Corporation'
device                = 'Sunrise Point-H Thermal subsystem'
class                 = dasp
cap 01[50]            = powerspec 3 supports D0 D3 current D0
cap 05[80]            = MSI supports 1 message
none1@pci:0:22:0:     class=0x078000 card=0x80691bcf chip=0xa13a8086 rev=0x31 hdr=0x00
vendor                = 'Intel Corporation'
device                = 'Sunrise Point-H CSME HECI'
class                 = simple comms
cap 01[50]            = powerspec 3 supports D0 D3 current D0
cap 05[8c]            = MSI supports 1 message, 64 bit
none2@pci:0:22:1:     class=0x078000 card=0x80691bcf chip=0xa13b8086 rev=0x31 hdr=0x00
vendor                = 'Intel Corporation'
device                = 'Sunrise Point-H CSME HECI'
class                 = simple comms
cap 01[50]            = powerspec 3 supports D0 D3 current D0
cap 05[8c]            = MSI supports 1 message, 64 bit
ahci0@pci:0:23:0:     class=0x010601 card=0x80691bcf chip=0xa1028086 rev=0x31 hdr=0x00
vendor                = 'Intel Corporation'
device                = 'Sunrise Point-H SATA controller [AHCI mode]'
class                 = mass storage
subclass              = SATA
cap 05[80]            = MSI supports 1 message enabled with 1 message
cap 01[70]            = powerspec 3 supports D0 D3 current D0
cap 12[a8]            = SATA Index-Data Pair
pcib2@pci:0:27:0:     class=0x060400 card=0x80691bcf chip=0xa1688086 rev=0xf1 hdr=0x01
vendor                = 'Intel Corporation'
device                = 'Sunrise Point-H PCI Root Port'
class                 = bridge
subclass              = PCI-PCI
cap 10[40]            = PCI-Express 2 root port max data 256(256) ARI disabled
                      link x1(x1) speed 5.0(8.0)
                      slot 2 power limit 100 mW
cap 05[80]            = MSI supports 1 message
cap 0d[90]            = PCI Bridge card=0x80691bcf
cap 01[a0]            = powerspec 3 supports D0 D3 current D0
ecap 0001[100]        = AER 1 0 fatal 0 non-fatal 0 corrected
ecap 000d[140]        = ACS 1
ecap 0019[220]        = PCIe Sec 1 lane errors 0
pcib3@pci:0:27:3:     class=0x060400 card=0x80691bcf chip=0xa16a8086 rev=0xf1 hdr=0x01
vendor                = 'Intel Corporation'
device                = 'Sunrise Point-H PCI Root Port'
class                 = bridge
subclass              = PCI-PCI
cap 10[40]            = PCI-Express 2 root port max data 256(256) ARI disabled
                      link x1(x1) speed 2.5(8.0)
                      slot 0 power limit 100 mW
cap 05[80]            = MSI supports 1 message
cap 0d[90]            = PCI Bridge card=0x80691bcf
cap 01[a0]            = powerspec 3 supports D0 D3 current D0
ecap 0001[100]        = AER 1 0 fatal 1 non-fatal 0 corrected
ecap 000d[140]        = ACS 1
ecap 0019[220]        = PCIe Sec 1 lane errors 0
pcib4@pci:0:28:0:     class=0x060400 card=0x80691bcf chip=0xa1108086 rev=0xf1 hdr=0x01
vendor                = 'Intel Corporation'
device                = 'Sunrise Point-H PCI Express Root Port'
class                 = bridge
subclass              = PCI-PCI
cap 10[40]            = PCI-Express 2 root port max data 256(256) ARI disabled
                      link x2(x2) speed 5.0(8.0)
                      slot 1 power limit 250 mW
cap 05[80]            = MSI supports 1 message
cap 0d[90]            = PCI Bridge card=0x80691bcf
cap 01[a0]            = powerspec 3 supports D0 D3 current D0
ecap 0001[100]        = AER 1 0 fatal 0 non-fatal 0 corrected
ecap 000d[140]        = ACS 1
ecap 0019[220]        = PCIe Sec 1 lane errors 0
pcib5@pci:0:28:3:     class=0x060400 card=0x80691bcf chip=0xa1138086 rev=0xf1 hdr=0x01
vendor                = 'Intel Corporation'
device                = 'Sunrise Point-H PCI Express Root Port'
class                 = bridge
subclass              = PCI-PCI
cap 10[40]            = PCI-Express 2 root port max data 256(256) ARI disabled
                      link x1(x1) speed 5.0(8.0)
                      slot 0 power limit 100 mW
cap 05[80]            = MSI supports 1 message
cap 0d[90]            = PCI Bridge card=0x80691bcf
cap 01[a0]            = powerspec 3 supports D0 D3 current D0
ecap 0001[100]        = AER 1 0 fatal 0 non-fatal 0 corrected
ecap 000d[140]        = ACS 1
ecap 0019[220]        = PCIe Sec 1 lane errors 0
pcib6@pci:0:28:4:     class=0x060400 card=0x80691bcf chip=0xa1148086 rev=0xf1 hdr=0x01
vendor                = 'Intel Corporation'
device                = 'Sunrise Point-H PCI Express Root Port'
class                 = bridge
```

```

subclass = PCI-PCI
cap 10[40] = PCI-Express 2 root port max data 256(256) ARI enabled
            link x4(x4) speed 8.0(8.0)
            slot 4 power limit 250 mW
cap 05[80] = MSI supports 1 message
cap 0d[90] = PCI Bridge card=0x80691bcf
cap 01[a0] = powerspec 3 supports D0 D3 current D0
ecap 0001[100] = AER 1 0 fatal 0 non-fatal 0 corrected
ecap 000d[140] = ACS 1
ecap 0019[220] = PCIe Sec 1 lane errors 0
pcib7@pci:0:29:0:      class=0x060400 card=0x80691bcf chip=0xa1188086 rev=0xf1 hdr=0x01
vendor      = 'Intel Corporation'
device      = 'Sunrise Point-H PCI Express Root Port'
class       = bridge
subclass    = PCI-PCI
cap 10[40] = PCI-Express 2 root port max data 128(256) ARI disabled
            link x0(x4) speed 0.0(8.0)
            slot 0 power limit 250 mW
cap 05[80] = MSI supports 1 message
cap 0d[90] = PCI Bridge card=0x80691bcf
cap 01[a0] = powerspec 3 supports D0 D3 current D0
ecap 0001[100] = AER 1 0 fatal 0 non-fatal 0 corrected
ecap 000d[140] = ACS 1
ecap 0019[220] = PCIe Sec 1 lane errors 0
isab0@pci:0:31:0:     class=0x060100 card=0x80691bcf chip=0xa1498086 rev=0x31 hdr=0x00
vendor      = 'Intel Corporation'
device      = 'Sunrise Point-H LPC Controller'
class       = bridge
subclass    = PCI-ISA
none3@pci:0:31:2:     class=0x058000 card=0x80691bcf chip=0xa1218086 rev=0x31 hdr=0x00
vendor      = 'Intel Corporation'
device      = 'Sunrise Point-H PMC'
class       = memory
none4@pci:0:31:4:     class=0x0c0500 card=0x80691bcf chip=0xa1238086 rev=0x31 hdr=0x00
vendor      = 'Intel Corporation'
device      = 'Sunrise Point-H SMBus'
class       = serial bus
subclass    = SMBus
bge0@pci:0:48:0:     class=0x020000 card=0x80641bcf chip=0x165714e4 rev=0x01 hdr=0x00
vendor      = 'Broadcom Corporation'
device      = 'NetXtreme BCM5719 Gigabit Ethernet PCIe'
class       = network
subclass    = ethernet
cap 01[48] = powerspec 3 supports D0 D3 current D0
cap 03[50] = VPD
cap 05[58] = MSI supports 8 messages, 64 bit enabled with 1 message
cap 11[a0] = MSI-X supports 17 messages
            Table in map 0x20[0x0], PBA in map 0x20[0x1000]
cap 10[ac] = PCI-Express 2 endpoint max data 256(256) FLR
            link x1(x4) speed 5.0(5.0) ASPM disabled(L0s/L1)
ecap 0001[100] = AER 1 0 fatal 0 non-fatal 1 corrected
ecap 0003[13c] = Serial 1 00008cdf9d818d40
ecap 0004[150] = Power Budgeting 1
ecap 0002[160] = VC 1 max VC0
bge1@pci:0:48:0:1:   class=0x020000 card=0x80641bcf chip=0x165714e4 rev=0x01 hdr=0x00
vendor      = 'Broadcom Corporation'
device      = 'NetXtreme BCM5719 Gigabit Ethernet PCIe'
class       = network
subclass    = ethernet
cap 01[48] = powerspec 3 supports D0 D3 current D0
cap 03[50] = VPD
cap 05[58] = MSI supports 8 messages, 64 bit enabled with 1 message
cap 11[a0] = MSI-X supports 17 messages
            Table in map 0x20[0x0], PBA in map 0x20[0x1000]
cap 10[ac] = PCI-Express 2 endpoint max data 256(256) FLR
            link x1(x4) speed 5.0(5.0) ASPM disabled(L0s/L1)
ecap 0001[100] = AER 1 0 fatal 0 non-fatal 1 corrected
ecap 0003[13c] = Serial 1 00008cdf9d818d41
ecap 0004[150] = Power Budgeting 1
ecap 0002[160] = VC 1 max VC0
bge2@pci:0:48:0:2:   class=0x020000 card=0x80641bcf chip=0x165714e4 rev=0x01 hdr=0x00
vendor      = 'Broadcom Corporation'
device      = 'NetXtreme BCM5719 Gigabit Ethernet PCIe'
class       = network
subclass    = ethernet
cap 01[48] = powerspec 3 supports D0 D3 current D0
cap 03[50] = VPD
cap 05[58] = MSI supports 8 messages, 64 bit enabled with 1 message
cap 11[a0] = MSI-X supports 17 messages
            Table in map 0x20[0x0], PBA in map 0x20[0x1000]
cap 10[ac] = PCI-Express 2 endpoint max data 256(256) FLR
            link x1(x4) speed 5.0(5.0) ASPM disabled(L0s/L1)
ecap 0001[100] = AER 1 0 fatal 0 non-fatal 1 corrected
ecap 0003[13c] = Serial 1 00008cdf9d818d42
ecap 0004[150] = Power Budgeting 1
ecap 0002[160] = VC 1 max VC0
bge3@pci:0:48:0:3:   class=0x020000 card=0x80641bcf chip=0x165714e4 rev=0x01 hdr=0x00
vendor      = 'Broadcom Corporation'
device      = 'NetXtreme BCM5719 Gigabit Ethernet PCIe'
class       = network
subclass    = ethernet
cap 01[48] = powerspec 3 supports D0 D3 current D0
cap 03[50] = VPD
cap 05[58] = MSI supports 8 messages, 64 bit enabled with 1 message
cap 11[a0] = MSI-X supports 17 messages
            Table in map 0x20[0x0], PBA in map 0x20[0x1000]
cap 10[ac] = PCI-Express 2 endpoint max data 256(256) FLR
            link x1(x4) speed 5.0(5.0) ASPM disabled(L0s/L1)
ecap 0001[100] = AER 1 0 fatal 0 non-fatal 1 corrected
ecap 0003[13c] = Serial 1 00008cdf9d818d43
ecap 0004[150] = Power Budgeting 1
ecap 0002[160] = VC 1 max VC0
vgapci0@pci:0:95:0:0: class=0x030000 card=0x80691bcf chip=0x0522102b rev=0x00 hdr=0x00
vendor      = 'Matrox Electronics Systems Ltd.'
device      = 'MGA G200e [Pilot] ServerEngines (SEP1)'
class       = display
subclass    = VGA
cap 01[dc] = powerspec 2 supports D0 D3 current D0
cap 10[e4] = PCI-Express 1 legacy endpoint max data 256(256)
            link x1(x1) speed 2.5(2.5) ASPM disabled(L0s)
cap 05[54] = MSI supports 1 message
bge4@pci:0:96:0:0:   class=0x020000 card=0x80641bcf chip=0x165714e4 rev=0x01 hdr=0x00
vendor      = 'Broadcom Corporation'
device      = 'NetXtreme BCM5719 Gigabit Ethernet PCIe'
class       = network
subclass    = ethernet
cap 01[48] = powerspec 3 supports D0 D3 current D0
cap 03[50] = VPD
cap 05[58] = MSI supports 8 messages, 64 bit enabled with 1 message
cap 11[a0] = MSI-X supports 17 messages

```

```

    Table in map 0x20[0x0], PBA in map 0x20[0x1000]
cap 10[ac] = PCI-Express 2 endpoint max data 256(256) FLR
           link x2(x4) speed 5.0(5.0) ASPM disabled(L0s/L1)
ecap 0001[100] = AER 1 0 fatal 0 non-fatal 1 corrected
ecap 0003[13c] = Serial 1 00008cdf9d818d4c
ecap 0004[150] = Power Budgeting 1
ecap 0002[160] = VC 1 max VC0
bge5@pci:96:0:1:      class=0x020000 card=0x80641bcf chip=0x165714e4 rev=0x01 hdr=0x00
vendor          = 'Broadcom Corporation'
device          = 'NetXtreme BCM5719 Gigabit Ethernet PCIe'
class           = network
subclass        = ethernet
cap 01[48]      = powerspec 3 supports D0 D3 current D0
cap 03[50]      = VPD
cap 05[58]      = MSI supports 8 messages, 64 bit enabled with 1 message
cap 11[a0]      = MSI-X supports 17 messages
           Table in map 0x20[0x0], PBA in map 0x20[0x1000]
cap 10[ac]      = PCI-Express 2 endpoint max data 256(256) FLR
           link x2(x4) speed 5.0(5.0) ASPM disabled(L0s/L1)
ecap 0001[100] = AER 1 0 fatal 0 non-fatal 1 corrected
ecap 0003[13c] = Serial 1 00008cdf9d818d4d
ecap 0004[150] = Power Budgeting 1
ecap 0002[160] = VC 1 max VC0
bge6@pci:96:0:2:      class=0x020000 card=0x80641bcf chip=0x165714e4 rev=0x01 hdr=0x00
vendor          = 'Broadcom Corporation'
device          = 'NetXtreme BCM5719 Gigabit Ethernet PCIe'
class           = network
subclass        = ethernet
cap 01[48]      = powerspec 3 supports D0 D3 current D0
cap 03[50]      = VPD
cap 05[58]      = MSI supports 8 messages, 64 bit enabled with 1 message
cap 11[a0]      = MSI-X supports 17 messages
           Table in map 0x20[0x0], PBA in map 0x20[0x1000]
cap 10[ac]      = PCI-Express 2 endpoint max data 256(256) FLR
           link x2(x4) speed 5.0(5.0) ASPM disabled(L0s/L1)
ecap 0001[100] = AER 1 0 fatal 0 non-fatal 1 corrected
ecap 0003[13c] = Serial 1 00008cdf9d818d4e
ecap 0004[150] = Power Budgeting 1
ecap 0002[160] = VC 1 max VC0
bge7@pci:96:0:3:      class=0x020000 card=0x80641bcf chip=0x165714e4 rev=0x01 hdr=0x00
vendor          = 'Broadcom Corporation'
device          = 'NetXtreme BCM5719 Gigabit Ethernet PCIe'
class           = network
subclass        = ethernet
cap 01[48]      = powerspec 3 supports D0 D3 current D0
cap 03[50]      = VPD
cap 05[58]      = MSI supports 8 messages, 64 bit enabled with 1 message
cap 11[a0]      = MSI-X supports 17 messages
           Table in map 0x20[0x0], PBA in map 0x20[0x1000]
cap 10[ac]      = PCI-Express 2 endpoint max data 256(256) FLR
           link x2(x4) speed 5.0(5.0) ASPM disabled(L0s/L1)
ecap 0001[100] = AER 1 0 fatal 0 non-fatal 1 corrected
ecap 0003[13c] = Serial 1 00008cdf9d818d4f
ecap 0004[150] = Power Budgeting 1
ecap 0002[160] = VC 1 max VC0
bge8@pci:143:0:0:     class=0x020000 card=0x80691bcf chip=0x165f14e4 rev=0x00 hdr=0x00
vendor          = 'Broadcom Corporation'
device          = 'NetXtreme BCM5720 Gigabit Ethernet PCIe'
class           = network
subclass        = ethernet
cap 01[48]      = powerspec 3 supports D0 D3 current D0
cap 03[50]      = VPD
cap 05[58]      = MSI supports 8 messages, 64 bit enabled with 1 message
cap 11[a0]      = MSI-X supports 17 messages
           Table in map 0x20[0x0], PBA in map 0x20[0x1000]
cap 10[ac]      = PCI-Express 2 endpoint max data 256(256) FLR
           link x1(x2) speed 5.0(5.0) ASPM disabled(L0s/L1)
ecap 0001[100] = AER 1 0 fatal 0 non-fatal 1 corrected
ecap 0003[13c] = Serial 1 0000408d5ca9e533
ecap 0004[150] = Power Budgeting 1
ecap 0002[160] = VC 1 max VC0
bge9@pci:143:0:1:     class=0x020000 card=0x80691bcf chip=0x165f14e4 rev=0x00 hdr=0x00
vendor          = 'Broadcom Corporation'
device          = 'NetXtreme BCM5720 Gigabit Ethernet PCIe'
class           = network
subclass        = ethernet
cap 01[48]      = powerspec 3 supports D0 D3 current D0
cap 03[50]      = VPD
cap 05[58]      = MSI supports 8 messages, 64 bit enabled with 1 message
cap 11[a0]      = MSI-X supports 17 messages
           Table in map 0x20[0x0], PBA in map 0x20[0x1000]
cap 10[ac]      = PCI-Express 2 endpoint max data 256(256) FLR
           link x1(x2) speed 5.0(5.0) ASPM disabled(L0s/L1)
ecap 0001[100] = AER 1 0 fatal 0 non-fatal 1 corrected
ecap 0003[13c] = Serial 1 0000408d5ca9e534
ecap 0004[150] = Power Budgeting 1
ecap 0002[160] = VC 1 max VC0
mrsas0@pci:144:0:0:   class=0x010400 card=0x93641000 chip=0x005d1000 rev=0x02 hdr=0x00
vendor          = 'LSI Logic / Symbios Logic'
device          = 'MegaRAID SAS-3 3108 [Invader]'
class           = mass storage
subclass        = RAID
cap 01[50]      = powerspec 3 supports D0 D1 D2 D3 current D0
cap 10[68]      = PCI-Express 2 endpoint max data 256(4096) FLR NS
           link x4(x8) speed 8.0(8.0) ASPM disabled(L0s)
cap 03[d0]      = VPD
cap 05[a8]      = MSI supports 1 message, 64 bit, vector masks
cap 11[c0]      = MSI-X supports 97 messages, enabled
           Table in map 0x14[0xe000], PBA in map 0x14[0xf000]
ecap 0001[100] = AER 2 0 fatal 0 non-fatal 1 corrected
ecap 0019[1e0] = PCIe Sec 1 lane errors 0
ecap 0004[1c0] = Power Budgeting 1
ecap 0016[190] = DPA 1
ecap 000e[148] = ARI 1

```

# ACPI電源管理データ詳細一覧

```
RSD PTR: OEM=AMI, ACPI_Rev=2.0x (2)
XSDT=0x000000007d1e7080, Length=36, cksum=81
XSDT: Length=124, Revision=1, Checksum=5
OEMID=AMI, OEM Table ID=0707, OEM Revision=0x1072009,
Creator ID=AMI, Creator Revision=0x10013
Entries={ 0x000000007d1fd6c8, 0x000000007d1fd7d8, 0x000000007d1fd898, 0x000000007d1fd938, 0x000000007d1fd978, 0x000000007d1fd9b0, 0x000000007d1ff9c8, 0x000000007d200840,
0x000000007d200ba8, 0x000000007d200f68, 0x000000007d200fd8 }
FACP: Length=268, Revision=5, Checksum=42,
OEMID=AMI, OEM Table ID=0707, OEM Revision=0x1072009,
Creator ID=AMI, Creator Revision=0x10013
FACS=0x7daf8f80, DSDT=0x7d1e7188
INT_MODEL=APIC
Preferred_PM_Profile=Enterprise Server (4)
SCI_INT=9
SMI_CMD=0xb2, ACPI_ENABLE=0xa0, ACPI_DISABLE=0xa1, S4BIOS_REQ=0x0
PSTATE_CNT=0x0
PM1a_EVT_BLK=0x1800-0x1803
PM1a_CNT_BLK=0x1804-0x1805
PM2_CNT_BLK=0x1850-0x1850
PM_TMR_BLK=0x1808-0x180b
GPE0_BLK=0x1880-0x189f
P_LVL2_LAT=101 us, P_LVL3_LAT=1001 us
FLUSH_SIZE=1024, FLUSH_STRIDE=16
DUTY_OFFSET=0, DUTY_WIDTH=0
DAY_ALRM=13, MON_ALRM=0, CENTURY=50
IAPC_BOOT_ARCH={LEGACY_DEVICES,NO_ASPM}
Flags={WBINVD,C1_SUPPORTED,C2_MP_SUPPORTED,SLEEP_BUTTON,S4_RTC_WAKE,DOCKING_SUPPORTED,RESET_REGISTER,PLATFORM_CLOCK,S4_RTC_VALID,REMOTE_POWER_ON}
RESET_REG=0xfdae0510:0[8] (Memory), RESET_VALUE=0
FACS: Length=64, HwSig=0x86005d5e, Firm_Wake_Vec=0x00000000
Global_Lock=
Flags=
Version=2
DSDT: Length=91456, Revision=2, Checksum=242,
OEMID=AMI, OEM Table ID=0707, OEM Revision=0x1072009,
Creator ID=INTL, Creator Revision=0x20120913
APIC: Length=188, Revision=3, Checksum=82,
OEMID=AMI, OEM Table ID=0707, OEM Revision=0x1072009,
Creator ID=AMI, Creator Revision=0x10013
Local APIC ADDR=0xfec00000
Flags={PC-AT}
Type=Local APIC
ACPI CPU=1
Flags={ENABLED}
APIC ID=0
Type=Local APIC NMI
ACPI CPU=1
LINT Pin=1
Flags={Polarity=active-hi, Trigger=edge}
Type=Local APIC
ACPI CPU=2
Flags={ENABLED}
APIC ID=2
Type=Local APIC NMI
ACPI CPU=2
LINT Pin=1
Flags={Polarity=active-hi, Trigger=edge}
Type=Local APIC
ACPI CPU=3
Flags={ENABLED}
APIC ID=4
Type=Local APIC NMI
ACPI CPU=3
LINT Pin=1
Flags={Polarity=active-hi, Trigger=edge}
Type=Local APIC
ACPI CPU=4
Flags={ENABLED}
APIC ID=6
Type=Local APIC NMI
ACPI CPU=4
LINT Pin=1
Flags={Polarity=active-hi, Trigger=edge}
Type=Local APIC
ACPI CPU=5
Flags={ENABLED}
APIC ID=1
Type=Local APIC NMI
ACPI CPU=5
LINT Pin=1
Flags={Polarity=active-hi, Trigger=edge}
Type=Local APIC
ACPI CPU=6
Flags={ENABLED}
APIC ID=3
Type=Local APIC NMI
ACPI CPU=6
LINT Pin=1
Flags={Polarity=active-hi, Trigger=edge}
Type=Local APIC
ACPI CPU=7
Flags={ENABLED}
APIC ID=5
Type=Local APIC NMI
ACPI CPU=7
LINT Pin=1
Flags={Polarity=active-hi, Trigger=edge}
Type=Local APIC
ACPI CPU=8
Flags={ENABLED}
APIC ID=7
Type=Local APIC NMI
ACPI CPU=8
LINT Pin=1
Flags={Polarity=active-hi, Trigger=edge}
Type=IO APIC
APIC ID=2
INT BASE=0
ADDR=0x00000000fec00000
Type=INT Override
BUS=0
IRQ=0
INTR=2
Flags={Polarity=conforming, Trigger=conforming}
Type=INT Override
```



```

BUS=0
IRQ=9
INTR=9
Flags={Polarity=active-hi, Trigger=level}
FIDT: Length=156, Revision=1, Checksum=75,
OEMID=AMI, OEM Table ID=0707, OEM Revision=0x1072009,
Creator ID=AMI, Creator Revision=0x10013
MCFG: Length=60, Revision=1, Checksum=64,
OEMID=AMI, OEM Table ID=0707, OEM Revision=0x1072009,
Creator ID=MSFT, Creator Revision=0x97
Base Address=0x00000000e0000000
Segment Group=0x0000
Start Bus=0
End Bus=255
HPET: Length=56, Revision=1, Checksum=234,
OEMID=AMI, OEM Table ID=0707, OEM Revision=0x1072009,
Creator ID=AMI., Creator Revision=0x5000b
HPET Number=0
ADDR=0x00000000fed00000:0[64] (Memory) HW Rev=0x1
Comparators=7
Counter Size=1
Legacy IRQ routing capable={TRUE}
PCI Vendor ID=0x8086
Minimal Tick=14318
Flags=0x00
SSDT: Length=8210, Revision=2, Checksum=145,
OEMID=SaSsdT, OEM Table ID=SaSsdT, OEM Revision=0x3000,
Creator ID=INTEL, Creator Revision=0x20120913
SSDT: Length=3699, Revision=2, Checksum=39,
OEMID=CpuRef, OEM Table ID=CpuSsdT, OEM Revision=0x3000,
Creator ID=INTEL, Creator Revision=0x20120913
SSDT: Length=869, Revision=2, Checksum=252,
OEMID=PmRef, OEM Table ID=Cpu0Tst, OEM Revision=0x3000,
Creator ID=INTEL, Creator Revision=0x20120913
SSDT: Length=959, Revision=2, Checksum=69,
OEMID=PmRef, OEM Table ID=ApTst, OEM Revision=0x3000,
Creator ID=INTEL, Creator Revision=0x20120913
DMAR: Length=112, Revision=1, Checksum=164,
OEMID=INTEL, OEM Table ID=SKL, OEM Revision=0x1,
Creator ID=INTEL, Creator Revision=0x1
Host Address Width=39
Flags={INTR_REMAP,X2APIC_OPT_OUT}
Type=DRHD
Length=32
Flags={INCLUDE_ALL}
Segment=0
Address=0x00000000fed90000
    Type=IOAPIC
    Length=8
    EnumerationId=2
    StartBusNumber=240
    Path={31:0}
    Type=HPET
    Length=8
    EnumerationId=0
    StartBusNumber=0
    Path={31:0}
Type=RMRR
Length=32
Segment=0
BaseAddress=0x000000007cf5b000
LimitAddress=0x000000007cf7afff
    Type=PCI Endpoint Device
    Length=8
    EnumerationId=0
    StartBusNumber=0
    Path={20:0}
BERT: Length=48, Revision=1, Checksum=29,
OEMID=AMI, OEM Table ID=AMI.BERT, OEM Revision=0x0,
Creator ID=AMI., Creator Revision=0x0
If (PSME == One)
DerefOf (SPLX [One]) [Zero] = PDT1 /* \PDT1 */
DerefOf (SPLX [One]) [One] = PLM1 /* \PLM1 */
DerefOf (SPLX [One]) [0x02] = PTW1 /* \PTW1 */
DerefOf (SPLX [0x02]) [Zero] = PDT2 /* \PDT2 */
DerefOf (SPLX [0x02]) [One] = PLM2 /* \PLM2 */
DerefOf (SPLX [0x02]) [0x02] = PTW2 /* \PTW2 */
DerefOf (DPLX [One]) [Zero] = DDT1 /* \DDT1 */
DerefOf (DPLX [One]) [One] = DDP1 /* \DDP1 */
    = DLI1 /* \DLI1 */
    = DPL1 /* \DPL1 */
    = DTW1 /* \DTW1 */
    = DMI1 /* \DMI1 */
    = DMA1 /* \DMA1 */
    = DMT1 /* \DMT1 */
DerefOf (DPLX [0x02]) [Zero] = DDT2 /* \DDT2 */
DerefOf (DPLX [0x02]) [One] = DDP2 /* \DDP2 */
    = DLI2 /* \DLI2 */
    = DPL2 /* \DPL2 */
    = DTW2 /* \DTW2 */
    = DMI2 /* \DMI2 */
    = DMA2 /* \DMA2 */
    = DMT2 /* \DMT2 */
If (EP_B == Zero)
    EP_B = (EPBR << 0x0C)
If (MH_B == Zero)
    MH_B = (MHR << 0x0F)
If (PC_B == Zero)
    PC_B = (PXR << 0x1A)
If (PC_L == Zero)
    PC_L = (0x10000000 >> PXSZ) /* \SB..PCI0.PXSZ */
If (DM_B == Zero)
    DM_B = (DIR << 0x0C)
Local0 = GPCL ()
PBMX = ((Local0 >> 0x14) - 0x02)
PBLN = ((Local0 >> 0x14) - One)
C0LN = Zero
If (PM1L == One)
    C0RN = Zero
    C4LN = Zero
If (PM1H == One)
    C4RN = Zero
    C8LN = Zero
If (PM2L == One)
    C8RN = Zero
    CCLN = Zero
If (PM2H == One)
    CCRN = Zero
    D0LN = Zero
If (PM3L == One)

```









```

Local0 = GGRP (Arg0)
Local1 = GNMB (Arg0)
Local0 = GGRP (Arg0)
Local1 = GNMB (Arg0)
Local2 = (GADR (Local0, PCFG) + (Local1 * 0x08))
If (RCFG != 0x02)
    Local3 = DereFOF (RXEV [Local0])
    Local3 &= ~(0x03 << (Local1 * 0x02))
    RXEV [Local0] = (Local3 | (RCFG << (Local1 * 0x02))
    RCFG = 0x02
    RDIS = One
Local0 = GGRP (Arg0)
Local1 = GNMB (Arg0)
Local2 = (GADR (Local0, PCFG) + (Local1 * 0x08))
Local3 = ((DereFOF (RXEV [Local0]) >> (Local1 * 0x02)
If (Local3 != 0x02)
    RDIS = Zero
    RCFG = Local3
Return ((D000 != 0xFFFFFFFF))
Local0 = 0xFE200000
Local0 += (0x40 * (0x80 * (Arg0 - 0x20)))
Local0 += (0x40 * Arg1)
Local0 = Zero
Local1 = Zero
    Local2 = DereFOF (Local3 [(Arg1 + Local1)])
    Local0 += (Local2 << (0x08 * Local1))
Local0 = SizeOf (Arg0)
Local1 = THDA (0x20, 0x16)
D010 = 0x01000242
W000 = Local0
Local6 = Zero
Local7 = Local0
While (Local7 >= 0x08)
    Q000 = STRD (Arg0, Local6, 0x08)
    Local6 += 0x08
    Local7 -= 0x08
If (Local7 >= 0x04)
    D000 = STRD (Arg0, Local6, 0x04)
    Local6 += 0x04
    Local7 -= 0x04
If (Local7 >= 0x02)
    W000 = STRD (Arg0, Local6, 0x02)
    Local6 += 0x02
    Local7 -= 0x02
If (Local7 >= One)
    B000 = STRD (Arg0, Local6, One)
    Local6 += One
    Local7 -= One
D030 = Zero
If (PCHS == One)
If (PCHS == 0x02)
    Local0 = ResourceTemplate ()
    AMI0 = ITA0 /* \ITA0 */
    AMA0 = ITA0 /* \ITA0 */
    AMI1 = ITA1 /* \ITA1 */
    AMA1 = ITA1 /* \ITA1 */
    AMI2 = ITA2 /* \ITA2 */
    AMA2 = ITA2 /* \ITA2 */
    AMI3 = ITA3 /* \ITA3 */
    AMA3 = ITA3 /* \ITA3 */
    If (ITS0 == One)
        Local0 = Local1
    If (ITS1 == One)
        Local0 = Local1
    If (ITS2 == One)
        Local0 = Local1
    If (ITS3 == One)
        Local0 = Local1
    I2CE = Zero
    HSTS = 0xBF
    TXSA = Arg0
    HCOM = Arg1
    HCON = 0x48
    HSTS |= 0xFF
    I2CE = Zero
    HSTS = 0xBF
    TXSA = (Arg0 | One)
    HCON = 0x44
    HSTS |= 0xFF
    I2CE = Zero
    HSTS = 0xBF
    TXSA = Arg0
    HCOM = Arg1
    DAT0 = Arg2
    HCON = 0x48
    HSTS |= 0xFF
    I2CE = Zero
    HSTS = 0xBF
    TXSA = (Arg0 | One)
    HCOM = Arg1
    HCON = 0x48
    HSTS |= 0xFF
    I2CE = Zero
    HSTS = 0xBF
    TXSA = Arg0
    HCOM = Arg1
    DAT1 = (Arg2 & 0xFF)
    DAT0 = ((Arg2 >> 0x08) & 0xFF)
    HCON = 0x4C
    HSTS |= 0xFF
    I2CE = Zero
    HSTS = 0xBF
    TXSA = (Arg0 | One)
    HCOM = Arg1
    HCON = 0x4C
    HSTS |= 0xFF
    I2CE = Arg3
    HSTS = 0xBF
    TXSA = Arg0
    HCOM = Arg1
    DAT0 = SizeOf (Arg2)
    Local1 = Zero
    HBDR = DereFOF (Arg2 [Zero])
    HCON = 0x54
    Local0 = 0x0FA0
    HSTS = 0x80
    HBDR = DereFOF (Arg2 [Local1])
    HSTS |= 0xFF
    I2CE = Arg2
    HSTS = 0xBF

```

```

TXSA = (Arg0 | One)
HCON = Arg1
HCON = 0x54
Local0 = 0x0FA0
TBUF [Zero] = DAT0 /* \_SB_.PCI0.SBUS.DAT0 */
HSTS = 0x80
Local1 = One
Local0 = 0x0FA0
TBUF [Local1] = HBDR /* \_SB_.PCI0.SBUS.HBDR */
HSTS = 0x80
HSTS |= 0xFF
Local0 = 0xC8
If (Local0 == Zero)
Local0 = Zero
Local0 = 0x0FA0
If (Local0 == Zero)
Local0 = 0x0FA0
If (Local0 == Zero)
HCON |= 0x02
HSTS |= 0xFF
PARC |= 0x80
IRQ0 = Zero
IRQ0 = (One << (PARC & 0x0F))
PARC = Local0
PBRC |= 0x80
IRQ0 = Zero
IRQ0 = (One << (PBRC & 0x0F))
PBRC = Local0
PCRC |= 0x80
IRQ0 = Zero
IRQ0 = (One << (PCRC & 0x0F))
PCRC = Local0
PDRC |= 0x80
IRQ0 = Zero
IRQ0 = (One << (PDRC & 0x0F))
PDRC = Local0
PERC |= 0x80
IRQ0 = Zero
IRQ0 = (One << (PERC & 0x0F))
PERC = Local0
PFRC |= 0x80
IRQ0 = Zero
IRQ0 = (One << (PFRC & 0x0F))
PFRC = Local0
PGRC |= 0x80
IRQ0 = Zero
IRQ0 = (One << (PGRC & 0x0F))
PGRC = Local0
PHRC |= 0x80
IRQ0 = Zero
IRQ0 = (One << (PHRC & 0x0F))
PHRC = Local0
Local0 = ((Arg0 << 0x10) + Arg1)
Local0 += SBRG
Local0 = ((Arg0 << 0x10) + Arg1)
Local0 += SBRG
DAT0 = Arg2
Local1 = DAT1 /* \PCRW.DAT1 */
Local0 = PCRR (Arg0, Arg1)
Local1 = (Local0 | Arg2)
Local0 = PCRR (Arg0, Arg1)
Local1 = (Local0 & Arg2)
Local0 = PCRR (Arg0, Arg1)
Local1 = ((Local0 & Arg2) | Arg3)
If (PMBV == Zero)
PMBV = (\_SB_.PCI0.PPMC.ACBA << 0x08)
If (PWRV == Zero)
PWRV = (\_SB_.PCI0.PPMC.PWBA << 0x0C)
If (TCBV == Zero)
Local0 = PCRR (0xEF, 0x2778)
TCBV = (Local0 & 0xFFE0)
PMEE = Arg0
If (DVID == 0xFFFF)
PMES = One
If (Arg0 == ToUUID ("ac340cb7-e901-45bf-b7e6-2b34ec931e23"))
If (Arg1 == 0x03)
XFLT = Arg1
If (XFLT == Zero)
PMEE = Arg0
Local0 = (XICM & 0xFFFFFFFFFFFF0000)
Local0 += 0x8400
If (DVID == 0xFFFF)
Local0 = PMES /* \_SB_.PCI0.XHC_.PMES */
PMES = One
If (PCHV () == SPTH)
If (PCHV () == SPTH)
If (DVID == 0xFFFF)
Local2 = MEMB /* \_SB_.PCI0.XHC_.MEMB */
Local1 = PDBM /* \_SB_.PCI0.XHC_.PDBM */
PDBM &= 0xFFFFFFFFFFFFF9
Local3 = D0D3 /* \_SB_.PCI0.XHC_.D0D3 */
D0D3 = Zero
MEMB = XWMB /* \XWMB */
PDBM = (Local1 | 0x02)
UPSW = Zero
PDBM &= 0xFFFFFFFFFFFFFD
MEMB = Local2
PDBM = Local1
If (UNAB && ((D0D3 == Zero) || (^XDCI.D0I3 == Zero)))
MPMC = One
If (DVID == 0xFFFF)
Local2 = MEMB /* \_SB_.PCI0.XHC_.MEMB */
Local1 = PDBM /* \_SB_.PCI0.XHC_.PDBM */
PDBM &= 0xFFFFFFFFFFFFF9
D0D3 = Zero
MEMB = XWMB /* \XWMB */
PDBM = (Local1 | 0x02)
UPSW = 0x03
PDBM &= 0xFFFFFFFFFFFFFD
D0D3 = 0x03
MEMB = Local2
PDBM = Local1
If (UNAB && ((D0D3 == 0x03) && (^XDCI.D0I3 == 0x03) || (^XDCI.DVID == 0xFFFF)))
MPMC = 0x03
If (Arg0 == ToUUID ("7c9512a9-1705-4cb4-af7d-506a2423ab71"))
If (DVID == 0xFFFF)
If (DVID == 0xFFFF)
If (DVID == 0xFFFF)
If (Arg0 == ToUUID ("732b85d5-b7a7-4a1b-9ba0-4bbd00ffd511"))

```

```

If (Arg1 == One)
    Local1 = Arg0
    Local2 = Arg1
    If (Local1 == Zero)
        UXPE = Zero
        PURC = Zero
        Local0 = Zero
        PUPS = Zero
        Local0 = Zero
        If ((U2CP == Zero) && (U3CP == Zero))
            If (U2CP != Zero) {}
            If (U3CP != Zero) {}
        If (Local1 == 0x03)
            If (U2CP != Zero) {}
            If (U3CP != Zero) {}
            PUPS = 0x03
            Local0 = Zero
            If ((U2CP == 0x03) && (U3CP == 0x03))
                If (U2CP != 0x03) {}
                If (U3CP != 0x03) {}
                PURC = One
                UXPE = Local2
            _T_0 = Arg2
            If (_T_0 == Zero)
                ElseIf (_T_0 == One)
                ElseIf (_T_0 == 0x04)
                Local1 = DereFof (Arg3 [Zero])
                ElseIf (_T_0 == 0x05)
                ElseIf (_T_0 == 0x06)
                If (OTHC == Zero)
                    CSFR = One
                    Local0 = Zero
                    If (CSFR == Zero)
                ElseIf (_T_0 == 0x07)
                    Local0 = P2PS /* \_SB_.PCI0.XDCI.DSM.P2PS */
If (DVID != 0xFFFFFFFF)
    PME = Arg0
    If (DVID == 0xFFFF)
        Local0 = PMES /* \_SB_.PCI0.XDCI.PMES */
    PMES = One
    GRGC = Arg0
    _T_0 = ToInteger (Arg4)
    If (_T_0 == Zero)
        ElseIf (_T_0 == 0x02)
            CAIR = Arg1
        ElseIf (_T_0 == One)
            Local0 = (Arg2 & RPCD) /* \_SB_.PCI0.SAT0.RDCA.RPCD */
            Local0 != Arg3
            RPCD = Local0
        ElseIf (_T_0 == 0x03)
            CAIR = Arg1
            Local0 = (Arg2 & CADR) /* \_SB_.PCI0.SAT0.RDCA.CADR */
            Local0 != Arg3
            CADR = Local0
    If (NITV == Zero)
    If (NLI1 != Zero)
    If (NLRV != Zero)
    If ((NEAV & 0x40) == 0x40)
        While ((RDCA (NCRN, 0x52, Zero, Zero, Zero) & 0x2000) == Zero)
    If (NMV != Zero)
    If (NPV != Zero)
    If (NITV == Zero)
    PCMD = RDCA (NCRN, 0x04, Zero, Zero, 0x02)
    If (NITV == One)
        PRBI = 0x24
        PRBD = RDCA (NCRN, 0x24, Zero, Zero, 0x02)
    ElseIf (NITV == 0x02)
        PRBI = 0x10
        PRBD = RDCA (NCRN, 0x10, Zero, Zero, 0x02)
    If (NITV == Zero)
        Local0 = RDCA (NCRN, (NPMV + 0x04), Zero, Zero, 0x02)
        If ((Local0 & 0x08) == Zero)
            NITV = NIT1 /* \NIT1 */
            NPMV = NPM1 /* \NPM1 */
            NPCV = NPC1 /* \NPC1 */
            NLI1 = NLI1 /* \NLI1 */
            ND2V = ND21 /* \ND21 */
            ND1V = ND11 /* \ND11 */
            NLRV = NLR1 /* \NLR1 */
            NLDV = NLD1 /* \NLD1 */
            NEAV = NEA1 /* \NEA1 */
            NEBV = NEB1 /* \NEB1 */
            NECV = NEC1 /* \NEC1 */
            NRAV = NRA1 /* \NRA1 */
            NMBV = NMB1 /* \NMB1 */
            NMV = NMV1 /* \NMV1 */
            NPBV = NPB1 /* \NPB1 */
            NPV = NPV1 /* \NPV1 */
            NCRN = Zero
            If (NITV == Zero)
            If (NLI1 != Zero)
            If (NLRV != Zero)
            If ((NEAV & 0x40) == 0x40)
                While ((RDCA (NCRN, 0x52, Zero, Zero, Zero) & 0x2000) == Zero)
            If (NMV != Zero)
            If (NPV != Zero)
            If (NITV == Zero)
            PCMD = RDCA (NCRN, 0x04, Zero, Zero, 0x02)
            If (NITV == One)
                PRBI = 0x24
                PRBD = RDCA (NCRN, 0x24, Zero, Zero, 0x02)
            ElseIf (NITV == 0x02)
                PRBI = 0x10
                PRBD = RDCA (NCRN, 0x10, Zero, Zero, 0x02)
            If (NITV == Zero)
                Local0 = RDCA (NCRN, (NPMV + 0x04), Zero, Zero, 0x02)
                If ((Local0 & 0x08) == Zero)
                    NITV = NIT2 /* \NIT2 */
                    NPMV = NPM2 /* \NPM2 */
                    NPCV = NPC2 /* \NPC2 */
                    NLI1 = NLI2 /* \NLI2 */
                    ND2V = ND22 /* \ND22 */
                    ND1V = ND12 /* \ND12 */
                    NLRV = NLR2 /* \NLR2 */
                    NLDV = NLD2 /* \NLD2 */
                    NEAV = NEA2 /* \NEA2 */
                    NEBV = NEB2 /* \NEB2 */
                    NECV = NEC2 /* \NEC2 */
                    NRAV = NRA2 /* \NRA2 */
                    NMBV = NMB2 /* \NMB2 */

```





```

IF ((I25C == 0x02) || (I25C == 0x03))
    _HID = "INT343A"
    _CID = "INT343A"
    CADR = 0x1C
If (I25C == 0x04)
    _HID = "INT343B"
    _CID = "INT343B"
    CADR = 0x34
ADR = CADR /* \_SB\_PCI0.I2C0.HDAC.CADR */
AINT = INUM (0x02040016)
If (BID == 0x20)
If (I25C != Zero)
INT1 = GNUM (GPLI)
INT2 = INUM (GPLI)
If (SDM1 == Zero)
If (SDS1 == One)
    _HID = "ATML3432"
    HID2 = Zero
    BADR = 0x4C
    SPED = 0x00061A80
If (SDS1 == 0x02)
    _HID = "ATML2952"
    HID2 = Zero
    BADR = 0x4A
    SPED = 0x00061A80
If (SDS1 == 0x03)
    _HID = "ELAN2097"
    HID2 = One
    BADR = 0x10
    SPED = 0x00061A80
If (SDS1 == 0x04)
    _HID = "NTRG0001"
    HID2 = One
    BADR = 0x07
    SPED = 0x00061A80
If (SDS1 == 0x05)
    _HID = "NTRG0002"
    HID2 = One
    BADR = 0x64
    SPED = 0x00061A80
If (SDS1 == 0x06)
    _HID = "CUST0000"
    HID2 = TPLH /* \TPLH */
    BADR = TPLB /* \TPLB */
    If (TPLS == Zero)
        SPED = 0x000186A0
    If (TPLS == One)
        SPED = 0x00061A80
    If (TPLS == 0x02)
        SPED = 0x000F4240
If (Arg0 == HIDG)
If (Arg0 == TP7G)
If (SDS1 != Zero)
If (SDM1 == Zero)
If (BID == 0x20)
If (SDS7 == One)
If (SDS7 == 0x02)
If (SDS7 == 0x03)
If (SDS7 == 0x04)
If (SDS7 == 0x05)
If (SDS7 == 0x06)
If (SDS7 != Zero)
SPIN = GNUM (GFPS)
GPIN = GNUM (GFPI)
IPIN = INUM (GFPI)
UPIN = GNUM (GFPI)
If ((SDS7 == 0x02) || (SDS7 == 0x06))
    ILVL = Zero
    ITRG = One
    GLVL = Zero
    GTRG = One
If (SDS7 == 0x04)
    ILVL = Zero
    ITRG = One
    _T_0 = SDS7 /* \SDS7 */
    If (_T_0 == One)
        SPEX = 0x00989680
        PHAX = Zero
    ElseIf (_T_0 == 0x02)
        SPEX = 0x002DC6C0
        PHAX = Zero
    ElseIf (_T_0 == 0x03)
        SPEX = 0x007A1200
        PHAX = One
    ElseIf (_T_0 == 0x04)
        SPEX = 0x007A1200
        PHAX = Zero
    ElseIf (_T_0 == 0x05)
        SPEX = 0x00F42400
        PHAX = Zero
    ElseIf (_T_0 == 0x06)
        SPEX = 0x002DC6C0
        PHAX = Zero
If (SDS7 == One)
If ((SDS7 == 0x04) && (SDM7 == Zero))
If ((SDS7 == 0x04) && (SDM7 != Zero))
If (SDM7 == Zero)
If (SDS8 == One)
If (SDS8 == 0x02)
INT3 = GNUM (GBTI)
WAK3 = GNUM (GBTW)
KIL3 = GNUM (GBTK)
INT4 = INUM (GBTI)
WAK4 = GNUM (GBTW)
KIL4 = GNUM (GBTK)
If (SDM8 == Zero)
If (SDS8 != Zero)
RPIN = GNUM (GGNR)
If (GNSC == One)
If (GNSC == Zero)
Local0 = Zero
    If (R3DC (Zero, Local0, Zero) == 0x02)
    If (R3DC (One, Local0, Zero) == 0x02)
        Local0 += One
    = Arg3
Local1 = (Timer + 0xC350)
RST = 0x07
ENB = Zero
Local0 = RBCK /* \_SB\_PCI0.GEXP.RBCK */
Local0 = CLR /* \_SB\_PCI0.GEXP.CLR_ */

```

```

HOLD = 0x001C001C
HCNT = 0x0210
LCNT = 0x0280
TAR = (EXPA + Arg1)
ICON = 0x65
ENB = One
While (ENSB != One)
DATA = Arg2
DATA = (END + Arg3)
While (ACTV != Zero)
ENB = Zero
While (ENSB != Zero)
If (R3DC (Arg1, Arg2, Zero) == One)
If (UCCH == One)
If (R3DC (Arg1, Arg2, Zero) == 0x02)
Local1 = (Timer + 0xC350)
RST = 0x07
ENB = Zero
Local0 = RBCK /* \_SB\_PCI0.GEXP.RBCK */
Local0 = CLR /* \_SB\_PCI0.GEXP.CLR */
HOLD = 0x001C001C
HCNT = 0x0210
LCNT = 0x0280
TAR = (EXPA + Arg1)
ICON = 0x65
ENB = One
While (ENSB != One)
DATA = Arg2
DATA = (END + READ)
While (ACTV != Zero)
Local0 = DATA /* \_SB\_PCI0.GEXP.DATA */
ENB = Zero
While (ENSB != Zero)
D0D3 = Zero
Local0 = D0D3 /* \_SB\_PCI0.GEXP.PS0_.D0D3 */
D0D3 = 0x03
Local0 = D0D3 /* \_SB\_PCI0.GEXP.PS3_.D0D3 */
If (Arg0 == Zero)
SB1X = SB10 /* \SB10 */
SB0X = SB00 /* \SB00 */
SMDX = SMD0 /* \SMD0 */
If (Arg0 == One)
SB1X = SB11 /* \SB11 */
SB0X = SB01 /* \SB01 */
SMDX = SMD1 /* \SMD1 */
If (Arg0 == 0x02)
SB1X = SB12 /* \SB12 */
SB0X = SB02 /* \SB02 */
SMDX = SMD2 /* \SMD2 */
If (Arg0 == 0x03)
SB1X = SB13 /* \SB13 */
SB0X = SB03 /* \SB03 */
SMDX = SMD3 /* \SMD3 */
If (Arg0 == 0x04)
SB1X = SB14 /* \SB14 */
SB0X = SB04 /* \SB04 */
SMDX = SMD4 /* \SMD4 */
If (Arg0 == 0x05)
SB1X = SB15 /* \SB15 */
SB0X = SB05 /* \SB05 */
SMDX = SMD5 /* \SMD5 */
If (SMDX != 0x03)
If (Arg4 == Zero)
Local0 = OUTR /* \_SB\_PCI0.GEXP.UTR */
If (Arg4 == One)
Local0 = CFGR /* \_SB\_PCI0.GEXP.CFGR */
If (Arg4 == 0x02)
Local0 = INR /* \_SB\_PCI0.GEXP.INR */
If (Arg4 == 0x03)
Local0 = POLR /* \_SB\_PCI0.GEXP.POLR */
REGA = DereFOF (Local0 [REGN])
OLDV = RREG (SB0X, Arg1, REGA)
If (Arg4 == 0x02)
RETV = (One & (OLDV >> PINN))
NEWV = (OLDV & ~(One << PINN))
NEWV |= (Arg3 << PINN) /* \_SB\_PCI0.GEXP.CSER.NEWV */
If (NEWV != OLDV)
PKG [Zero] = Arg0
PKG [Zero] = Arg0
PKG [One] = Arg1
PKG [0x02] = Arg2
If (PCHV C) == SPTL)
PGEN = Zero
PSTA &= 0xFFFFFFFF
Local0 = PSTA /* \_SB\_PCI0.PEMC.PSTA */
PGEN = One
PSTA |= 0x03
Local0 = PSTA /* \_SB\_PCI0.PEMC.PSTA */
ADBG (Concatenate ("EMH4-", ToDecimalString (EMH4)))
If (Arg0 == ToUUID ("f6c13ea5-65cd-461f-ab7a-29f7e8d5bd61"))
If (Arg1 == Zero)
_T_0 = Arg2
If (_T_0 == Zero)
If (EMH4 == One)
ElseIf (_T_0 == 0x05)
ElseIf (_T_0 == 0x06)
PGEN = Zero
PSTA &= 0xFFFFFFFF
Local0 = PSTA /* \_SB\_PCI0.PSDC.PSTA */
PGEN = One
PSTA |= 0x03
Local0 = PSTA /* \_SB\_PCI0.PSDC.PSTA */
If (Arg0 == ToUUID ("f6c13ea5-65cd-461f-ab7a-29f7e8d5bd61"))
If (Arg1 == Zero)
_T_0 = Arg2
If (_T_0 == Zero)
ElseIf (_T_0 == 0x03)
ElseIf (_T_0 == 0x04)
If (CHEN == One)
If (Arg0 == ToUUID ("1730e71d-e5dd-4a34-be57-4d76b6a2fe37"))
If (Zero == ToInteger (Arg1))
_T_0 = ToInteger (Arg2)
If (_T_0 == Zero)
ElseIf (_T_0 == One)
_T_1 = DereFOF (Arg3 [Zero])
If (_T_1 == Zero) {}
If (Arg0 == Zero)
P80D = ((P80D & 0xFFFFFFFF00) | Arg1)
If (Arg0 == One)
P80D = ((P80D & 0xFFFF00FF) | (Arg1 << 0x08))

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If (Arg0 == 0x02)
    P800 = ((P800 & 0xFF00FFFF) | (Arg1 << 0x10))
If (Arg0 == 0x03)
    P800 = ((P800 & 0x00FFFFFF) | (Arg1 << 0x18))
P80H = P800 /* \P80D */
GPIC = Arg0
PICM = Arg0
P80D = Zero
ADBG (Concatenate ("_PTS=", ToHexString (Arg0)))
If (Arg0 == 0x03)
If ((Arg0 == 0x03) || (Arg0 == 0x04) || (Arg0 == 0x05))
    If (PFLV == 0x02)
If (S0ID == One) {}
If ((Arg0 == 0x03) || (Arg0 == 0x04))
    If (\_SB.PCI0.RP01.VDID != 0xFFFFFFFF)
    If (\_SB.PCI0.RP02.VDID != 0xFFFFFFFF)
    If (\_SB.PCI0.RP03.VDID != 0xFFFFFFFF)
    If (\_SB.PCI0.RP04.VDID != 0xFFFFFFFF)
    If (\_SB.PCI0.RP05.VDID != 0xFFFFFFFF)
    If (\_SB.PCI0.RP06.VDID != 0xFFFFFFFF)
    If (\_SB.PCI0.RP07.VDID != 0xFFFFFFFF)
        If (DSTS == Zero)
    If (\_SB.PCI0.RP08.VDID != 0xFFFFFFFF)
        If (DSTS == Zero)
    If (\_SB.PCI0.RP09.VDID != 0xFFFFFFFF)
    If (\_SB.PCI0.RP10.VDID != 0xFFFFFFFF)
    If (\_SB.PCI0.RP11.VDID != 0xFFFFFFFF)
    If (\_SB.PCI0.RP12.VDID != 0xFFFFFFFF)
    If (\_SB.PCI0.RP13.VDID != 0xFFFFFFFF)
    If (\_SB.PCI0.RP14.VDID != 0xFFFFFFFF)
    If (\_SB.PCI0.RP15.VDID != 0xFFFFFFFF)
    If (\_SB.PCI0.RP16.VDID != 0xFFFFFFFF)
    If (\_SB.PCI0.RP17.VDID != 0xFFFFFFFF)
    If (\_SB.PCI0.RP18.VDID != 0xFFFFFFFF)
    If (\_SB.PCI0.RP19.VDID != 0xFFFFFFFF)
    If (\_SB.PCI0.RP20.VDID != 0xFFFFFFFF)
If ((Arg0 == 0x03) || (Arg0 == 0x04))
    If (\_SB.PWRB.PBST == One)
        PBSS = One
Local0 = (Arg0 * 0x08)
Local1 = (Arg1 * 0x08)
If (ECON == One) {}
If (CSEM == One)
    CSEM = One
PLSV = PPL1 /* \PPL1 */
PLEN = PL1E /* \PL1E */
CLMP = CLP1 /* \CLP1 */
If (PWRU == Zero)
    PPUU = One
    PPUU = (PWRU-- << 0x02)
Local0 = (PVL * PPUU) /* \SPL1.PPUU */
Local1 = (Local0 / 0x03E8)
PPL1 = Local1
PL1E = One
CLP1 = One
PPL1 = PLSV /* \PLSV */
PL1E = PLEN /* \PLEN */
CLP1 = CLMP /* \CLMP */
CSEM = Zero
If (OSYS == 0x07DC)
    SMIF = Arg1
If (Arg0 == 0x02)
    \_PR.DTSF = Arg1
    \_PR.TRPD = Zero
If (Arg0 == 0x03)
    TRPH = Zero
If (Arg0 == 0x04)
    \_PR.TRPF = Zero
    OSYS = 0x07D0
        OSYS = 0x03E8
        OSYS = 0x07D1
        OSYS = 0x07D1
        OSYS = 0x07D2
        OSYS = 0x07D3
        OSYS = 0x07D6
        OSYS = 0x07D9
        OSYS = 0x07DC
        OSYS = 0x07DD
        OSYS = 0x07DF
        If (\_PR.DTSE >= One)
            \_PR.DSAE = One
^RP01.HPEX = Zero
^RP02.HPEX = Zero
^RP03.HPEX = Zero
^RP04.HPEX = Zero
^RP05.HPEX = Zero
^RP06.HPEX = Zero
^RP07.HPEX = Zero
^RP08.HPEX = Zero
^RP09.HPEX = Zero
^RP10.HPEX = Zero
^RP11.HPEX = Zero
^RP12.HPEX = Zero
^RP13.HPEX = Zero
^RP14.HPEX = Zero
^RP15.HPEX = Zero
^RP16.HPEX = Zero
^RP17.HPEX = Zero
^RP18.HPEX = Zero
^RP19.HPEX = Zero
^RP20.HPEX = Zero
^RP01.HPSX = One
^RP02.HPSX = One
^RP03.HPSX = One
^RP04.HPSX = One
^RP05.HPSX = One
^RP06.HPSX = One
^RP07.HPSX = One
^RP08.HPSX = One
^RP09.HPSX = One
^RP10.HPSX = One
^RP11.HPSX = One
^RP12.HPSX = One
^RP13.HPSX = One
^RP14.HPSX = One
^RP15.HPSX = One
^RP16.HPSX = One
^RP17.HPSX = One
^RP18.HPSX = One
^RP19.HPSX = One

```

```

^RP20.PHSX = One
^RP01.PMEX = Zero
^RP02.PMEX = Zero
^RP03.PMEX = Zero
^RP04.PMEX = Zero
^RP05.PMEX = Zero
^RP06.PMEX = Zero
^RP07.PMEX = Zero
^RP08.PMEX = Zero
^RP09.PMEX = Zero
^RP10.PMEX = Zero
^RP11.PMEX = Zero
^RP12.PMEX = Zero
^RP13.PMEX = Zero
^RP14.PMEX = Zero
^RP15.PMEX = Zero
^RP16.PMEX = Zero
^RP17.PMEX = Zero
^RP18.PMEX = Zero
^RP19.PMEX = Zero
^RP20.PMEX = Zero
^RP01.PMSX = One
^RP02.PMSX = One
^RP03.PMSX = One
^RP04.PMSX = One
^RP05.PMSX = One
^RP06.PMSX = One
^RP07.PMSX = One
^RP08.PMSX = One
^RP09.PMSX = One
^RP10.PMSX = One
^RP11.PMSX = One
^RP12.PMSX = One
^RP13.PMSX = One
^RP14.PMSX = One
^RP15.PMSX = One
^RP16.PMSX = One
^RP17.PMSX = One
^RP18.PMSX = One
^RP19.PMSX = One
^RP20.PMSX = One
PRWP [Zero] = Arg0
Local0 = (SS1 << 0ne)
Local0 |= (SS2 << 0x02)
Local0 |= (SS3 << 0x03)
Local0 |= (SS4 << 0x04)
PRWP [One] = Arg1
Local0 >>= One
If (S0ID == 0ne)
    SCGE = 0ne
If (Arg0 == ToUUID ("ed855e0c-6c90-47bf-a62a-26de0fc5ad5c"))
    If (OSYS >= 0x07D9)
If (Arg0 == ToUUID ("0811b06e-4a27-44f9-8d60-3cbbc22e7b48")) /* Platform-wide Capabilities */
If (Arg0 == ToUUID ("0811b06e-4a27-44f9-8d60-3cbbc22e7b48")) /* Platform-wide Capabilities */
    If (Arg1 == 0ne)
        OSCP = CAP0 /* \_SB\_OSC.CAP0 */
        OSC0 = 0x04
        If ((SGMD & 0x0F) != 0x02)
            If (RTD3 == Zero)
                CAP0 &= 0x3B
                STS0 |= 0x10
        STS0 &= 0xFFFFF00
        STS0 |= 0x0A
If (Arg0 == ToUUID ("ed855e0c-6c90-47bf-a62a-26de0fc5ad5c"))
    STS0 &= 0xFFFFF00
    STS0 |= 0x06
    If (OSYS >= 0x07D9)
        EMIN = \_PR.EMNA
        ELEN = \_PR.ELNG
        EMAX = ((\_PR.EMNA + \_PR.ELNG) - 0ne)
        If (Arg0 == ToUUID ("c60fac0c-fd9e-4273-9a3f-938f2327035e"))
            If (Zero == ToInteger (Arg1))
                \_T_0 = ToInteger (Arg2)
                If (\_T_0 == Zero)
                    ElseIf (\_T_0 == 0ne)
                        BUF0 [Zero] = SEC0 /* \SEC0 */
                        BUF0 [One] = SEC1 /* \SEC1 */
                        BUF0 [0x02] = SEC2 /* \SEC2 */
                        BUF0 [0x03] = SEC3 /* \SEC3 */
                        BUF0 [0x04] = SEC4 /* \SEC4 */
                        BUF0 [0x05] = SEC5 /* \SEC5 */
                        BUF0 [0x06] = SEC6 /* \SEC6 */
                        BUF0 [0x07] = SEC7 /* \SEC7 */
                        BUF0 [0x08] = SEC8 /* \SEC8 */
                    If (\_PR.EPCS != Zero)
                        Local0 = 0xFF
                        \_T_0 = (Arg0 + Zero)
                        If (\_T_0 == 0x03F8)
                            Local0 = Zero
                        ElseIf (\_T_0 == 0x02F8)
                            Local0 = 0ne
                        ElseIf (\_T_0 == 0x0220)
                            Local0 = 0x02
                        ElseIf (\_T_0 == 0x0228)
                            Local0 = 0x03
                        ElseIf (\_T_0 == 0x0238)
                            Local0 = 0x04
                        ElseIf (\_T_0 == 0x02E8)
                            Local0 = 0x05
                        ElseIf (\_T_0 == 0x0338)
                            Local0 = 0x06
                        ElseIf (\_T_0 == 0x03E8)
                            Local0 = 0x07
                        \_T_0 = (Arg0 + Zero)
                        If (\_T_0 == Zero)
                            CALE = Zero
                            Local0 = UXDV (Arg2)
                            If (Local0 != 0xFF)
                                CARN = Local0
                                CALE = 0ne
                        ElseIf (\_T_0 == 0ne)
                            CBLE = Zero
                            Local0 = UXDV (Arg2)
                            If (Local0 != 0xFF)
                                CBDR = Local0
                                CBLE = 0ne
                        ElseIf (\_T_0 == 0x02)
                            LTLE = Zero
                            If (Arg2 == 0x0378)
                                LTDR = Zero

```

```

    If (Arg2 == 0x0278)
        LTDR = One
    If (Arg2 == 0x03BC)
        LTDR = 0x02
        LTL = One
    ElseIf (_T_0 == 0x03)
        FDLE = Zero
        If (Arg2 == 0x03F0)
            FDDR = Zero
        If (Arg2 == 0x0370)
            FDDR = One
            FDLE = One
    ElseIf (_T_0 == 0x08)
        If (Arg2 == 0x0200)
            GLE = One
            GLE = Zero
        If (Arg2 == 0x0208)
            GLE = One
            GLE = Zero
    ElseIf (_T_0 == 0x09)
        If (Arg2 == 0x0200)
            GLE = One
            GLE = Zero
        If (Arg2 == 0x0208)
            GLE = One
            GLE = Zero
    ElseIf (_T_0 == 0x0A)
        If ((Arg2 == 0x60) || (Arg2 == 0x64))
            KLE = One
            KLE = Zero
    ElseIf (_T_0 == 0x0B)
        If ((Arg2 == 0x62) || (Arg2 == 0x66))
            MLE = One
            MLE = Zero
    ElseIf (_T_0 == 0x0C)
        If (Arg2 == 0x2E)
            CLE = One
            CLE = Zero
        If (Arg2 == 0x4E)
            CLE = One
            CLE = Zero
    ElseIf (_T_0 == 0x0D)
        If (Arg2 == 0x2E)
            CLE = One
            CLE = Zero
        If (Arg2 == 0x4E)
            CLE = One
            CLE = Zero
    If (DIF0 == One)
    If (DIF1 == One)
    If (DIF2 == One)
    GPEC = Zero
    If (_PR_HMPI == One)
        _PR_HMPI = Zero
    If (CAMT == Zero)
        If (Arg0 == ToUID ("f5cf0ff7-5d60-4842-82c0-fa1a61d873f2"))
            _T_0 = ToInteger (Arg2)
            If (_T_0 == Zero)
                If (ToInteger (Arg1) == Zero)
                    ElseIf (_T_0 == One)
                        If (DerefOf (Arg3 [Zero]) == Zero)
                            If (BID == 0x20)
                                IDFU = Zero
                                If (((BID == 0x04) || (BID == 0x14)) || (BID == 0x0B)) || (BID == 0x1B))
                                    IDFU = Zero
                                If (BID == 0x20)
                                    IDFU = One
                                If (((BID == 0x04) || (BID == 0x14)) || (BID == 0x0B)) || (BID == 0x1B))
                                    IDFU = One
                            ElseIf (_T_0 == 0x02)
                                If (DerefOf (Arg3 [Zero]) == Zero)
                                    If (BID == 0x20)
                                        IPWR = One
                                        If (((BID == 0x04) || (BID == 0x14)) || (BID == 0x0B)) || (BID == 0x1B))
                                            IPWR = One
                                    If (BID == 0x20)
                                        IPWR = Zero
                                    If (((BID == 0x04) || (BID == 0x14)) || (BID == 0x0B)) || (BID == 0x1B))
                                        IPWR = Zero
                                If (PCHV () == SPTL)
                                    If (SDWE != Zero)
                                        If (SDWE == One)
                                            SDIP = GNUM (0x02010011)
                                            SDGP = GNUM (0x02010011)
                                        If (SDWE == 0x02)
                                            SDIP = GNUM (0x02060005)
                                            SDGP = GNUM (0x02060005)
                                    If ((S0ID == One) || (OSYS >= 0x07DF))
                                    If (((S0ID == One) || (OSYS >= 0x07DF)) && ((PEPC & 0x03) != Zero))
                                If ((S0ID == One) || (OSYS >= 0x07DF))
                                    If ((S0ID == One) || (OSYS >= 0x07DF))
                                        If (((S0ID == One) || (OSYS >= 0x07DF)) && ((PEPC & 0x0400) == 0x0400) || ((PEPC & 0x0800) == 0x0800)))
                                            If (BCCX == One)
                                                If (SCCX == 0x06)
                                                    If (PIXX == One)
                                                If (BCCX == One)
                                                    If (SCCX == 0x08)
                                                        If (PIXX == 0x02)
                                                            If (((S0ID == One) || (OSYS >= 0x07DF)) && ((PEPC & 0x0400) == 0x0400) || ((PEPC & 0x0800) == 0x0800)))
                                                                If (BCCX == One)
                                                                    If (SCCX == 0x06)
                                                                        If (PIXX == One)
                                                                    If (BCCX == One)
                                                                        If (SCCX == 0x08)
                                                                            If (PIXX == 0x02)
                                                                                If (((S0ID == One) || (OSYS >= 0x07DF)) && ((PEPC & 0x0400) == 0x0400) || ((PEPC & 0x0800) == 0x0800)))
                                                                                    If (BCCX == One)
                                                                                        If (SCCX == 0x06)
                                                                                            If (PIXX == One)
                                                                                        If (BCCX == One)
                                                                                            If (SCCX == 0x08)
                                                                                                If (PIXX == 0x02)

```







```

Local0 = (Local1 | Arg0)
Local0 |= Local2
Local0 |= Local3
_T_0 = Arg0
If (_T_0 == Zero)
    BUF [Zero] = L0H0 /* \L0H0 */
    BUF [One] = L0H1 /* \L0H1 */
    BUF [0x02] = L0H2 /* \L0H2 */
    BUF [0x03] = L0H3 /* \L0H3 */
    BUF [0x04] = L0H4 /* \L0H4 */
    BUF [0x05] = L0H5 /* \L0H5 */
    BUF [0x06] = L0H6 /* \L0H6 */
    BUF [0x07] = L0H7 /* \L0H7 */
    BUF [0x08] = L0H8 /* \L0H8 */
ElseIf (_T_0 == One)
    BUF [Zero] = L1H0 /* \L1H0 */
    BUF [One] = L1H1 /* \L1H1 */
    BUF [0x02] = L1H2 /* \L1H2 */
    BUF [0x03] = L1H3 /* \L1H3 */
    BUF [0x04] = L1H4 /* \L1H4 */
    BUF [0x05] = L1H5 /* \L1H5 */
    BUF [0x06] = L1H6 /* \L1H6 */
    BUF [0x07] = L1H7 /* \L1H7 */
    BUF [0x08] = L1H8 /* \L1H8 */
ElseIf (_T_0 == 0x02)
    BUF [Zero] = L2H0 /* \L2H0 */
    BUF [One] = L2H1 /* \L2H1 */
    BUF [0x02] = L2H2 /* \L2H2 */
    BUF [0x03] = L2H3 /* \L2H3 */
    BUF [0x04] = L2H4 /* \L2H4 */
    BUF [0x05] = L2H5 /* \L2H5 */
    BUF [0x06] = L2H6 /* \L2H6 */
    BUF [0x07] = L2H7 /* \L2H7 */
    BUF [0x08] = L2H8 /* \L2H8 */
    BUF [Zero] = L3H0 /* \L3H0 */
    BUF [One] = L3H1 /* \L3H1 */
    BUF [0x02] = L3H2 /* \L3H2 */
    BUF [0x03] = L3H3 /* \L3H3 */
    BUF [0x04] = L3H4 /* \L3H4 */
    BUF [0x05] = L3H5 /* \L3H5 */
    BUF [0x06] = L3H6 /* \L3H6 */
    BUF [0x07] = L3H7 /* \L3H7 */
    BUF [0x08] = L3H8 /* \L3H8 */
_T_0 = Arg0
If (_T_0 == Zero)
    Local0 = L0SM /* \L0SM */
ElseIf (_T_0 == One)
    Local0 = L1SM /* \L1SM */
ElseIf (_T_0 == 0x02)
    Local0 = L2SM /* \L2SM */
    Local0 = L3SM /* \L3SM */
_T_1 = Local0
If (_T_1 == Zero)
ElseIf (_T_1 == One)
ElseIf (_T_1 == 0x02)
ElseIf (_T_1 == 0x03)
ElseIf (_T_1 == 0x04)
ElseIf (_T_1 == 0x05)
ElseIf (_T_1 == 0x06)
ElseIf (_T_1 == 0x07)
ElseIf (_T_1 == 0x08)
ElseIf (_T_1 == 0x09)
ElseIf (_T_1 == 0x0A)
If (Arg0 == Zero)
    If (C0TP == One)
        _T_0 = L0BS /* \L0BS */
        If (_T_0 == Zero)
        ElseIf (_T_0 == One)
        ElseIf (_T_0 == 0x02)
        ElseIf (_T_0 == 0x03)
        ElseIf (_T_0 == 0x04)
        ElseIf (_T_0 == 0x05)
If (Arg0 == One)
    If (C1TP == One)
        _T_1 = L1BS /* \L1BS */
        If (_T_1 == Zero)
        ElseIf (_T_1 == One)
        ElseIf (_T_1 == 0x02)
        ElseIf (_T_1 == 0x03)
        ElseIf (_T_1 == 0x04)
        ElseIf (_T_1 == 0x05)
If (Arg0 == 0x02)
    If (C2TP == One)
        _T_2 = L2BS /* \L2BS */
        If (_T_2 == Zero)
        ElseIf (_T_2 == One)
        ElseIf (_T_2 == 0x02)
        ElseIf (_T_2 == 0x03)
        ElseIf (_T_2 == 0x04)
        ElseIf (_T_2 == 0x05)
If (Arg0 == 0x03)
    If (C3TP == One)
        _T_3 = L3BS /* \L3BS */
        If (_T_3 == Zero)
        ElseIf (_T_3 == One)
        ElseIf (_T_3 == 0x02)
        ElseIf (_T_3 == 0x03)
        ElseIf (_T_3 == 0x04)
        ElseIf (_T_3 == 0x05)
Local0 = ResourceTemplate C
Local1 = PINR (C0P0, C0G0)
Local0 = Local2
Local1 = PINR (C0P1, C0G1)
Local0 = Local2
Local1 = PINR (C0P2, C0G2)
Local0 = Local2
Local1 = PINR (C0P3, C0G3)
Local0 = Local2
If (C0TP == One)
PAR [One] = C0TP /* \C0TP */
PAR [0x03] = C0CV /* \C0CV */
If (Arg0 == ToUUID ("79234640-9e10-4fea-a5c1-b5aa8b19756f"))
    If (Arg2 == Zero)
        If (Arg1 == Zero)
        If (Arg2 == One)
        If (Arg2 == 0x02)
        If (Arg2 == 0x03)
        If (Arg2 == 0x04)
        If (Arg2 == 0x05)
Local0 = ResourceTemplate C

```

```

Local1 = PINR (C1P0, C1G0)
Local0 = Local2
Local1 = PINR (C1P1, C1G1)
Local0 = Local2
Local1 = PINR (C1P2, C1G2)
Local0 = Local2
Local1 = PINR (C1P3, C1G3)
Local0 = Local2
If (C1TP == One)
PAR [One] = C1TP /* \C1TP */
PAR [0x03] = C1CV /* \C1CV */
If (Arg0 == ToUUID ("79234640-9e10-4fea-a5c1-b5aa8b19756f"))
If (Arg2 == Zero)
If (Arg1 == Zero)
If (Arg2 == One)
If (Arg2 == 0x02)
If (Arg2 == 0x03)
If (Arg2 == 0x04)
If (Arg2 == 0x05)
Local0 = ResourceTemplate ()
Local1 = PINR (C2P0, C2G0)
Local0 = Local2
Local1 = PINR (C2P1, C2G1)
Local0 = Local2
Local1 = PINR (C2P2, C2G2)
Local0 = Local2
Local1 = PINR (C2P3, C2G3)
Local0 = Local2
If (C2TP == One)
PAR [One] = C2TP /* \C2TP */
PAR [0x03] = C2CV /* \C2CV */
If (Arg0 == ToUUID ("79234640-9e10-4fea-a5c1-b5aa8b19756f"))
If (Arg2 == Zero)
If (Arg1 == Zero)
If (Arg2 == One)
If (Arg2 == 0x02)
If (Arg2 == 0x03)
If (Arg2 == 0x04)
If (Arg2 == 0x05)
Local0 = ResourceTemplate ()
Local1 = PINR (C3P0, C3G0)
Local0 = Local2
Local1 = PINR (C3P1, C3G1)
Local0 = Local2
Local1 = PINR (C3P2, C3G2)
Local0 = Local2
Local1 = PINR (C3P3, C3G3)
Local0 = Local2
If (C3TP == One)
PAR [One] = C3TP /* \C3TP */
PAR [0x03] = C3CV /* \C3CV */
If (Arg0 == ToUUID ("79234640-9e10-4fea-a5c1-b5aa8b19756f"))
If (Arg2 == Zero)
If (Arg1 == Zero)
If (Arg2 == One)
If (Arg2 == 0x02)
If (Arg2 == 0x03)
If (Arg2 == 0x04)
If (Arg2 == 0x05)
If (C0IB == 0x02)
If (C0IB == 0x03)
Local0 = ResourceTemplate ()
Local0 = Local1
PAR [One] = C0TP /* \C0TP */
PAR [0x03] = C0CV /* \C0CV */
BPOS = C0PL /* \C0PL */
If (Arg0 == ToUUID ("26257549-9271-4ca4-bb43-c4899d5a4881"))
If (Arg2 == Zero)
If (Arg1 == Zero)
If (Arg2 == One)
If (Arg2 == 0x02)
If (Arg0 == ToUUID ("5815c5c8-c47d-477b-9a8d-76173176414b"))
If (Arg2 == Zero)
If (Arg1 == Zero)
If (Arg2 == One)
I2CC = One
DEV0 = DSMI (0x0B, C0IA, Zero, C0IB)
If (C1IB == 0x02)
If (C1IB == 0x03)
Local0 = ResourceTemplate ()
Local0 = Local1
PAR [One] = C1TP /* \C1TP */
PAR [0x03] = C1CV /* \C1CV */
BPOS = C1PL /* \C1PL */
If (Arg0 == ToUUID ("26257549-9271-4ca4-bb43-c4899d5a4881"))
If (Arg2 == Zero)
If (Arg1 == Zero)
If (Arg2 == One)
If (Arg2 == 0x02)
If (Arg0 == ToUUID ("5815c5c8-c47d-477b-9a8d-76173176414b"))
If (Arg2 == Zero)
If (Arg1 == Zero)
If (Arg2 == One)
I2CC = One
DEV0 = DSMI (0x0B, C1IA, Zero, C1IB)
If (C2IB == 0x02)
If (C2IB == 0x03)
Local0 = ResourceTemplate ()
Local0 = Local1
PAR [One] = C2TP /* \C2TP */
PAR [0x03] = C2CV /* \C2CV */
BPOS = C2PL /* \C2PL */
If (Arg0 == ToUUID ("26257549-9271-4ca4-bb43-c4899d5a4881"))
If (Arg2 == Zero)
If (Arg1 == Zero)
If (Arg2 == One)
If (Arg2 == 0x02)
If (Arg0 == ToUUID ("5815c5c8-c47d-477b-9a8d-76173176414b"))
If (Arg2 == Zero)
If (Arg1 == Zero)
If (Arg2 == One)
I2CC = One
DEV0 = DSMI (0x0B, C2IA, Zero, C2IB)
If (C3IB == 0x02)
If (C3IB == 0x03)
Local0 = ResourceTemplate ()
Local0 = Local1
PAR [One] = C3TP /* \C3TP */
PAR [0x03] = C3CV /* \C3CV */
BPOS = C3PL /* \C3PL */

```

```

If (Arg0 == ToUUID ("26257549-9271-4ca4-bb43-c4899d5a4881"))
  If (Arg2 == Zero)
    If (Arg1 == Zero)
      If (Arg2 == One)
      If (Arg2 == 0x02)
    If (Arg0 == ToUUID ("5815c5c8-c47d-477b-9a8d-76173176414b"))
      If (Arg2 == Zero)
        If (Arg1 == Zero)
          If (Arg2 == One)
            I2CC = One
            DEV0 = DSMI (0x08, C3IA, Zero, C3IB)
          BUF [Zero] = L0M0 /* \L0M0 */
          BUF [One] = L0M1 /* \L0M1 */
          BUF [0x02] = L0M2 /* \L0M2 */
          BUF [0x03] = L0M3 /* \L0M3 */
          BUF [0x04] = L0M4 /* \L0M4 */
          BUF [0x05] = L0M5 /* \L0M5 */
          BUF [0x06] = L0M6 /* \L0M6 */
          BUF [0x07] = L0M7 /* \L0M7 */
          BUF [0x08] = L0M8 /* \L0M8 */
          BUF [0x09] = L0M9 /* \L0M9 */
          BUF [0x0A] = L0MA /* \L0MA */
          BUF [0x0B] = L0MB /* \L0MB */
          BUF [0x0C] = L0MC /* \L0MC */
          BUF [0x0D] = L0MD /* \L0MD */
          BUF [0x0E] = L0ME /* \L0ME */
          BUF [0x0F] = L0MF /* \L0MF */
          BPOS = L0PL /* \L0PL */
          Local0 = ResourceTemplate ()
          Local1 = IICB (L0A0, L0B5)
          Local0 = Local2
          Local1 = IICB (L0A1, L0B5)
          Local0 = Local2
          Local1 = IICB (L0A2, L0B5)
          Local0 = Local2
          Local1 = IICB (L0A3, L0B5)
          Local0 = Local2
          Local1 = IICB (L0A4, L0B5)
          Local0 = Local2
          Local1 = IICB (L0A5, L0B5)
          Local0 = Local2
          Local1 = IICB (L0A6, L0B5)
          Local0 = Local2
          Local1 = IICB (L0A7, L0B5)
          Local0 = Local2
          Local1 = IICB (L0A8, L0B5)
          Local0 = Local2
          Local1 = IICB (L0A9, L0B5)
          Local0 = Local2
          Local1 = IICB (L0AA, L0B5)
          Local0 = Local2
          Local1 = IICB (L0AB, L0B5)
          Local0 = Local2
          PAR [Zero] = L0DV /* \L0DV */
          PAR [One] = L0CV /* \L0CV */
          PAR [0x18] = CDIV /* \CDIV */
          PAR [0x1C] = L0LU /* \L0LU */
          PAR [0x1D] = L0NL /* \L0NL */
          PAR [0x4E] = L0EE /* \L0EE */
          PAR [0x4F] = L0VC /* \L0VC */
          PAR [0x52] = L0FS /* \L0FS */
          PAR [0x54] = L0DG /* \L0DG */
          DAT = L0CK /* \L0CK */
          PAR [0x5A] = L0CL /* \L0CL */
        If (Arg0 == ToUUID ("822ace8f-2814-4174-a56b-5f029fe079ee"))
          If (Arg2 == Zero)
            If (Arg1 == Zero)
              If (Arg2 == One)
                BUF [Zero] = L0M0 /* \L0M0 */
                BUF [One] = L0M1 /* \L0M1 */
                BUF [0x02] = L0M2 /* \L0M2 */
                BUF [0x03] = L0M3 /* \L0M3 */
                BUF [0x04] = L0M4 /* \L0M4 */
                BUF [0x05] = L0M5 /* \L0M5 */
                BUF [0x06] = L0M6 /* \L0M6 */
                BUF [0x07] = L0M7 /* \L0M7 */
                BUF [0x08] = L0M8 /* \L0M8 */
                BUF [0x09] = L0M9 /* \L0M9 */
                BUF [0x0A] = L0MA /* \L0MA */
                BUF [0x0B] = L0MB /* \L0MB */
                BUF [0x0C] = L0MC /* \L0MC */
                BUF [0x0D] = L0MD /* \L0MD */
                BUF [0x0E] = L0ME /* \L0ME */
                BUF [0x0F] = L0MF /* \L0MF */
            If (Arg0 == ToUUID ("5815c5c8-c47d-477b-9a8d-76173176414b"))
              If (Arg2 == Zero)
                If (Arg1 == Zero)
                  If (Arg2 == One)
                    I2CC = L0DI /* \L0DI */
                    DEV0 = DSMI (L0D0, L0A0, Zero, L0B5)
                    DEV1 = DSMI (L0D1, L0A1, Zero, L0B5)
                    DEV2 = DSMI (L0D2, L0A2, Zero, L0B5)
                    DEV3 = DSMI (L0D3, L0A3, Zero, L0B5)
                    DEV4 = DSMI (L0D4, L0A4, Zero, L0B5)
                    DEV5 = DSMI (L0D5, L0A5, Zero, L0B5)
                    DEV6 = DSMI (L0D6, L0A6, Zero, L0B5)
                    DEV7 = DSMI (L0D7, L0A7, Zero, L0B5)
                    DEV8 = DSMI (L0D8, L0A8, Zero, L0B5)
                    DEV9 = DSMI (L0D9, L0A9, Zero, L0B5)
                    DEVA = DSMI (L0DA, L0AA, Zero, L0B5)
                    DEVB = DSMI (L0DB, L0AB, Zero, L0B5)
              If (Arg0 == ToUUID ("26257549-9271-4ca4-bb43-c4899d5a4881"))
                If (Arg2 == Zero)
                  If (Arg1 == Zero)
                    If (Arg2 == One)
                    If (Arg2 == 0x02)
                    If (Arg2 == 0x03)
                    If (Arg2 == 0x04)
                    If (Arg2 == 0x05)
                    If (Arg2 == 0x06)
                    If (Arg2 == 0x07)
                    If (Arg2 == 0x08)
                    If (Arg2 == 0x08)
                    If (Arg2 == 0x09)
                    If (Arg2 == 0x0A)
                    If (Arg2 == 0x0B)
                    If (Arg2 == 0x0C)
                    BUF [Zero] = L1M0 /* \L1M0 */
                    BUF [One] = L1M1 /* \L1M1 */
                    BUF [0x02] = L1M2 /* \L1M2 */

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```

BUF [0x03] = L1M3 /* \L1M3 */
BUF [0x04] = L1M4 /* \L1M4 */
BUF [0x05] = L1M5 /* \L1M5 */
BUF [0x06] = L1M6 /* \L1M6 */
BUF [0x07] = L1M7 /* \L1M7 */
BUF [0x08] = L1M8 /* \L1M8 */
BUF [0x09] = L1M9 /* \L1M9 */
BUF [0x0A] = L1MA /* \L1MA */
BUF [0x0B] = L1MB /* \L1MB */
BUF [0x0C] = L1MC /* \L1MC */
BUF [0x0D] = L1MD /* \L1MD */
BUF [0x0E] = L1ME /* \L1ME */
BUF [0x0F] = L1MF /* \L1MF */
BPOS = L1PL /* \L1PL */
Local0 = ResourceTemplate ()
Local1 = IICB (L1A0, L1BS)
Local0 = Local2
Local1 = IICB (L1A1, L1BS)
Local0 = Local2
Local1 = IICB (L1A2, L1BS)
Local0 = Local2
Local1 = IICB (L1A3, L1BS)
Local0 = Local2
Local1 = IICB (L1A4, L1BS)
Local0 = Local2
Local1 = IICB (L1A5, L1BS)
Local0 = Local2
Local1 = IICB (L1A6, L1BS)
Local0 = Local2
Local1 = IICB (L1A7, L1BS)
Local0 = Local2
Local1 = IICB (L1A8, L1BS)
Local0 = Local2
Local1 = IICB (L1A9, L1BS)
Local0 = Local2
Local1 = IICB (L1AA, L1BS)
Local0 = Local2
Local1 = IICB (L1AB, L1BS)
Local0 = Local2
PAR [Zero] = L1DV /* \L1DV */
PAR [One] = L1CV /* \L1CV */
PAR [0x18] = CDIV /* \CDIV */
PAR [0x1C] = L1LU /* \L1LU */
PAR [0x1D] = L1NL /* \L1NL */
PAR [0x4E] = L1EE /* \L1EE */
PAR [0x4F] = L1VC /* \L1VC */
PAR [0x52] = L1FS /* \L1FS */
PAR [0x54] = L1DG /* \L1DG */
DAT = L1CK /* \L1CK */
PAR [0x5A] = L1CL /* \L1CL */
If (Arg0 == ToUUID ("822ace8f-2814-4174-a56b-5f029fe079ee"))
If (Arg2 == Zero)
If (Arg1 == Zero)
If (Arg2 == One)
BUF [Zero] = L1M0 /* \L1M0 */
BUF [One] = L1M1 /* \L1M1 */
BUF [0x02] = L1M2 /* \L1M2 */
BUF [0x03] = L1M3 /* \L1M3 */
BUF [0x04] = L1M4 /* \L1M4 */
BUF [0x05] = L1M5 /* \L1M5 */
BUF [0x06] = L1M6 /* \L1M6 */
BUF [0x07] = L1M7 /* \L1M7 */
BUF [0x08] = L1M8 /* \L1M8 */
BUF [0x09] = L1M9 /* \L1M9 */
BUF [0x0A] = L1MA /* \L1MA */
BUF [0x0B] = L1MB /* \L1MB */
BUF [0x0C] = L1MC /* \L1MC */
BUF [0x0D] = L1MD /* \L1MD */
BUF [0x0E] = L1ME /* \L1ME */
BUF [0x0F] = L1MF /* \L1MF */
If (Arg0 == ToUUID ("5815c5c8-c47d-477b-9a8d-76173176414b"))
If (Arg2 == Zero)
If (Arg1 == Zero)
If (Arg2 == One)
I2CC = L1DI /* \L1DI */
DEV0 = DSMI (L1D0, L1A0, Zero, L1BS)
DEV1 = DSMI (L1D1, L1A1, Zero, L1BS)
DEV2 = DSMI (L1D2, L1A2, Zero, L1BS)
DEV3 = DSMI (L1D3, L1A3, Zero, L1BS)
DEV4 = DSMI (L1D4, L1A4, Zero, L1BS)
DEV5 = DSMI (L1D5, L1A5, Zero, L1BS)
DEV6 = DSMI (L1D6, L1A6, Zero, L1BS)
DEV7 = DSMI (L1D7, L1A7, Zero, L1BS)
DEV8 = DSMI (L1D8, L1A8, Zero, L1BS)
DEV9 = DSMI (L1D9, L1A9, Zero, L1BS)
DEVA = DSMI (L1DA, L1AA, Zero, L1BS)
DEVB = DSMI (L1DB, L1AB, Zero, L1BS)
If (Arg0 == ToUUID ("26257549-9271-4ca4-bb43-c4899d5a4881"))
If (Arg2 == Zero)
If (Arg1 == Zero)
If (Arg2 == One)
If (Arg2 == 0x02)
If (Arg2 == 0x03)
If (Arg2 == 0x04)
If (Arg2 == 0x05)
If (Arg2 == 0x06)
If (Arg2 == 0x07)
If (Arg2 == 0x08)
If (Arg2 == 0x08)
If (Arg2 == 0x09)
If (Arg2 == 0x0A)
If (Arg2 == 0x0B)
If (Arg2 == 0x0C)
BUF [Zero] = L2M0 /* \L2M0 */
BUF [One] = L2M1 /* \L2M1 */
BUF [0x02] = L2M2 /* \L2M2 */
BUF [0x03] = L2M3 /* \L2M3 */
BUF [0x04] = L2M4 /* \L2M4 */
BUF [0x05] = L2M5 /* \L2M5 */
BUF [0x06] = L2M6 /* \L2M6 */
BUF [0x07] = L2M7 /* \L2M7 */
BUF [0x08] = L2M8 /* \L2M8 */
BUF [0x09] = L2M9 /* \L2M9 */
BUF [0x0A] = L2MA /* \L2MA */
BUF [0x0B] = L2MB /* \L2MB */
BUF [0x0C] = L2MC /* \L2MC */
BUF [0x0D] = L2MD /* \L2MD */
BUF [0x0E] = L2ME /* \L2ME */
BUF [0x0F] = L2MF /* \L2MF */
BPOS = L2PL /* \L2PL */

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```

Local0 = ResourceTemplate ()
Local1 = IICB (L2A0, L2B5)
Local0 = Local2
Local1 = IICB (L2A1, L2B5)
Local0 = Local2
Local1 = IICB (L2A2, L2B5)
Local0 = Local2
Local1 = IICB (L2A3, L2B5)
Local0 = Local2
Local1 = IICB (L2A4, L2B5)
Local0 = Local2
Local1 = IICB (L2A5, L2B5)
Local0 = Local2
Local1 = IICB (L2A6, L2B5)
Local0 = Local2
Local1 = IICB (L2A7, L2B5)
Local0 = Local2
Local1 = IICB (L2A8, L2B5)
Local0 = Local2
Local1 = IICB (L2A9, L2B5)
Local0 = Local2
Local1 = IICB (L2AA, L2B5)
Local0 = Local2
Local1 = IICB (L2AB, L2B5)
Local0 = Local2
PAR [Zero] = L2DV /* \L2DV */
PAR [One] = L2CV /* \L2CV */
PAR [0x18] = CDIV /* \CDIV */
PAR [0x1C] = L2LU /* \L2LU */
PAR [0x1D] = L2NL /* \L2NL */
PAR [0x4E] = L2EE /* \L2EE */
PAR [0x4F] = L2VC /* \L2VC */
PAR [0x52] = L2FS /* \L2FS */
PAR [0x54] = L2DG /* \L2DG */
DAT = L2CK /* \L2CK */
PAR [0x5A] = L2CL /* \L2CL */
If (Arg0 == ToUUID ("822ace8f-2814-4174-a56b-5f029fe079ee"))
If (Arg2 == Zero)
If (Arg1 == Zero)
If (Arg2 == One)
BUF [Zero] = L2M0 /* \L2M0 */
BUF [One] = L2M1 /* \L2M1 */
BUF [0x02] = L2M2 /* \L2M2 */
BUF [0x03] = L2M3 /* \L2M3 */
BUF [0x04] = L2M4 /* \L2M4 */
BUF [0x05] = L2M5 /* \L2M5 */
BUF [0x06] = L2M6 /* \L2M6 */
BUF [0x07] = L2M7 /* \L2M7 */
BUF [0x08] = L2M8 /* \L2M8 */
BUF [0x09] = L2M9 /* \L2M9 */
BUF [0x0A] = L2MA /* \L2MA */
BUF [0x0B] = L2MB /* \L2MB */
BUF [0x0C] = L2MC /* \L2MC */
BUF [0x0D] = L2MD /* \L2MD */
BUF [0x0E] = L2ME /* \L2ME */
BUF [0x0F] = L2MF /* \L2MF */
If (Arg0 == ToUUID ("5815c5c8-c47d-477b-9a8d-76173176414b"))
If (Arg2 == Zero)
If (Arg1 == Zero)
If (Arg2 == One)
I2CC = L2DI /* \L2DI */
DEV0 = DSMI (L2D0, L2A0, Zero, L2B5)
DEV1 = DSMI (L2D1, L2A1, Zero, L2B5)
DEV2 = DSMI (L2D2, L2A2, Zero, L2B5)
DEV3 = DSMI (L2D3, L2A3, Zero, L2B5)
DEV4 = DSMI (L2D4, L2A4, Zero, L2B5)
DEV5 = DSMI (L2D5, L2A5, Zero, L2B5)
DEV6 = DSMI (L2D6, L2A6, Zero, L2B5)
DEV7 = DSMI (L2D7, L2A7, Zero, L2B5)
DEV8 = DSMI (L2D8, L2A8, Zero, L2B5)
DEV9 = DSMI (L2D9, L2A9, Zero, L2B5)
DEVA = DSMI (L2DA, L2AA, Zero, L2B5)
DEVB = DSMI (L2DB, L2AB, Zero, L2B5)
If (Arg0 == ToUUID ("26257549-9271-4ca4-bb43-c4899d5a4881"))
If (Arg2 == Zero)
If (Arg1 == Zero)
If (Arg2 == One)
If (Arg2 == 0x02)
If (Arg2 == 0x03)
If (Arg2 == 0x04)
If (Arg2 == 0x05)
If (Arg2 == 0x06)
If (Arg2 == 0x07)
If (Arg2 == 0x08)
If (Arg2 == 0x08)
If (Arg2 == 0x09)
If (Arg2 == 0x0A)
If (Arg2 == 0x0B)
If (Arg2 == 0x0C)
BUF [Zero] = L3M0 /* \L3M0 */
BUF [One] = L3M1 /* \L3M1 */
BUF [0x02] = L3M2 /* \L3M2 */
BUF [0x03] = L3M3 /* \L3M3 */
BUF [0x04] = L3M4 /* \L3M4 */
BUF [0x05] = L3M5 /* \L3M5 */
BUF [0x06] = L3M6 /* \L3M6 */
BUF [0x07] = L3M7 /* \L3M7 */
BUF [0x08] = L3M8 /* \L3M8 */
BUF [0x09] = L3M9 /* \L3M9 */
BUF [0x0A] = L3MA /* \L3MA */
BUF [0x0B] = L3MB /* \L3MB */
BUF [0x0C] = L3MC /* \L3MC */
BUF [0x0D] = L3MD /* \L3MD */
BUF [0x0E] = L3ME /* \L3ME */
BUF [0x0F] = L3MF /* \L3MF */
BPOS = L3PL /* \L3PL */
Local0 = ResourceTemplate ()
Local1 = IICB (L3A0, L3B5)
Local0 = Local2
Local1 = IICB (L3A1, L3B5)
Local0 = Local2
Local1 = IICB (L3A2, L3B5)
Local0 = Local2
Local1 = IICB (L3A3, L3B5)
Local0 = Local2
Local1 = IICB (L3A4, L3B5)
Local0 = Local2
Local1 = IICB (L3A5, L3B5)
Local0 = Local2
Local1 = IICB (L3A6, L3B5)

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Local0 = Local2
Local1 = IICB (L3A7, L3B5)
Local0 = Local2
Local1 = IICB (L3A8, L3B5)
Local0 = Local2
Local1 = IICB (L3A9, L3B5)
Local0 = Local2
Local1 = IICB (L3AA, L3B5)
Local0 = Local2
Local1 = IICB (L3AB, L3B5)
Local0 = Local2
PAR [Zero] = L3DV /* \L3DV */
PAR [One] = L3CV /* \L3CV */
PAR [0x18] = CDIV /* \CDIV */
PAR [0x1C] = L3LU /* \L3LU */
PAR [0x1D] = L3NL /* \L3NL */
PAR [0x4E] = L3EE /* \L3EE */
PAR [0x4F] = L3VC /* \L3VC */
PAR [0x52] = L3FS /* \L3FS */
PAR [0x54] = L3DG /* \L3DG */
DAT = L3CK /* \L3CK */
PAR [0x5A] = L3CL /* \L3CL */
If (Arg0 == ToUUID ("822ace8f-2814-4174-a56b-5f029fe079ee"))
  If (Arg2 == Zero)
    If (Arg1 == Zero)
      If (Arg2 == One)
        BUF [Zero] = L3M0 /* \L3M0 */
        BUF [One] = L3M1 /* \L3M1 */
        BUF [0x02] = L3M2 /* \L3M2 */
        BUF [0x03] = L3M3 /* \L3M3 */
        BUF [0x04] = L3M4 /* \L3M4 */
        BUF [0x05] = L3M5 /* \L3M5 */
        BUF [0x06] = L3M6 /* \L3M6 */
        BUF [0x07] = L3M7 /* \L3M7 */
        BUF [0x08] = L3M8 /* \L3M8 */
        BUF [0x09] = L3M9 /* \L3M9 */
        BUF [0x0A] = L3MA /* \L3MA */
        BUF [0x0B] = L3MB /* \L3MB */
        BUF [0x0C] = L3MC /* \L3MC */
        BUF [0x0D] = L3MD /* \L3MD */
        BUF [0x0E] = L3ME /* \L3ME */
        BUF [0x0F] = L3MF /* \L3MF */
      If (Arg0 == ToUUID ("5815c5c8-c47d-477b-9a8d-76173176414b"))
        If (Arg2 == Zero)
          If (Arg1 == Zero)
            If (Arg2 == One)
              I2CC = L3DI /* \L3DI */
              DEV0 = DSMI (L3D0, L3A0, Zero, L3B5)
              DEV1 = DSMI (L3D1, L3A1, Zero, L3B5)
              DEV2 = DSMI (L3D2, L3A2, Zero, L3B5)
              DEV3 = DSMI (L3D3, L3A3, Zero, L3B5)
              DEV4 = DSMI (L3D4, L3A4, Zero, L3B5)
              DEV5 = DSMI (L3D5, L3A5, Zero, L3B5)
              DEV6 = DSMI (L3D6, L3A6, Zero, L3B5)
              DEV7 = DSMI (L3D7, L3A7, Zero, L3B5)
              DEV8 = DSMI (L3D8, L3A8, Zero, L3B5)
              DEV9 = DSMI (L3D9, L3A9, Zero, L3B5)
              DEVA = DSMI (L3DA, L3AA, Zero, L3B5)
              DEVB = DSMI (L3DB, L3AB, Zero, L3B5)
            If (Arg0 == ToUUID ("26257549-9271-4ca4-bb43-c4899d5a4881"))
              If (Arg2 == Zero)
                If (Arg1 == Zero)
                  If (Arg2 == One)
                    If (Arg2 == 0x02)
                      If (Arg2 == 0x03)
                        If (Arg2 == 0x04)
                          If (Arg2 == 0x05)
                            If (Arg2 == 0x06)
                              If (Arg2 == 0x07)
                                If (Arg2 == 0x08)
                                  If (Arg2 == 0x08)
                                    If (Arg2 == 0x09)
                                      If (Arg2 == 0x0A)
                                        If (Arg2 == 0x0B)
                                          If (Arg2 == 0x0C)
                                            HPT0 = HPTB /* \HPTB */
                                          If (PCHV () == SPTH)
                                            If (PCHV () == SPTH)
                                              If (ALSE == 0x02)
                                                SMIC = BSMI /* \BSMI */
                                                SMIC = (Arg0 + BSMI) /* \BSMI */
                                                SMIC = BSMI /* \BSMI */
                                              If (HIDM == One)
                                                HRD = One
                                                HIE = One
                                                HIG = One
                                              If ((Arg0 == 0x03) || (Arg0 == 0x04))
                                                HIE = Zero
                                                HRD = Zero
                                                HIG = One
                                              Local0 = ((Arg2 << 0x10) | (Arg1 << 0x08))
                                              Local0 |= Arg0
                                              CBWW = 0x80040011
                                              CBWW = Local0
                                              HIG = One
                                              If (HIDM == One)
                                                CBWW = 0x80040011
                                                CBWW = Arg0
                                              HIG = One
                                              If (HIDM != One)
                                                HRS = One
                                                HIG = One
                                                HRS = Zero
                                                HRD = One
                                                HIG = One
                                              If (MWP != MRP)
                                                Local1 = CBRW /* \_SB_.PCI0.HEC2.CBRW */
                                                Local0 = CBRW /* \_SB_.PCI0.HEC2.CBRW */
                                                HIG = One
                                                Local5 = (Local0 & 0xFF)
                                                If (Local5 == Zero)
                                                  \_PR.CPPC = RPS0 /* \_SB_.PCI0.HEC2.RPS0 */
                                                  Local0 |= 0x80
                                                  \_PR.CTPC = RTS0 /* \_SB_.PCI0.HEC2.RTS0 */
                                                  Local0 |= 0x40
                                                  CPS0 = \_PR.CTPC
                                                  CTS0 = \_PR.CPPC
                                                  CBWW = Local1
                                                  CBWW = Local0
                                                  HIG = One

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ElseIf (Local5 == 0x03)
    PURA = Local0
ElseIf (Local5 == 0x04)
    PWR = (((Local0 >> 0x10) & 0xFFFF) * 0x03E8)
ElseIf (Local5 == 0x05)
    PMAI = (((Local0 >> 0x10) & 0xFFFF) * 0x03E8)
ElseIf (Local5 == 0x06)
    HWL = (((Local0 >> 0x10) & 0xFFFF) * 0x03E8)
ElseIf (Local5 == 0x0C)
    If (One == PMAV)
        If (((Local0 >> 0x10) & 0xFF) == One)
            Local2 = (HWL / 0x03E8)
            Local2 = (PMAI / 0x03E8)
            Local2 = (PTPU / 0x03E8)
            Local3 = (PTPL / 0x03E8)
ElseIf ((Local5 == 0x88) | (Local5 == 0x0D))
    PMCP [Zero] = ((Local0 >> 0x18) & 0xFF)
    PMCP [0x02] = ((Local0 >> 0x88) & One)
    If (((Local0 >> 0x10) & One) == One)
        PMCP [0x08] = 0xFFFFFFFF
        PMCP [0x08] = Zero
        PMCP [0x03] = CBRW /* \_SB_.PCI0.HEC2.CBRW */
        PMCP [0x04] = CBRW /* \_SB_.PCI0.HEC2.CBRW */
        PMCP [0x05] = CBRW /* \_SB_.PCI0.HEC2.CBRW */
        PMCP [0x06] = CBRW /* \_SB_.PCI0.HEC2.CBRW */
        PMCP [0x07] = CBRW /* \_SB_.PCI0.HEC2.CBRW */
        PMCP [0x09] = CBRW /* \_SB_.PCI0.HEC2.CBRW */
        PMCP [0xA] = CBRW /* \_SB_.PCI0.HEC2.CBRW */
        If (Local5 == 0x0D)
            HIG = One
            PMAV = One
ElseIf (Local5 == 0x09)
ElseIf (Local5 == 0x87)
    HLST = ((Local0 >> 0x08) & 0xFF)
ElseIf (Local5 == 0x8A)
    AIST = ((Local0 >> 0x08) & 0xFF)
ElseIf (Local5 == 0x8B)
    TPST = ((Local0 >> 0x08) & 0xFF)
\_SB_.PCI0.HEC2.HIS = One
Local0 = Zero
While ((\_SB_.PCI0.HEC2.H2RD () != 0xFF) & (Local0 != 0x08))
    Local0 += One
Local1 = HIE /* \_SB_.PCI0.HEC2.HIE_ */
HIE = Zero
If (Arg0 == Zero)
    CBWW = 0x80040011
    CBWW = ((Arg2 << 0x10) | (Arg1 & 0xFF))
ElseIf (Arg0 == One)
    CBWW = 0x80080011
    CBWW = (Arg1 & 0xFF)
    CBWW = ((Arg3 << 0x10) | (Arg2 & 0xFFFF))
HIG = One
Local0 = Zero
While (Local0 != 0x0A)
    If (One == HIS)
        HIS = One
        If (H2RD () == (Arg1 | 0x80))
            Local0 += One
HIE = Local1
If (OSTP () >= 0W7)
If (PMAV == Zero)
Local0 = (Arg0 / 0x03E8)
AIST = 0x02
If ((AIST & 0xFF) == Zero)
    PMAI = Arg0
Local0 = (Arg0 / 0x03E8)
Local1 = (Arg1 + 0x01F4)
Local1 = (Arg1 / 0x03E8)
TPST = 0x02
If ((TPST & 0xFF) == Zero)
    PTPU = Arg0
    PTPL = Arg1
Local0 = (Arg0 / 0x03E8)
HLST = 0x02
If ((HLST & 0xFF) == Zero)
    HWL = Arg0
ElseIf (TTDP == Zero)
If (TTDP == Zero)
If (AMDT == One)
    MTFB = TPMB /* \TPMB */
    LTFB = 0x1000
    MTFC = TPMC /* \TPMC */
    LTFC = 0x1000
ElseIf (DTPT == One)
    MTFE = 0xFED40000
    LTFE = 0x0880
ElseIf (TTPF == One)
    MTFD = 0xFED40000
    LTFD = 0x5000
ElseIf (TTPF == Zero)
    MTFE = TPMM /* \TPMM */
If (TTDP == Zero)
ElseIf (TTDP == One)
    _T_0 = ToInteger (Arg1)
    If (_T_0 == Zero)
    ElseIf (_T_0 == One)
        TIMR = Zero
        If (AMDT == One)
            While ((BEGN == One) && (TIMR < 0x0200))
                If (BEGN == One)
                    ) == 0x03)
                    HCMD = One
                    FERR = One
                    BEGN = Zero
    _T_0 = ToInteger (Arg1)
    If (_T_0 == Zero)
    ElseIf (_T_0 == One)
        If (_STA () == Zero)
If (Arg0 == ToUUID ("3dddfaa6-361b-4eb4-a424-8d10089d1653")) /* Physical Presence Interface */
    _T_0 = ToInteger (Arg2)
    If (_T_0 == Zero)
    ElseIf (_T_0 == One)
    ElseIf (_T_0 == 0x02)
        TMF1 = OFST /* \OFST */
        TMF1 |= 0x1200 /* \TMF1 */
        SMI = TMF1 /* \TMF1 */
        TMF1 = SMI /* \_SB_.TPM_.SMI_ */
        TMF1 >>= 0x08
        If (TMF1 == 0xFF)
            TMF2 <<= 0x08

```

```

TMF2 |= OFST /* \TMF2 */
SMI = TMF2 /* \TMF2 */
TMF1 = SMI /* \_SB_.TPM_.SMI_ */
TMF1 >= 0x08
If (TMF1 == 0xFF)
If (TMF1 == 0xFF)
ElseIf (_T_0 == 0x03)
TMF1 = OFST /* \OFST */
TMF1 |= 0x1100 /* \TMF1 */
SMI = TMF1 /* \TMF1 */
TMF1 = SMI /* \_SB_.TPM_.SMI_ */
TMF1 >= 0x08
If (TMF1 == 0xFF)
PPI1 [One] = TMF1 /* \TMF1 */
ElseIf (_T_0 == 0x04)
ElseIf (_T_0 == 0x05)
TMF1 = OFST /* \OFST */
TMF1 |= 0x2100 /* \TMF1 */
SMI = TMF1 /* \TMF1 */
TMF1 = SMI /* \_SB_.TPM_.SMI_ */
TMF1 >= 0x08
If (TMF1 == 0xFF)
PPI2 [One] = TMF1 /* \TMF1 */
TMF1 = OFST /* \OFST */
TMF1 |= 0x3100 /* \TMF1 */
SMI = TMF1 /* \TMF1 */
TMF1 = SMI /* \_SB_.TPM_.SMI_ */
TMF1 >= 0x08
If (TMF1 == 0xFF)
TMF1 = OFST /* \OFST */
TMF1 |= 0x5100 /* \TMF1 */
SMI = TMF1 /* \TMF1 */
TMF1 = SMI /* \_SB_.TPM_.SMI_ */
TMF1 >= 0x08
If (TMF1 == 0xFF)
PPI2 [0x02] = 0xFFFFFFFF
ElseIf (TMF1 == 0xF1)
TMF1 = OFST /* \OFST */
TMF1 |= 0x5100 /* \TMF1 */
SMI = TMF1 /* \TMF1 */
TMF1 = SMI /* \_SB_.TPM_.SMI_ */
TMF1 >= 0x08
If (TMF1 == 0xFF)
PPI2 [0x02] = 0xFFFFFFFF
ElseIf (_T_0 == 0x06)
ElseIf (_T_0 == 0x07)
TMF1 = OFST /* \OFST */
TMF1 |= 0x1200 /* \TMF1 */
SMI = TMF1 /* \TMF1 */
TMF1 = SMI /* \_SB_.TPM_.SMI_ */
TMF1 >= 0x08
If (TMF1 == 0xFF)
TMF2 <= 0x08
TMF2 |= OFST /* \TMF2 */
SMI = TMF2 /* \TMF2 */
TMF1 = SMI /* \_SB_.TPM_.SMI_ */
TMF1 >= 0x08
If (TMF1 == 0xFF)
If (TMF1 == 0xF1)
ElseIf (_T_0 == 0x08)
TMF1 = OFST /* \OFST */
TMF1 |= 0x4300 /* \TMF1 */
SMI = TMF1 /* \TMF1 */
TMF1 = SMI /* \_SB_.TPM_.SMI_ */
TMF2 <= 0x08
TMF2 |= OFST /* \TMF2 */
SMI = TMF2 /* \TMF2 */
TMF1 = SMI /* \_SB_.TPM_.SMI_ */
TMF1 >= 0x08
If (TMF1 == 0xFF)
ElseIf (Arg0 == ToUUID ("376054ed-cc13-4675-901c-4756d7f2d45d"))
_T_1 = ToInteger (Arg2)
If (_T_1 == Zero)
ElseIf (_T_1 == One)
TMF1 = OFST /* \OFST */
TMF1 |= 0x2200 /* \TMF1 */
SMI = TMF1 /* \TMF1 */
TMF1 = SMI /* \_SB_.TPM_.SMI_ */
TMF1 >= 0x08
If (TMF1 == 0xFF)
TMF1 <= 0x08
TMF1 |= OFST /* \TMF1 */
SMI = TMF1 /* \TMF1 */
TMF1 = SMI /* \_SB_.TPM_.SMI_ */
TMF1 >= 0x08
If (TMF1 == 0xFF)
If (Arg0 == ToUUID ("cf8e16a5-c1e8-4e25-b712-4f54a96702c8"))
If (Arg0 == ToUUID ("6bbf6cab-5463-4714-b7cd-f0203c0368d4"))
VISI = Arg0
GPOS = Arg1
PCKG [Zero] = Arg0
PCKG [One] = Arg1
If (Arg0 == ToUUID ("e5c937d0-3553-4d7a-9117-ea4d19c3434d") /* Device Labeling Interface */)
If ((Arg1 == 0x02) && (Arg2 == Zero))
If ((Arg1 == 0x02) && (Arg2 == 0x07))
If (Arg0 == ToUUID ("e5c937d0-3553-4d7a-9117-ea4d19c3434d") /* Device Labeling Interface */)
If ((Arg1 == 0x02) && (Arg2 == Zero))
If ((Arg1 == 0x02) && (Arg2 == 0x07))
If (Arg0 == ToUUID ("e5c937d0-3553-4d7a-9117-ea4d19c3434d") /* Device Labeling Interface */)
If ((Arg1 == 0x02) && (Arg2 == Zero))
If ((Arg1 == 0x02) && (Arg2 == 0x07))
If (SYST == One)
If (Arg0 == ToUUID ("e5c937d0-3553-4d7a-9117-ea4d19c3434d") /* Device Labeling Interface */)
If ((Arg1 == 0x02) && (Arg2 == Zero))
If ((Arg1 == 0x02) && (Arg2 == 0x07))
If (SYST == One)
If (Arg0 == ToUUID ("e5c937d0-3553-4d7a-9117-ea4d19c3434d") /* Device Labeling Interface */)
If ((Arg1 == 0x02) && (Arg2 == Zero))
If ((Arg1 == 0x02) && (Arg2 == 0x07))
If (SYST == One)
If (Arg0 == ToUUID ("e5c937d0-3553-4d7a-9117-ea4d19c3434d") /* Device Labeling Interface */)
If ((Arg1 == 0x02) && (Arg2 == Zero))
If ((Arg1 == 0x02) && (Arg2 == 0x07))
If (SYST == Zero)
If (SYST == Zero)
MMBS = PMMB /* \_SB_.PCI0.RP20.PPF1.PMMB */

```



```

MMLN = PMMS /* \_SB_.PCI0.RP20.PPF1.PMMS */
MSBS = PMSB /* \_SB_.PCI0.RP20.PPF1.PMSB */
MSLN = PMSS /* \_SB_.PCI0.RP20.PPF1.PMSS */
If (OSTB == 0xFFFFFFFF)
    OSTB = OW15 /* \_SB_.OW15 */
    OSTB = OW13 /* \_SB_.OW13 */
    OSTB = OW12 /* \_SB_.OW12 */
    OSTB = OW7 /* \_SB_.OW7 */
    OSTB = OWLH /* \_SB_.OWLH */
    OSTB = OWXP /* \_SB_.OWXP */
    OSTB = OUNK /* \_SB_.OUNK */
    OSTB = OW98 /* \_SB_.OW98 */
    OSTB = OWME /* \_SB_.OWME */
    OSTB = OW2K /* \_SB_.OW2K */
    OSTB = OUNK /* \_SB_.OUNK */
    OSTB = OUNK /* \_SB_.OUNK */
Local0 = SizeOf (Arg0)
Local1 = SizeOf (Arg1)
If (Local0 != Local1)
    BUF0 = Arg0
    BUF1 = Arg1
Local2 = Zero
    Local3 = DerefOf (BUF0 [Local2])
    Local4 = DerefOf (BUF1 [Local2])
    If (Local3 != Local4)
PSTS = One
LTRS = LTRX /* \LTRX */
OBFS = OBFX /* \OBFX */
    CMDR |= 0x07
    D0ST = Zero
    If (VEID == 0x100E)
        NVID = HYSS /* \HYSS */
    If (VEID == 0x1002)
        ATID = HYSS /* \HYSS */
    _STA = One
    _STA = Zero
    WKEN = Zero
    WKEN = One
    WKEN = Zero
    If (SGGP != Zero)
        If (SGGP == One)
PSTS = One
LTRS = LTRY /* \LTRY */
OBFS = OBFY /* \OBFY */
    _STA = One
    _STA = Zero
    WKEN = Zero
    WKEN = One
    WKEN = Zero
    If (PIGP != Zero)
        If (PIGP == One)
PSTS = One
LTRS = LTRZ /* \LTRZ */
OBFS = OBFZ /* \OBFZ */
    _STA = One
    _STA = Zero
    WKEN = Zero
    WKEN = One
    WKEN = Zero
    If (P2GP != Zero)
        If (P2GP == One)
Local0 = Zero
If ((CPEX & 0x0FFF0FF0) == 0x000506E0)
    If ((MODDI & 0x06) != 0x04)
        Local0 = One
PION = Arg0
If (PION == Zero)
    If (SGGP == Zero)
ElseIf (PION == One)
    If (PIGP == Zero)
ElseIf (PION == 0x02)
    If (P2GP == Zero)
PEBA = \XBAS
PDEV = GDEV (PION)
PFUN = GFUN (PION)
If (DerefOf (SCLK [Zero]) != Zero)
If (CCHK (PION, One) == Zero)
If (OSYS == 0x07D9)
    If (PION == Zero)
        P0AP = Zero
        P0RM = Zero
    ElseIf (PION == One)
        P1AP = Zero
        P1RM = Zero
    ElseIf (PION == 0x02)
        P2AP = Zero
        P2RM = Zero
    If (PBGE != Zero)
        CBDL = GUBC (PION)
        MBDL = GMBX (PION)
        CBDL = MBDL /* \_SB_.PCI0.MBDL */
    If (PION == Zero)
        P0LD = Zero
        P0TR = One
        TCNT = Zero
        If (P0VC == Zero)
            TCNT += 0x10
    ElseIf (PION == One)
        P1LD = Zero
        P1TR = One
        TCNT = Zero
        If (P1VC == Zero)
            TCNT += 0x10
    ElseIf (PION == 0x02)
        P2LD = Zero
        P2TR = One
        TCNT = Zero
        If (P2VC == Zero)
            TCNT += 0x10
    If (PION == Zero)
        S0VI = H0VI /* \_SB_.PCI0.H0VI */
        S0DI = H0DI /* \_SB_.PCI0.H0DI */
        LCT0 = ((ELC0 & 0x43) | (LCT0 & 0xFFBC))
    ElseIf (PION == One)
        S1VI = H1VI /* \_SB_.PCI0.H1VI */
        S1DI = H1DI /* \_SB_.PCI0.H1DI */
        LCT1 = ((ELC1 & 0x43) | (LCT1 & 0xFFBC))
    ElseIf (PION == 0x02)
        S2VI = H2VI /* \_SB_.PCI0.H2VI */
        S2DI = H2DI /* \_SB_.PCI0.H2DI */

```

```

LCT2 = ((ELC2 & 0x43) | (LCT2 & 0xFFBC))
PIOF = Arg0
If (PIOF == Zero)
  If (SGGP == Zero)
ElseIf (PIOF == One)
  If (P1GP == Zero)
ElseIf (PIOF == 0x02)
  If (P2GP == Zero)
PEBA = \XBAS
PDEV = GDEV (PIOF)
PFUN = GFUN (PIOF)
If (CCHK (PIOF, Zero) == Zero)
If (Arg0 == Zero)
  ELC0 = LCT0 /* \SB_.PCI0.LCT0 */
  H0VI = S0VI /* \SB_.PCI0.S0VI */
  H0DI = S0DI /* \SB_.PCI0.S0DI */
  ECP0 = LCP0 /* \SB_.PCI0.LCP0 */
ElseIf (Arg0 == One)
  ELC1 = LCT1 /* \SB_.PCI0.LCT1 */
  H1VI = S1VI /* \SB_.PCI0.S1VI */
  H1DI = S1DI /* \SB_.PCI0.S1DI */
  ECP1 = LCP1 /* \SB_.PCI0.LCP1 */
ElseIf (Arg0 == 0x02)
  ELC2 = LCT2 /* \SB_.PCI0.LCT2 */
  H2VI = S2VI /* \SB_.PCI0.S2VI */
  H2DI = S2DI /* \SB_.PCI0.S2DI */
  ECP2 = LCP2 /* \SB_.PCI0.LCP2 */
If (OSYS == 0x07D9)
  If (PIOF == Zero)
    P0LD = One
    TCNT = Zero
    If (P0LT == 0x08)
      TCNT += 0x10
    P0RM = One
    P0AP = 0x03
  ElseIf (PIOF == One)
    P1LD = One
    TCNT = Zero
    If (P1LT == 0x08)
      TCNT += 0x10
    P1RM = One
    P1AP = 0x03
  ElseIf (PIOF == 0x02)
    P2LD = One
    TCNT = Zero
    If (P2LT == 0x08)
      TCNT += 0x10
    P2RM = One
    P2AP = 0x03
  If (PBG8 != Zero)
    MBDL = GMB8 (PIOF)
If (DerefOf (SCLK [Zero]) != Zero)
If (OSYS != 0x07D9)
  Local7 = Arg0
  Local7 |= (Arg1 << 0x14)
  Local7 |= (Arg2 << 0x0F)
  Local7 |= (Arg3 << 0x0C)
  Local7 |= Arg4
  Local7 = Arg0
  Local7 |= (Arg1 << 0x14)
  Local7 |= (Arg2 << 0x0F)
  Local7 |= (Arg3 << 0x0C)
  Local7 |= Arg4
  TEMP = Arg5
  Local7 = Arg0
  Local7 |= (Arg1 << 0x14)
  Local7 |= (Arg2 << 0x0F)
  Local7 |= (Arg3 << 0x0C)
  Local7 |= Arg4
  Local7 = Arg0
  Local7 |= (Arg1 << 0x14)
  Local7 |= (Arg2 << 0x0F)
  Local7 |= (Arg3 << 0x0C)
  Local7 |= Arg4
  TEMP = Arg5
  Local7 = Arg0
  Local7 |= (Arg1 << 0x14)
  Local7 |= (Arg2 << 0x0F)
  Local7 |= (Arg3 << 0x0C)
  Local7 |= Arg4
  TEMP = Arg5
  Local7 = MMRD (PEBA, PBUS, PDEV, PFUN, 0xAC)
  Local7 >>= 0x04
  Local7 &= 0x3F
  Local6 = Arg0
  Local6 >>= 0x04
  Local6 &= 0x3F
  Local0 = (Local7 - Local6)
  Local0 = Zero
If (Arg0 == Zero)
  HSTR = HST0 /* \SB_.PCI0.HST0 */
ElseIf (Arg0 == One)
  HSTR = HST1 /* \SB_.PCI0.HST1 */
ElseIf (Arg0 == 0x02)
  HSTR = HST2 /* \SB_.PCI0.HST2 */
HSTR >>= 0x10
HSTR &= 0x03
If (Arg0 == Zero)
  If (HSTR == 0x03)
    Local0 = 0x08
    Local0 = 0x04
  ElseIf (Arg0 == One)
    If (HSTR == 0x02)
      Local0 = 0x04
    ElseIf (HSTR == Zero)
      Local0 = 0x02
  ElseIf (Arg0 == 0x02)
    If (HSTR == Zero)
      Local0 = 0x02
FBDL = Zero
CBDL = Zero
If (Arg0 == Zero)
  HSTR = HST0 /* \SB_.PCI0.HST0 */
  LREV = LRV0 /* \SB_.PCI0.LRV0 */
ElseIf (Arg0 == One)

```

```

HSTR = HST1 /* \_SB_.PCI0.HST1 */
LREV = LRV1 /* \_SB_.PCI0.LRV1 */
ElseIf (Arg0 == 0x02)
HSTR = HST2 /* \_SB_.PCI0.HST2 */
LREV = LRV2 /* \_SB_.PCI0.LRV2 */
HSTR >= 0x10
HSTR &= 0x03
LREV >= 0x14
LREV &= One
If (Arg0 == Zero)
If (HSTR == 0x03)
FBDL = Zero
CBDL = 0x08
ElseIf (LREV == Zero)
FBDL = Zero
CBDL = 0x04
FBDL = 0x04
CBDL = 0x04
ElseIf (Arg0 == One)
If (HSTR == 0x02)
If (LREV == Zero)
FBDL = 0x04
CBDL = 0x04
FBDL = Zero
CBDL = 0x04
ElseIf (HSTR == Zero)
If (LREV == Zero)
FBDL = 0x04
CBDL = 0x02
FBDL = 0x02
CBDL = 0x02
ElseIf (Arg0 == 0x02)
If (HSTR == Zero)
If (LREV == Zero)
FBDL = 0x06
CBDL = 0x02
FBDL = Zero
CBDL = 0x02
INDX = One
If (CBDL != Zero)
While (INDX <= CBDL)
If (P0VI == IVID) {}
ElseIf (P0VI != IVID)
If (FBDL == Zero)
BSP1 = Zero
If (FBDL == One)
BSP2 = Zero
If (FBDL == 0x02)
BSP3 = Zero
If (FBDL == 0x03)
BSP4 = Zero
If (FBDL == 0x04)
BSP5 = Zero
If (FBDL == 0x05)
BSP6 = Zero
If (FBDL == 0x06)
BSP7 = Zero
If (FBDL == 0x07)
BSP8 = Zero
FBDL = Zero
CBDL = Arg1
If (CBDL == Zero)
If (Arg0 == Zero)
HSTR = HST0 /* \_SB_.PCI0.HST0 */
LREV = LRV0 /* \_SB_.PCI0.LRV0 */
ElseIf (Arg0 == One)
HSTR = HST1 /* \_SB_.PCI0.HST1 */
LREV = LRV1 /* \_SB_.PCI0.LRV1 */
ElseIf (Arg0 == 0x02)
HSTR = HST2 /* \_SB_.PCI0.HST2 */
LREV = LRV2 /* \_SB_.PCI0.LRV2 */
HSTR >= 0x10
HSTR &= 0x03
LREV >= 0x14
LREV &= One
If (Arg0 == Zero)
If (HSTR == 0x03)
If (LREV == Zero)
FBDL = (0x08 - CBDL)
FBDL = Zero
ElseIf (LREV == Zero)
FBDL = (0x04 - CBDL)
FBDL = 0x04
ElseIf (Arg0 == One)
If (HSTR == 0x02)
If (LREV == Zero)
FBDL = (0x08 - CBDL)
FBDL = Zero
ElseIf (HSTR == Zero)
If (LREV == Zero)
FBDL = (0x06 - CBDL)
FBDL = 0x02
ElseIf (Arg0 == 0x02)
If (HSTR == Zero)
If (LREV == Zero)
FBDL = (0x08 - CBDL)
FBDL = Zero
INDX = One
While (INDX <= CBDL)
If (P0VI == IVID) {}
ElseIf (P0VI != IVID)
If (FBDL == Zero)
BSP1 = One
If (FBDL == One)
BSP2 = One
If (FBDL == 0x02)
BSP3 = One
If (FBDL == 0x03)
BSP4 = One
If (FBDL == 0x04)
BSP5 = One
If (FBDL == 0x05)
BSP6 = One
If (FBDL == 0x06)
BSP7 = One
If (FBDL == 0x07)
BSP8 = One
If (Arg0 == Zero)
If (P0UB == Zero)
ElseIf (Arg0 == One)

```

```

If (P1UB == Zero)
ElseIf (Arg0 == 0x02)
If (P2UB == Zero)
Local7 = Zero
If (Arg0 == Zero)
Local6 = LCP0 /* \_SB_.PCI0.LCP0 */
ElseIf (Arg0 == One)
Local6 = LCP1 /* \_SB_.PCI0.LCP1 */
ElseIf (Arg0 == 0x02)
Local6 = LCP2 /* \_SB_.PCI0.LCP2 */
If (Arg0 == Zero)
If (P0UB == 0xFF)
Local5 = GULC (Local6)
Local7 = (Local5 / 0x02)
ElseIf (P0UB != Zero)
Local7 = P0UB /* \P0UB */
ElseIf (Arg0 == One)
If (P1UB == 0xFF)
Local5 = GULC (Local6)
Local7 = (Local5 / 0x02)
ElseIf (P1UB != Zero)
Local7 = P1UB /* \P1UB */
ElseIf (Arg0 == 0x02)
If (P2UB == 0xFF)
Local5 = GULC (Local6)
Local7 = (Local5 / 0x02)
ElseIf (P2UB != Zero)
Local7 = P2UB /* \P2UB */
Local3 = (CPEX & 0x0F)
If (Local3 == Zero)
If (Arg0 == Zero)
P0L0 = One
Local0 = Zero
ElseIf (Arg0 == One)
P1L0 = One
Local0 = Zero
ElseIf (Arg0 == 0x02)
P2L0 = One
Local0 = Zero
ElseIf (Local3 != Zero)
If (Arg0 == Zero)
Q0L0 = One
Local0 = Zero
ElseIf (Arg0 == One)
Q1L0 = One
Local0 = Zero
ElseIf (Arg0 == 0x02)
Q2L0 = One
Local0 = Zero
Local3 = (CPEX & 0x0F)
If (Local3 == Zero)
If (Arg0 == Zero)
P0L2 = One
Local0 = Zero
ElseIf (Arg0 == One)
P1L2 = One
Local0 = Zero
ElseIf (Arg0 == 0x02)
P2L2 = One
Local0 = Zero
ElseIf (Local3 != Zero)
If (Arg0 == Zero)
Q0L2 = One
Local0 = Zero
ElseIf (Arg0 == One)
Q1L2 = One
Local0 = Zero
ElseIf (Arg0 == 0x02)
Q2L2 = One
Local0 = Zero
If (Arg0 == Zero)
ElseIf (Arg0 == One)
ElseIf (Arg0 == 0x02)
If (Arg0 == Zero)
Local0 = One
ElseIf (Arg0 == One)
Local0 = One
If (Arg0 == 0x02)
Local0 = One
If (Arg0 == Zero)
Local0 = Zero
ElseIf (Arg0 == One)
Local0 = One
If (Arg0 == 0x02)
Local0 = 0x02
If (Arg0 == Zero)
Local7 = P0VI /* \_SB_.PCI0.P0VI */
ElseIf (Arg0 == One)
Local7 = P1VI /* \_SB_.PCI0.P1VI */
ElseIf (Arg0 == 0x02)
Local7 = P2VI /* \_SB_.PCI0.P2VI */
If (Local7 == IVID)
If (Arg0 != Zero)
Local7 = P0VI /* \_SB_.PCI0.P0VI */
If (Local7 == IVID)
If (Arg1 == Zero)
If (Arg0 == Zero)
If (SGPI (SGGP, PWE0, PWG0, PWA0) == Zero)
If (Arg0 == One)
If (SGPI (PIGP, PWE1, PWG1, PWA1) == Zero)
If (Arg0 == 0x02)
If (SGPI (P2GP, PWE2, PWG2, PWA2) == Zero)
ElseIf (Arg1 == One)
If (Arg0 == Zero)
If (SGPI (SGGP, PWE0, PWG0, PWA0) == One)
If (Arg0 == One)
If (SGPI (PIGP, PWE1, PWG1, PWA1) == One)
If (Arg0 == 0x02)
If (SGPI (P2GP, PWE2, PWG2, PWA2) == One)
If (Arg0 == Zero)
ElseIf (Arg0 == One)
ElseIf (Arg0 == 0x02)
If (Arg1 == Zero)
If (Arg0 == Zero)
If (Arg0 == One)
If (Arg0 == 0x02)
ElseIf (Arg1 == One)
If (Arg0 == Zero)
If (Arg0 == One)
If (Arg0 == 0x02)

```

```

If (Arg3 == Zero)
    Arg4 = ~Arg4
    Arg4 &= One
If (Arg0 == One)
ElseIf (Arg0 == 0x02)
If (Arg0 == One)
    Local0 = \_SB.GG0V (Arg2)
ElseIf (Arg0 == 0x02)
    Local0 = \_SB.PCI0.GEXP.GEPS (Arg1, Arg2)
If (Arg3 == Zero)
    Local0 = ~Local0
    Local0 &= One
Local0 = CPDC (Arg0)
Local0 = COSC (Arg0, Arg1, Arg2, Arg3)
Local0 = SizeOf (Arg0)
Local1 = (Local0 - 0x08)
If (!( ((IID0 == EID0) && (IID1 == EID1)) && ((
    IID2 == EID2) && (IID3 == EID3))))
    STS0 = 0x06
If (Arg1 != One)
    STS0 = 0x0A
    \_PR.HDCE = Zero
    IOB2 = 0x28
If ((STS0 == 0x06) || (STS0 == 0x0A))
    CAP0 &= 0x0BFF
PDC0 = ((PDC0 & 0x7FFFFFFF) | CAP0) /* \_PR_.CPU0.GCAP.CAP0 */
    SDTL |= 0x02
    SDTL |= 0x08
If (OSYS == 0x07DF)
    SDTL |= 0x40
    HWPV = Zero
    HWPV = 0x02
    IOB2 = 0x27
Local0 = \_PR.CPU0.CPDC (Arg0)
Local0 = \_PR.CPU0.COSC (Arg0, Arg1, Arg2, Arg3)
If ((STS1 == 0x06) || (STS1 == 0x0A))
    CAP1 &= 0x0BFF
PDC1 = ((PDC1 & 0x7FFFFFFF) | CAP1) /* \_PR_.CPU1.GCAP.CAP1 */
If ((PDC1 & 0x09) == 0x09)
PDC0 = PDC1 /* \PDC1 */
    SDTL |= 0x20
    SDTL |= 0x10
If (OSYS == 0x07DF)
    SDTL |= 0x80
Local0 = \_PR.CPU0.CPDC (Arg0)
Local0 = \_PR.CPU0.COSC (Arg0, Arg1, Arg2, Arg3)
If ((STS2 == 0x06) || (STS2 == 0x0A))
    CAP2 &= 0x0BFF
PDC2 = ((PDC2 & 0x7FFFFFFF) | CAP2) /* \_PR_.CPU2.GCAP.CAP2 */
If ((PDC2 & 0x09) == 0x09)
PDC0 = PDC2 /* \PDC2 */
Local0 = \_PR.CPU0.CPDC (Arg0)
Local0 = \_PR.CPU0.COSC (Arg0, Arg1, Arg2, Arg3)
If ((STS3 == 0x06) || (STS3 == 0x0A))
    CAP3 &= 0x0BFF
PDC3 = ((PDC3 & 0x7FFFFFFF) | CAP3) /* \_PR_.CPU3.GCAP.CAP3 */
If ((PDC3 & 0x09) == 0x09)
PDC0 = PDC3 /* \PDC3 */
Local0 = \_PR.CPU0.CPDC (Arg0)
Local0 = \_PR.CPU0.COSC (Arg0, Arg1, Arg2, Arg3)
If ((STS4 == 0x06) || (STS4 == 0x0A))
    CAP4 &= 0x0BFF
PDC4 = ((PDC4 & 0x7FFFFFFF) | CAP4) /* \_PR_.CPU4.GCAP.CAP4 */
If ((PDC4 & 0x09) == 0x09)
PDC0 = PDC4 /* \PDC4 */
Local0 = \_PR.CPU0.CPDC (Arg0)
Local0 = \_PR.CPU0.COSC (Arg0, Arg1, Arg2, Arg3)
If ((STS5 == 0x06) || (STS5 == 0x0A))
    CAP5 &= 0x0BFF
PDC5 = ((PDC5 & 0x7FFFFFFF) | CAP5) /* \_PR_.CPU5.GCAP.CAP5 */
If ((PDC5 & 0x09) == 0x09)
PDC0 = PDC5 /* \PDC5 */
Local0 = \_PR.CPU0.CPDC (Arg0)
Local0 = \_PR.CPU0.COSC (Arg0, Arg1, Arg2, Arg3)
If ((STS6 == 0x06) || (STS6 == 0x0A))
    CAP6 &= 0x0BFF
PDC6 = ((PDC6 & 0x7FFFFFFF) | CAP6) /* \_PR_.CPU6.GCAP.CAP6 */
If ((PDC6 & 0x09) == 0x09)
PDC0 = PDC6 /* \PDC6 */
Local0 = \_PR.CPU0.CPDC (Arg0)
Local0 = \_PR.CPU0.COSC (Arg0, Arg1, Arg2, Arg3)
If ((STS7 == 0x06) || (STS7 == 0x0A))
    CAP7 &= 0x0BFF
PDC7 = ((PDC7 & 0x7FFFFFFF) | CAP7) /* \_PR_.CPU7.GCAP.CAP7 */
If ((PDC7 & 0x09) == 0x09)
PDC0 = PDC7 /* \PDC7 */
    LFMI = SizeOf (\_PR.CPU0.TPSS)
    LFMP = DereFOF (DereFOF (\_PR.CPU0.TPSS [LFMI]) [One])
    LFMI = SizeOf (\_PR.CPU0.LPSS)
    LFMP = DereFOF (DereFOF (\_PR.CPU0.LPSS [LFMI]) [One])
    Local1 = RefOf (TSMF)
    Local2 = SizeOf (TSMF)
    Local1 = RefOf (TSMC)
    Local2 = SizeOf (TSMC)
Local0 = Zero
    Local14 = ((LFMP * (Local2 - Local0)) / Local2)
    DereFOF (DereFOF (Local1) [Local0]) [One] = Local14
TSSF = Ones

```

# ネットワークインターフェースデータ詳細一覧

```
bge0: flags=8802<BROADCAST,SIMPLEX,MULTICAST> metric 0 mtu 1500
options=c019b<RXCSUM, TXCSUM, VLAN_MTU, VLAN_HWTAGGING, VLAN_HWCSUM, TS04, VLAN_HWTSO, LINKSTATE>
ether 8c:df:9d:81:8d:40
nd6 options=29<PERFORMNUD, IFDISABLED, AUTO_LINKLOCAL>
media: Ethernet autoselect
bge1: flags=8802<BROADCAST,SIMPLEX,MULTICAST> metric 0 mtu 1500
options=c019b<RXCSUM, TXCSUM, VLAN_MTU, VLAN_HWTAGGING, VLAN_HWCSUM, TS04, VLAN_HWTSO, LINKSTATE>
ether 8c:df:9d:81:8d:41
nd6 options=29<PERFORMNUD, IFDISABLED, AUTO_LINKLOCAL>
media: Ethernet autoselect
bge2: flags=8802<BROADCAST,SIMPLEX,MULTICAST> metric 0 mtu 1500
options=c019b<RXCSUM, TXCSUM, VLAN_MTU, VLAN_HWTAGGING, VLAN_HWCSUM, TS04, VLAN_HWTSO, LINKSTATE>
ether 8c:df:9d:81:8d:42
nd6 options=29<PERFORMNUD, IFDISABLED, AUTO_LINKLOCAL>
media: Ethernet autoselect
bge3: flags=8802<BROADCAST,SIMPLEX,MULTICAST> metric 0 mtu 1500
options=c019b<RXCSUM, TXCSUM, VLAN_MTU, VLAN_HWTAGGING, VLAN_HWCSUM, TS04, VLAN_HWTSO, LINKSTATE>
ether 8c:df:9d:81:8d:43
nd6 options=29<PERFORMNUD, IFDISABLED, AUTO_LINKLOCAL>
media: Ethernet autoselect
bge4: flags=8802<BROADCAST,SIMPLEX,MULTICAST> metric 0 mtu 1500
options=c019b<RXCSUM, TXCSUM, VLAN_MTU, VLAN_HWTAGGING, VLAN_HWCSUM, TS04, VLAN_HWTSO, LINKSTATE>
ether 8c:df:9d:81:8d:4c
nd6 options=29<PERFORMNUD, IFDISABLED, AUTO_LINKLOCAL>
media: Ethernet autoselect
bge5: flags=8802<BROADCAST,SIMPLEX,MULTICAST> metric 0 mtu 1500
options=c019b<RXCSUM, TXCSUM, VLAN_MTU, VLAN_HWTAGGING, VLAN_HWCSUM, TS04, VLAN_HWTSO, LINKSTATE>
ether 8c:df:9d:81:8d:4d
nd6 options=29<PERFORMNUD, IFDISABLED, AUTO_LINKLOCAL>
media: Ethernet autoselect
bge6: flags=8802<BROADCAST,SIMPLEX,MULTICAST> metric 0 mtu 1500
options=c019b<RXCSUM, TXCSUM, VLAN_MTU, VLAN_HWTAGGING, VLAN_HWCSUM, TS04, VLAN_HWTSO, LINKSTATE>
ether 8c:df:9d:81:8d:4e
nd6 options=29<PERFORMNUD, IFDISABLED, AUTO_LINKLOCAL>
media: Ethernet autoselect
bge7: flags=8802<BROADCAST,SIMPLEX,MULTICAST> metric 0 mtu 1500
options=c019b<RXCSUM, TXCSUM, VLAN_MTU, VLAN_HWTAGGING, VLAN_HWCSUM, TS04, VLAN_HWTSO, LINKSTATE>
ether 8c:df:9d:81:8d:4f
nd6 options=29<PERFORMNUD, IFDISABLED, AUTO_LINKLOCAL>
media: Ethernet autoselect
bge8: flags=8843<UP, BROADCAST, RUNNING, SIMPLEX, MULTICAST> metric 0 mtu 1500
options=c019b<RXCSUM, TXCSUM, VLAN_MTU, VLAN_HWTAGGING, VLAN_HWCSUM, TS04, VLAN_HWTSO, LINKSTATE>
ether 40:8d:5c:a9:e5:33
inet 192.168.10.103 netmask 0xfffff00 broadcast 192.168.10.255
nd6 options=29<PERFORMNUD, IFDISABLED, AUTO_LINKLOCAL>
media: Ethernet autoselect (1000baseT <full-duplex>)
status: active
bge9: flags=8802<BROADCAST,SIMPLEX,MULTICAST> metric 0 mtu 1500
options=c019b<RXCSUM, TXCSUM, VLAN_MTU, VLAN_HWTAGGING, VLAN_HWCSUM, TS04, VLAN_HWTSO, LINKSTATE>
ether 40:8d:5c:a9:e5:34
nd6 options=29<PERFORMNUD, IFDISABLED, AUTO_LINKLOCAL>
media: Ethernet autoselect
lo0: flags=8049<UP, LOOPBACK, RUNNING, MULTICAST> metric 0 mtu 16384
options=600003<RXCSUM, TXCSUM, RXCSUM_IPV6, TXCSUM_IPV6>
inet6 ::1 prefixlen 128
inet6 fe80::1%lo0 prefixlen 64 scopeid 0xb
inet 127.0.0.1 netmask 0xff000000
nd6 options=21<PERFORMNUD, AUTO_LINKLOCAL>
groups: lo
```

# デバイス認識詳細一覧

2	1 dr-xr-xr-x	11 root	wheel	512 Feb 4 02:26 /dev
117	0 lrwxr-xr-x	1 root	wheel	12 Feb 3 17:26 /dev/Log -> /var/run/Log
96	0 lrwxr-xr-x	1 root	wheel	11 Feb 4 02:26 /dev/dmptdev -> /dev/da0s1b
92	1 dr-xr-xr-x	2 root	wheel	512 Feb 3 17:26 /dev/reroot
3	0 crw-----	1 root	wheel	0x3 Feb 3 17:26 /dev/reroot/reroot
4	0 crw-r-----	1 root	operator	0x4 Feb 3 17:26 /dev/geom.ctl
5	0 crw-r--r--	1 root	wheel	0x5 Feb 3 17:26 /dev/sndstat
6	0 crw-r--r--	1 root	wheel	0x6 Feb 3 17:26 /dev/random
7	0 lrwxr-xr-x	1 root	wheel	6 Feb 3 17:26 /dev/urandom -> random
8	0 crw-----	1 root	wheel	0x8 Feb 3 17:44 /dev/console
9	0 crw-----	1 root	wheel	0x9 Feb 3 17:26 /dev/devctl
10	0 crw-----	1 root	wheel	0xa Feb 3 17:26 /dev/devctl2
11	0 crw-rw-rw-	1 root	wheel	0xb Feb 3 17:26 /dev/tty
12	0 crw-----	1 root	wheel	0xc Feb 3 17:26 /dev/bpf
13	0 lrwxr-xr-x	1 root	wheel	3 Feb 3 17:26 /dev/bpf0 -> bpf
14	0 crw-----	1 root	wheel	0xe Feb 3 17:26 /dev/conslectl
15	0 crw-----	1 root	wheel	0xf Feb 3 17:26 /dev/sysmouse
16	0 crw-----	1 root	wheel	0x10 Feb 3 17:26 /dev/fido
17	0 crw-----	1 root	wheel	0x11 Feb 3 17:26 /dev/kbdmux0
18	0 lrwxr-xr-x	1 root	wheel	7 Feb 3 17:26 /dev/kbd1 -> kbdmux0
19	0 crw-r-----	1 root	kmem	0x13 Feb 3 17:26 /dev/mem
20	0 crw-r-----	1 root	kmem	0x14 Feb 3 17:26 /dev/kmem
21	0 crw-----	1 root	kmem	0x15 Feb 3 17:26 /dev/nfslock
22	0 crw-----	1 root	wheel	0x16 Feb 3 17:26 /dev/auditpipe
23	0 crw-----	1 root	kmem	0x17 Feb 3 17:26 /dev/audit
24	0 crw-----	1 root	wheel	0x18 Feb 3 17:26 /dev/netmap
25	0 crw-rw-rw-	1 root	wheel	0x19 Feb 3 17:26 /dev/full
26	0 crw-rw-rw-	1 root	wheel	0x1a Feb 3 17:44 /dev/null
27	0 crw-rw-rw-	1 root	wheel	0x1b Feb 3 17:26 /dev/zero
93	1 dr-xr-xr-x	2 root	wheel	512 Feb 3 17:26 /dev/fd
28	0 crw-rw-rw-	1 root	wheel	0x1c Feb 3 17:26 /dev/fd/0
30	0 crw-rw-rw-	1 root	wheel	0x1e Feb 3 17:26 /dev/fd/1
32	0 crw-rw-rw-	1 root	wheel	0x20 Feb 3 17:26 /dev/fd/2
29	0 lrwxr-xr-x	1 root	wheel	4 Feb 3 17:26 /dev/stdin -> fd/0
31	0 lrwxr-xr-x	1 root	wheel	4 Feb 3 17:26 /dev/stdout -> fd/1
33	0 lrwxr-xr-x	1 root	wheel	4 Feb 3 17:26 /dev/stderr -> fd/2
34	0 crw-r--r--	1 root	wheel	0x22 Feb 3 17:26 /dev/pci
35	0 crw-----	1 root	wheel	0x23 Feb 3 17:26 /dev/io
36	0 crw-----	1 root	wheel	0x24 Feb 3 17:26 /dev/klog
37	0 crw-rw-rw-	1 root	wheel	0x25 Feb 3 17:26 /dev/midstat
38	0 crw-r--r--	1 root	wheel	0x26 Feb 3 17:26 /dev/acpi
39	0 crw-rw-rw-	1 root	operator	0x27 Feb 3 17:26 /dev/apmctl
40	0 crw-rw-rw-	1 root	operator	0x28 Feb 3 17:26 /dev/apm
41	0 crw-----	1 root	wheel	0x29 Feb 3 17:26 /dev/hpet0
94	1 dr-xr-xr-x	2 root	wheel	512 Feb 3 17:26 /dev/Led
42	0 crw-----	1 root	wheel	0x2a Feb 3 17:26 /dev/Led/ahci0.0.locate
43	0 crw-----	1 root	wheel	0x2b Feb 3 17:26 /dev/Led/ahci0.0.fault
44	0 crw-----	1 root	wheel	0x2c Feb 3 17:26 /dev/Led/ahci0.1.locate
45	0 crw-----	1 root	wheel	0x2d Feb 3 17:26 /dev/Led/ahci0.1.fault
46	0 crw-----	1 root	wheel	0x2e Feb 3 17:26 /dev/Led/ahci0.2.locate
47	0 crw-----	1 root	wheel	0x2f Feb 3 17:26 /dev/Led/ahci0.2.fault
48	0 crw-----	1 root	wheel	0x30 Feb 3 17:26 /dev/Led/ahci0.3.locate
49	0 crw-----	1 root	wheel	0x31 Feb 3 17:26 /dev/Led/ahci0.3.fault
50	0 crw-----	1 root	wheel	0x32 Feb 3 17:26 /dev/Led/ahci0.4.locate
51	0 crw-----	1 root	wheel	0x33 Feb 3 17:26 /dev/Led/ahci0.4.fault
52	0 crw-----	1 root	wheel	0x34 Feb 3 17:26 /dev/tty0
53	0 crw-----	1 root	wheel	0x35 Feb 3 17:26 /dev/tty0.init
54	0 crw-----	1 root	wheel	0x36 Feb 3 17:26 /dev/tty0.lock
55	0 crw-rw-rw-	1 uucp	dioler	0x37 Feb 3 17:26 /dev/cuau0
56	0 crw-rw-rw-	1 uucp	dioler	0x38 Feb 3 17:26 /dev/cuau0.init
57	0 crw-rw-rw-	1 uucp	dioler	0x39 Feb 3 17:26 /dev/cuau0.lock
58	0 crw-----	1 root	wheel	0x3a Feb 3 17:26 /dev/ttyu1
59	0 crw-----	1 root	wheel	0x3b Feb 3 17:26 /dev/ttyu1.init
60	0 crw-----	1 root	wheel	0x3c Feb 3 17:26 /dev/ttyu1.lock
61	0 crw-rw-rw-	1 uucp	dioler	0x3d Feb 3 17:26 /dev/cuau1
62	0 crw-rw-rw-	1 uucp	dioler	0x3e Feb 3 17:26 /dev/cuau1.init
63	0 crw-rw-rw-	1 uucp	dioler	0x3f Feb 3 17:26 /dev/cuau1.lock
64	0 crw-----	1 root	wheel	0x40 Feb 3 17:26 /dev/ufssuspend
67	0 crw-r--r--	1 root	operator	0x43 Feb 3 17:26 /dev/usbctl
68	0 crw-----	1 root	operator	0x44 Feb 3 17:26 /dev/xpt0
69	0 crw-rw-rw-	1 root	operator	0x45 Feb 3 17:26 /dev/mrsas0
70	0 lrwxr-xr-x	1 root	wheel	6 Feb 3 17:26 /dev/megaraid_sas_ioctl_node -> mrsas0
71	0 crw-----	1 root	wheel	0x47 Feb 3 17:26 /dev/mdctl
95	1 dr-xr-xr-x	2 root	wheel	512 Feb 3 17:26 /dev/usb
72	0 crw-----	1 root	operator	0x48 Feb 3 17:26 /dev/usb/0.1.0
74	0 crw-----	1 root	operator	0x4a Feb 3 17:26 /dev/usb/0.1.1
99	0 crw-----	1 root	operator	0x63 Feb 3 17:26 /dev/usb/0.2.0
104	0 crw-----	1 root	operator	0x68 Feb 3 17:26 /dev/usb/0.2.1
105	0 crw-----	1 root	operator	0x69 Feb 3 17:26 /dev/usb/0.3.0
107	0 crw-----	1 root	operator	0x6b Feb 3 17:26 /dev/usb/0.3.1
110	0 crw-----	1 root	operator	0x6e Feb 3 17:26 /dev/usb/0.4.0
112	0 crw-----	1 root	operator	0x70 Feb 3 17:26 /dev/usb/0.4.1
113	0 crw-----	1 root	operator	0x71 Feb 3 17:26 /dev/usb/0.4.2
73	0 lrwxr-xr-x	1 root	wheel	9 Feb 3 17:26 /dev/ugen0.1 -> usb/0.1.0
75	0 cr--r--r--	1 root	wheel	0x4b Feb 3 17:26 /dev/devstat
76	0 crw-----	1 root	operator	0x4c Feb 3 17:26 /dev/pass0
77	0 crw-----	1 root	operator	0x4d Feb 3 17:26 /dev/pass1
78	0 crw-----	1 root	operator	0x4e Feb 3 17:26 /dev/pass2
79	0 crw-----	1 root	operator	0x4f Feb 3 17:26 /dev/ses0
80	0 crw-----	1 root	tty	0x50 Feb 3 17:45 /dev/ttyv0
81	0 crw-----	1 root	wheel	0x51 Feb 3 17:26 /dev/ttyv1
82	0 crw-----	1 root	wheel	0x52 Feb 3 17:26 /dev/ttyv2
83	0 crw-----	1 root	wheel	0x53 Feb 3 17:26 /dev/ttyv3
84	0 crw-----	1 root	wheel	0x54 Feb 3 17:26 /dev/ttyv4
85	0 crw-----	1 root	wheel	0x55 Feb 3 17:26 /dev/ttyv5
86	0 crw-----	1 root	wheel	0x56 Feb 3 17:26 /dev/ttyv6
87	0 crw-----	1 root	wheel	0x57 Feb 3 17:26 /dev/ttyv7
88	0 crw-----	1 root	wheel	0x58 Feb 3 17:26 /dev/ttyv8
89	0 crw-----	1 root	wheel	0x59 Feb 3 17:26 /dev/ttyv9
90	0 crw-----	1 root	wheel	0x5a Feb 3 17:26 /dev/ttyva
91	0 crw-----	1 root	wheel	0x5b Feb 3 17:26 /dev/ttyvb
97	0 crw-r-----	1 root	operator	0x61 Feb 3 17:26 /dev/da0
98	0 crw-r-----	1 root	operator	0x62 Feb 3 17:26 /dev/da0s1
100	0 crw-r-----	1 root	operator	0x64 Feb 3 17:26 /dev/cd0
101	0 crw-r-----	1 root	operator	0x65 Feb 4 02:26 /dev/da0s1a
102	0 crw-r-----	1 root	operator	0x66 Feb 4 02:26 /dev/da0s1b
103	0 lrwxr-xr-x	1 root	wheel	9 Feb 3 17:26 /dev/ugen0.2 -> usb/0.2.0
106	0 lrwxr-xr-x	1 root	wheel	9 Feb 3 17:26 /dev/ugen0.3 -> usb/0.3.0
108	0 crw-----	1 root	wheel	0x6c Feb 3 17:26 /dev/ukbd0
109	0 lrwxr-xr-x	1 root	wheel	5 Feb 3 17:26 /dev/kbd2 -> ukbd0
111	0 lrwxr-xr-x	1 root	wheel	9 Feb 3 17:26 /dev/ugen0.4 -> usb/0.4.0
114	0 crw-----	1 root	wheel	0x72 Feb 3 17:26 /dev/ukbd1
115	0 lrwxr-xr-x	1 root	wheel	5 Feb 3 17:26 /dev/kbd3 -> ukbd1
116	0 crw-r--r--	1 root	operator	0x74 Feb 3 17:26 /dev/ums0





## 更新履歷

## 更新履歴

- 2017年02月03日 FreeBSD動作検証レポート2017年2月3日版作成

# FreeBSD動作検証レポート

## T110h-S / 11.0-RELEASE-p1

### 対象ハードウェア

maker:NEC, product:Express5800/T110h-S [N8100-2310Y],  
serial:0000003, version:FR1.5

### 対象OSバージョン

FreeBSD 11.0-RELEASE-p1 #0 r306420: Thu Sep 29 01:43:23 UTC 2016

2017年2月3日版

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