

FreeBSD動作検証レポート

R110g-1E / 10.2-RELEASE

対象ハードウェア

maker:NEC, product:Express5800/R110g-1E
[N8100-2174Y], serial:4800112, version:FR1.0

対象OSバージョン

FreeBSD 10.2-RELEASE #0: Wed Nov 11 16:02:59
JST 2015

2015年11月11日版

BSDコンサルティング株式会社

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動作検証レポート

要約

ハードウェア基本情報

製品UUID	00ebd656-8e31-e411-8001-94de80ff25c3
製品データ	maker:NEC, product:Express5800/R110g-1E [N8100-2174Y], serial:4800112, version:FR1.0
筐体データ	maker:NEC, serial:01, tag:_____, version:856-180006-502
CPU	Intel(R) Pentium(R) CPU G3240 @ 3.10GHz
MEMORY	8388608[KB]
BIOS	reldate:05/22/2014, vendor:American Megatrends Inc., version:4.6.0203
ACPI	oem:AMI , revision:2, rsdp:0x000f0490, rsdt:0xcc7f3028, xsdt:0x00000000cc7f3090, disabled:1
OS	FreeBSD 10.2-RELEASE #0: Wed Nov 11 16:02:59 JST 2015

基礎性能データ

メモリアクセス速度	27.295 GiB/sec
IPv4スタック処理速度	281.493 MiB/sec
IPv6スタック処理速度	290.745 MiB/sec

動作検証要約

FreeBSD 10.2-RELEASE #0: Wed Nov 11 16:02:59 JST 2015は、検証対象ハードウェア「maker:NEC, product:Express5800/R110g-1E [N8100-2174Y], serial:4800112, version:FR1.0」において正常に動作する。

インストールしたFreeBSD 10.2-RELEASE #0: Wed Nov 11 16:02:59 JST 2015をFreeBSD 11.0-RELEASEやそれ以降のバージョンにアップグレードする場合には注意が必要。FreeBSD 11.0-RELEASEにはLSI MegaRAID SASドライバとしてmfi(4)ではなくmrsas(4)がデフォルトでアタッチされるように挙動が変更される可能性がある。

LSI MegaRAID SASドライバとしてmrsas(4)が使われた場合、/dev/以下に表示されるファイル名は次のように変更される見通し。

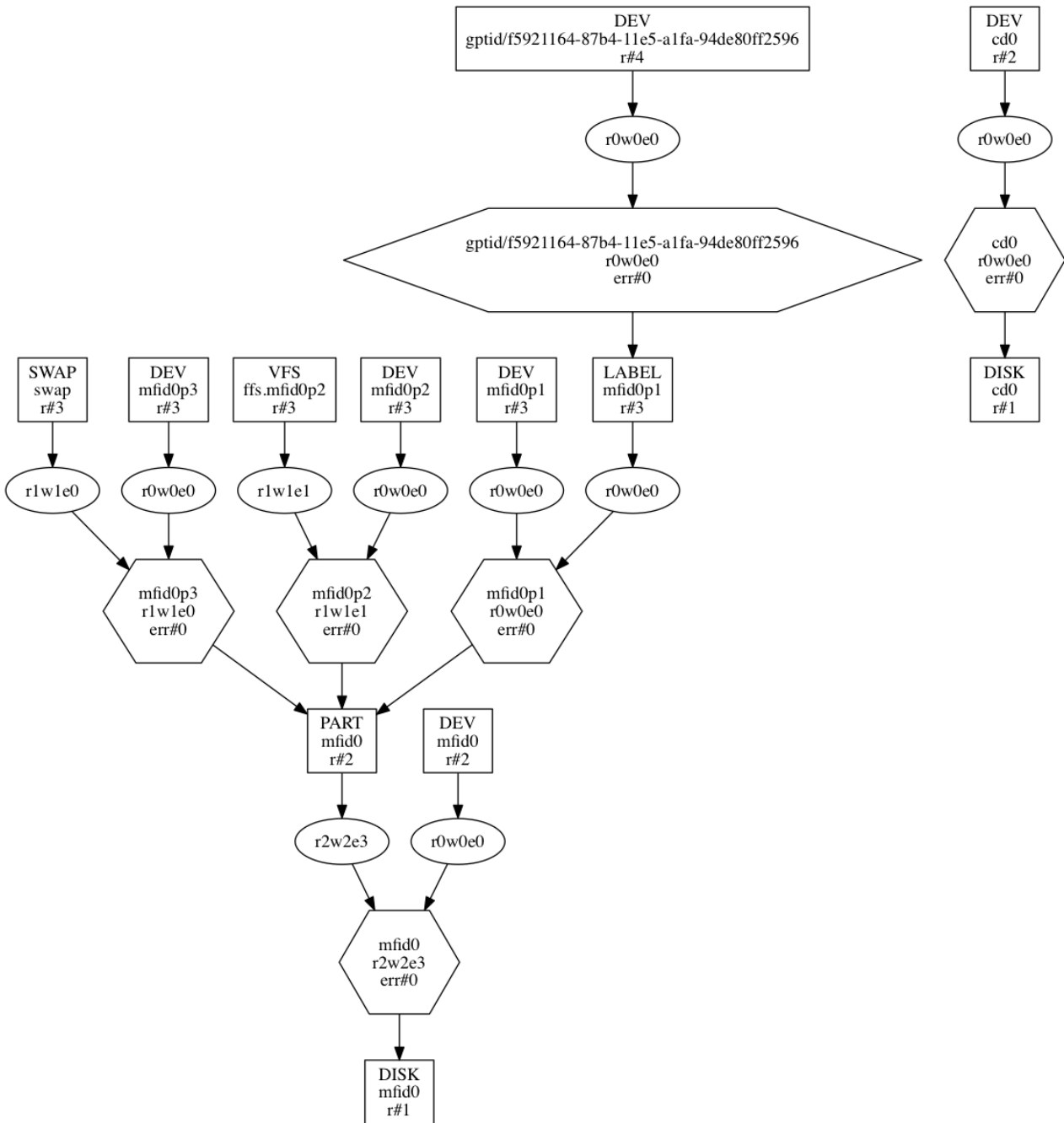
```
mfi0    → mrsas0
mfid0   → da0
mfid0s1 → da0s1a
mfid0s2 → da0s1b
```

論理パーティションの表示や名称は今後変更される可能性もあり、上記とは違う名前になる可能性もある。アップグレード対象のバージョンのリリースノートを参考にするとともに、適切な名称で/etc/fstabを書き換えるなどしてアップグレードを実施

する必要がある。

FreeBSDプロジェクトは、FreeBSD 11.0-RELEASE以降はブランチでの最低5年間サポートの開始を予定している。FreeBSD 11.0-RELEASE以降は、ブランチにおける最新のリリースバージョンのみがサポートの対象となる。このため、FreeBSD 11.0-RELEASE以降はマイナーアップグレードごとにアップグレードを実施し、FreeBSD 11.1-RELEASE、FreeBSD 11.2-RELEASEへとバージョンアップを実施する必要がある。カーネルをカスタマイズしない限り、`freebsd-update(8)`コマンドを使ってバイナリアップデートを実施できる。

検証機GEOMツリー



高負荷耐久試験

高負荷耐久試験

試験結果

検証対象ハードウェア(maker:NEC, product:Express5800/R110g-1E [N8100-2174Y], serial:4800112, version:FR1.0)は24時間高負荷耐久試験をクリアしており、所定の負荷に対して安定して動作するものとみられる。

不正処理高負荷耐久試験	クリア
通常ファイル作成高負荷耐久試験	クリア
FIFOファイル作成高負荷耐久試験	クリア
ディレクトリ作成高負荷耐久試験	クリア
ハードリンクファイル作成高負荷耐久試験	クリア
シンボリックリンクファイル作成高負荷耐久試験	クリア
ファイルロック機構高負荷耐久試験	クリア
メモリファイルマッピング機能高負荷耐久試験	クリア
ファイルシステム階層構造トラバース高負荷耐久試験	クリア
ファイルシステムパス解析高負荷耐久試験	クリア
ファイルシステムパスリネーム高負荷耐久試験	クリア
ファイルシステムライト・キャッシュリード高負荷耐久試験	クリア
ファイルシステムスワップ高負荷耐久試験	クリア
スレッド機構高負荷耐久試験	クリア
間接システムコール高負荷耐久試験	クリア
擬似ターミナル機構高負荷耐久試験	クリア
共有メモリ機構高負荷耐久試験	クリア
ネットワークソケット高負荷耐久試験	クリア
TCPスタック高負荷耐久試験	クリア
UDPスタック高負荷耐久試験	クリア

動作検証レポート

動作検証レポート

ACPI認識データ

ACPI_APIC	Revision=3, OEMID=AMI, OEM_Table ID=06A0, OEM_Revision=0x1072009, Creator_ID=AMI, Creator_Revision=0x10013
ACPI_BERT	<p>Revision=1, OEMID=AMI, OEM_Table ID=AMI BERT, OEM_Revision=0x0, Creator_ID=AMI, Creator_Revision=0x10013, 0x52, Zero)& 0x2000) == Zero)), 0x02)= 0xFFFFFFFF, 0x02)= 0xFFFFFFFF, 0x02)= DAT /Applications /Library /Network /System /Users /Volumes /bin /cores /dev /etc /home /installer.failurerequests /net /opt /private /sbin /tmp /usr /var /ユーザ情報 _SB_.PCI0.LPCB.TPM_.DAT_ *, 0x02)= 0xFFFFFFFF, 0x02)= 0xFFFFFFFF, 0x02)= DAT /Applications /Library /Network /System /Users /Volumes /bin /cores /dev /etc /home /installer.failurerequests /net /opt /private /sbin /tmp /usr /var /ユーザ情報 _SB_.PCI0.ITPM.DAT_ *, Zero)= 0x05, 0x02)= Zero, 0x03)= 0x00015F90, 0x04)= 0x03E8, 0x05)= MINA /Applications /Library /Network /System /Users /Volumes /bin /cores /dev /etc /home /installer.failurerequests /net /opt /private /sbin /tmp /usr /var /ユーザ情報 _SB_.PMIO.MINA *, 0x06)= MAXA /Applications /Library /Network /System /Users /Volumes /bin /cores /dev /etc /home /installer.failurerequests /net /opt /private /sbin /tmp /usr /var /ユーザ情報 _SB_.PMIO.MAXA *, 0x07)= 0xFFFFFFFF, 0x08)= 0xFFFFFFFF, 0x09)= MINL /Applications /Library /Network /System /Users /Volumes /bin /cores /dev /etc /home /installer.failurerequests /net /opt /private /sbin /tmp /usr /var /ユーザ情報 _SB_.PMIO.MINL *, 0x0A)= MAXL /Applications /Library /Network /System /Users /Volumes /bin /cores /dev /etc /home /installer.failurerequests /net /opt /private /sbin /tmp /usr /var /ユーザ情報 _SB_.PMIO.MAXL *, 0x0B)= "Model", 0x0C)= "Serial", 0x0D)= "OEMInfo", 0x04)= TCNT /Applications /Library /Network /System /Users /Volumes /bin /cores /dev /etc /home /installer.failurerequests /net /opt /private /sbin /tmp /usr /var /ユーザ情報 TCNT *, 0x04)= TCNT /Applications /Library /Network /System /Users /Volumes /bin /cores /dev /etc /home /installer.failurerequests /net /opt /private /sbin /tmp /usr /var /ユーザ情報 TCNT *, 0x04)= TCNT /Applications /Library /Network /System /Users /Volumes /bin /cores /dev /etc /home /installer.failurerequests /net /opt /private /sbin /tmp /usr /var /ユーザ情報 TCNT *, Zero)= ((SMSL >> 0x0A) & 0x07), One)= (SMSL & 0x03FF), 0x02)= ((SNSL >> 0x0A) & 0x07), 0x03)= (SNSL & 0x03FF), Zero)= ((SMSL >> 0x0A) & 0x07), One)= (SMSL & 0x03FF), 0x02)= ((SNSL >> 0x0A) & 0x07), 0x03)= (SNSL & 0x03FF), Zero)= (0x00010000 DID1), Zero)= (0x00010000 DID1), One)= (0x00010000 DID2), Zero)= (0x00010000 DID1), One)= (0x00010000 DID2), 0x02)= (0x00010000 DID3), Zero)= (0x00010000 DID1), One)= (0x00010000 DID2), 0x02)= (0x00010000 DID3), 0x03)= (0x00010000 DID4), Zero)= (0x00010000 DID1), One)= (0x00010000 DID2), 0x02)= (0x00010000 DID3), 0x03)= (0x00010000 DID4), 0x04)= (0x00010000 DID5), Zero)= (0x00010000 DID1), One)= (0x00010000 DID2), 0x02)= (0x00010000 DID3), 0x03)= (0x00010000 DID4), 0x04)= (0x00010000 DID5), 0x05)= (0x00010000 DID6), Zero)= (0x00010000 DID1), One)= (0x00010000 DID2), 0x02)= (0x00010000 DID3), 0x03)= (0x00010000 DID4), 0x04)= (0x00010000 DID5), 0x05)= (0x00010000 DID6), 0x06)= (0x00010000 DID7), Zero)= (0x00010000 DID1), One)= (0x00010000 DID2), 0x02)= (0x00010000 DID3), 0x03)= (0x00010000 DID4), 0x04)= (0x00010000 DID5), 0x05)= (0x00010000 DID6), 0x06)= (0x00010000 DID7), 0x07)= (0x00010000 DID8), Zero)= (0x00010000 DID1), One)= (0x00010000 DID2), 0x02)= (0x00010000 DID3), 0x03)= (0x00010000 DID4), 0x04)= (0x00010000 DID5), 0x05)= (0x00010000 DID6), 0x06)= (0x00010000 DID7), 0x07)= (0x00010000 DID8), 0x08)= (0x00010000 DID9), Zero)= (0x00010000 DID1), One)= (0x00010000 DID2), 0x02)= (0x00010000 DID3), 0x03)= (0x00010000 DID4), 0x04)= (0x00010000 DID5), 0x05)= (0x00010000 DID6), 0x06)= (0x00010000 DID7), 0x07)= (0x00010000 DID8), 0x08)= (0x00010000 DID9), 0x09)= (0x00010000 DIDA), Zero)= (0x00010000 DID1), One)= (0x00010000 DID2), 0x02)= (0x00010000 DID3), 0x03)= (0x00010000 DID4), 0x04)= (0x00010000 DID5), 0x05)= (0x00010000 DID6), 0x06)= (0x00010000 DID7), 0x07)= (0x00010000 DID8), 0x08)= (0x00010000 DID9), 0x09)= (0x00010000 DIDA), 0x0A)= (0x00010000 DIDB), Zero)= (0x00010000 DID1), One)= (0x00010000 DID2), 0x02)= (0x00010000 DID3), 0x03)= (0x00010000 DID4), 0x04)=</p>

	(0x00010000 DID5), 0x05)= (0x00010000 DID6), 0x06)= (0x00010000 DID7), 0x07)= (0x00010000 DID8), 0x08)= (0x00010000 DID9), 0x09)= (0x00010000 DIDA), 0x0A)= (0x00010000 DIDB), 0x0B)= (0x00010000 DIDC), Zero)= (0x00010000 DID1), One)= (0x00010000 DID2), 0x02)= (0x00010000 DID3), 0x03)= (0x00010000 DID4), 0x04)= (0x00010000 DID5), 0x05)= (0x00010000 DID6), 0x06)= (0x00010000 DID7), 0x07)= (0x00010000 DID8), 0x08)= (0x00010000 DID9), 0x09)= (0x00010000 DIDA), 0x0A)= (0x00010000 DIDB), 0x0B)= (0x00010000 DIDC), 0x0C)= (0x00010000 DIDD), Zero)= (0x00010000 DID1), One)= (0x00010000 DID2), 0x02)= (0x00010000 DID3), 0x03)= (0x00010000 DID4), 0x04)= (0x00010000 DID5), 0x05)= (0x00010000 DID6), 0x06)= (0x00010000 DID7), 0x07)= (0x00010000 DID8), 0x08)= (0x00010000 DID9), 0x09)= (0x00010000 DIDA), 0x0A)= (0x00010000 DIDB), 0x0B)= (0x00010000 DIDC), 0x0C)= (0x00010000 DIDD), 0x0D)= (0x00010000 DIDE), Zero)= (0x00010000 DID1), One)= (0x00010000 DID2), 0x02)= (0x00010000 DID3), 0x03)= (0x00010000 DID4), 0x04)= (0x00010000 DID5), 0x05)= (0x00010000 DID6), 0x06)= (0x00010000 DID7), 0x07)= (0x00010000 DID8), 0x08)= (0x00010000 DID9), 0x09)= (0x00010000 DIDA), 0x0A)= (0x00010000 DIDB), 0x0B)= (0x00010000 DIDC), 0x0C)= (0x00010000 DIDD), 0x0D)= (0x00010000 DIDE), 0x0E)= (0x00010000 DIDE), /*_0060 */ 0x3C, 0x00, 0x01, 0x00, 0x3D, 0x00, 0x01, 0x00, /*_0060 */ 0x3C, 0x00, 0x01, 0x00, 0x3D, 0x00, 0x01, 0x00, /*_0060 */ 0x3C, 0x00, 0x01, 0x00, 0x3D, 0x00, 0x01, 0x00
ACPI_DSDT	Revision=2, OEMID=AMI, OEM_Table ID=06A0, OEM_Revision=0x0, Creator_ID=INTL, Creator_Revision=0x20091112
ACPI_FACP	Revision=5, OEMID=AMI, OEM_Table ID=06A0, OEM_Revision=0x1072009, Creator_ID=AMI, Creator_Revision=0x10013, FACS=0xccfa7080, DSDT=0xcc7f31b8, SMI_CMD=0xb2, ACPI_ENABLE=0xa0, ACPI_DISABLE=0xa1, S4BIOS_REQ=0x0, FLUSH_STRIDE=16, DUTY_WIDTH=0, RESET_VALUE=0xf7
ACPI_FACS	HwSig=0x0000001a, Firm_Wake_Vec=0x00000000
ACPI_FPDT	Revision=1, OEMID=AMI, OEM_Table ID=06A0, OEM_Revision=0x1072009, Creator_ID=AMI, Creator_Revision=0x10013
ACPI_HPET	Revision=1, OEMID=AMI, OEM_Table ID=06A0, OEM_Revision=0x1072009, Creator_ID=AMI, Creator_Revision=0x5
ACPI_MCFG	Revision=1, OEMID=AMI, OEM_Table ID=06A0, OEM_Revision=0x1072009, Creator_ID=MSFT, Creator_Revision=0x97
ACPI_PRAD	Revision=2, OEMID=PRADID, OEM_Table ID=PRADTID, OEM_Revision=0x1, Creator_ID=MSFT, Creator_Revision=0x3000001
ACPI_RSD_PTR	OEM=AMI, ACPI_Rev=2.0x_(2), XSDT=0x00000000cc7f3090
ACPI_SPCR	Revision=1, OEMID=A_M_I, OEM_Table ID=APTIO4, OEM_Revision=0x1072009, Creator_ID=AMI, Creator_Revision=0x5
ACPI_XSDT	Revision=1, OEMID=AMI, OEM_Table ID=06A0, OEM_Revision=0x1072009, Creator_ID=AMI, Creator_Revision=0x10013
ACPI_SSDT_1	Length=1411, Revision=1, Checksum=30, OEMID=PmRef, OEM_Table ID=Cpu0Ist, OEM_Revision=0x3000, Creator_ID=INTL, Creator_Revision=0x20051117
ACPI_SSDT_2	Length=2937, Revision=1, Checksum=202, OEMID=CpuRef, OEM_Table ID=CpuSsdT, OEM_Revision=0x3000, Creator_ID=INTL, Creator_Revision=0x20051117
ACPI_SSDT_3	Length=734, Revision=1, Checksum=191, OEMID=PmRef, OEM_Table ID=Cpu0Tst, OEM_Revision=0x3000, Creator_ID=INTL, Creator_Revision=0x20051117
ACPI_SSDT_4	Length=840, Revision=1, Checksum=74, OEMID=PmRef, OEM_Table ID=ApTst, OEM_Revision=0x3000, Creator_ID=INTL, Creator_Revision=0x20051117
ACPI_SSDT_5	Length=1173, Revision=1, Checksum=1, OEMID=IdeRef, OEM_Table ID=IdeTable, OEM_Revision=0x1000, Creator_ID=INTL, Creator_Revision=0x20120711
ACPI_SSDT_6	Length=23390, Revision=1, Checksum=21, OEMID=SaSsdT, OEM_Table ID=SaSsdT, OEM_Revision=0x3000, Creator_ID=INTL, Creator_Revision=0x20120711
ACPI_SSDT_7	Length=505, Revision=1, Checksum=51, OEMID=SgRef, OEM_Table ID=SgPeg, OEM_Revision=0x1000, Creator_ID=INTL, Creator_Revision=0x20120711

MFIデバイス認識データ

アダプタ	mfi0 Adapter:, Product Name: LSI MegaRAID SAS 9272-8i, Serial Number: SV42641826, Firmware: 23.11.0-0038, RAID Levels: JBOD, RAID0, RAID1, RAID5, RAID6, RAID10, RAID50, Battery Backup: present, NVRAM: 32K, Onboard Memory: 512M, Minimum Stripe: 8K, Maximum Stripe: 1M
ドライバ	mfi0 Physical Drives:, 8 (136G) ONLINE lt;HGST HUC156030CSS200 A30B serial=0TV3M0XLgt; SCSI-6 E1:S0
ボリューム	mfi0 Volumes:, Id Size Level Stripe State Cache Name, mfid0 (136G) RAID-0 256K OPTIMAL Disabled lt;array0gt;
コンフィグ	mfi0 Configuration: 1 arrays, 1 volumes, 0 spares, array 0 of 1 drives:, drive 8 (136G) ONLINE lt;HGST HUC156030CSS200 A30B serial=0TV3M0XLgt; SCSI-6, volume mfid0 (136G) RAID-0 256K OPTIMAL lt;array0gt; spans:, array 0

※ 対象となるデバイスがmfi(4)ドライバを使用しないストレージ構成の場合、当項目は該当データなし。

MPTデバイス認識データ

アダプタ	該当データなし
ドライバ	該当データなし
ボリューム	該当データなし
コンフィグ	該当データなし

※ 対象となるデバイスがmpt(4)ドライバを使用しないストレージ構成の場合、当項目は該当データなし。

NMIデバイス認識データ

NMIスイッチ動作確認	クリア
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※ NMI機能を使用するには、"options DDB"を有効にしたカーネルである必要がある。また、sysctl(8)の"machdep.panic_on_nmi"及び"machdep.kdb_on_nmi"の値がともに"1"である必要がある。

NIC認識データ

NIC	bge0, bge1
bge0	flags=8843<UP,BROADCAST,RUNNING,SIMPLEX,MULTICAST>, metric=0, mtu=1500, options=c019b<RXCSUM, TXCSUM, VLAN_MTU, VLAN_HWTAGGING, VLAN_HWCSUM, TSO4, VLAN_HWTSO, LINKSTATE>, ether=94:de:80:ff:25:96, inet=192.168.10.127 netmask 0xfffff00 broadcast 192.168.10.255, nd6_options=29<PERFORMNUD,IFDISABLED,AUTO_LINKLOCAL>, media=Ethernet autoselect (1000baseT <full-duplex>), status=active
bge1	flags=8802<BROADCAST,SIMPLEX,MULTICAST>, metric=0, mtu=1500, options=c019b<RXCSUM, TXCSUM, VLAN_MTU, VLAN_HWTAGGING, VLAN_HWCSUM, TSO4, VLAN_HWTSO, LINKSTATE>, ether=94:de:80:ff:25:97, nd6_options=29<PERFORMNUD,IFDISABLED,AUTO_LINKLOCAL>, media=Ethernet autoselect

カーネルデバイス認識データ

acpi	apm	apmctl	audit
bpf	bpf0(bpf)	cd0	console
consolectl	ctty	cuau0	cuau0.init
cuau0.lock	cuau1	cuau1.init	cuau1.lock
devctl	devstat	dumpdev(mfid0p3)	fd
fd/0	fd/1	fd/2	fido
geom.ctl	gptid	gptid/f5921164-87b4-11e5-a1fa-94de80ff2596	hpet0
io	kbd0(ukbd0)	kbd1(kbdmux0)	kbd2(ukbd1)
kbdmux0	klog	kmem	log(/var/run/log)
mdctl	megaraid_sas_ioctl_node(mfi0)	mem	mfi0
mfid0	mfid0p1	mfid0p2	mfid0p3
midistat	nfslock	null	pass0
pci	random	sndstat	stderr(fd/2)
stdin(fd/0)	stdout(fd/1)	sysmouse	ttyu0
ttyu0.init	ttyu0.lock	ttyu1	ttyu1.init
ttyu1.lock	ttyv0	ttyv1	ttyv2
ttyv3	ttyv4	ttyv5	ttyv6
ttyv7	ttyv8	ttyv9	ttyva
ttyvb	ttyvc	ttyvd	ttyve
ttyvf	ufssuspend	ugen0.1(usb/0.1.0)	ugen0.2(usb/0.2.0)
ugen0.3(usb/0.3.0)	ugen0.4(usb/0.4.0)	ugen1.1(usb/1.1.0)	ugen1.2(usb/1.2.0)
ugen2.1(usb/2.1.0)	ugen2.2(usb/2.2.0)	ukbd0	ukbd1
urandom(random)	usb	usb/0.1.0	usb/0.1.1
usb/0.2.0	usb/0.2.1	usb/0.3.0	usb/0.3.1
usb/0.4.0	usb/0.4.1	usb/0.4.2	usb/1.1.0
usb/1.1.1	usb/1.2.0	usb/1.2.1	usb/2.1.0
usb/2.1.1	usb/2.2.0	usb/2.2.1	usbctl
xpt0	zero		

PCI認識データ

1	pci0:0:0:0(hostb0), class=HOST-PCI, vendor='Intel Corporation', device='Haswell DRAM Controller'
2	pci0:0:20:0(xhci0), class=USB, vendor='Intel Corporation', device='Lynx Point USB xHCI Host Controller'
3	pci0:0:22:0(none0), class=simple comms, vendor='Intel Corporation', device='Lynx Point MEI Controller'
4	pci0:0:22:1(none1), class=simple comms, vendor='Intel Corporation', device='Lynx Point MEI Controller'
5	pci0:0:26:0(ehci0), class=USB, vendor='Intel Corporation', device='Lynx Point USB Enhanced Host Controller'
6	pci0:0:28:0(pci1), class=PCI-PCI, vendor='Intel Corporation', device='Lynx Point PCI Express Root Port'
7	pci0:0:28:4(pci2), class=PCI-PCI, vendor='Intel Corporation', device='Lynx Point PCI Express Root Port'
8	pci0:0:28:5(pci3), class=PCI-PCI, vendor='Intel Corporation', device='Lynx Point PCI Express Root Port'
9	pci0:0:29:0(ehci1), class=USB, vendor='Intel Corporation', device='Lynx Point USB Enhanced Host Controller'
10	pci0:0:31:0(isab0), class=PCI-ISA, vendor='Intel Corporation', device='Lynx Point LPC Controller'
11	pci0:0:31:2(atapci0), class=ATA, vendor='Intel Corporation', device='Lynx Point 4-port SATA Controller 1 [IDE mode]'
12	pci0:0:31:3(none2), class=SMBus, vendor='Intel Corporation', device='Lynx Point SMBus Controller'
13	pci0:0:31:5(atapci1), class=ATA, vendor='Intel Corporation', device='Lynx Point 2-port SATA Controller 2 [IDE mode]'
14	pci0:33:0:0(mfi0), class=RAID, vendor='LSI Logic / Symbios Logic', device='MegaRAID SAS 2208 [Thunderbolt]'
15	pci0:57:0:0(vgapci0), class=VGA, vendor='Matrox Electronics Systems Ltd.', device='MGA G200e [Pilot] ServerEngines (SEP1)'
16	pci0:58:0:0(bge0), class=ethernet, vendor='Broadcom Corporation', device='NetXtreme BCM5718 Gigabit Ethernet PCIe'
17	pci0:58:0:1(bge1), class=ethernet, vendor='Broadcom Corporation', device='NetXtreme BCM5718 Gigabit Ethernet PCIe'

メモリ認識データ

1	0x0/0x10000 BIOS write-back fixed-base fixed-length set-by-firmware active
2	0x10000/0x10000 BIOS write-back fixed-base fixed-length set-by-firmware active
3	0x20000/0x10000 BIOS write-back fixed-base fixed-length set-by-firmware active
4	0x30000/0x10000 BIOS write-back fixed-base fixed-length set-by-firmware active
5	0x40000/0x10000 BIOS write-back fixed-base fixed-length set-by-firmware active
6	0x50000/0x10000 BIOS write-back fixed-base fixed-length set-by-firmware active
7	0x60000/0x10000 BIOS write-back fixed-base fixed-length set-by-firmware active
8	0x70000/0x10000 BIOS write-back fixed-base fixed-length set-by-firmware active
9	0x80000/0x4000 BIOS write-back fixed-base fixed-length set-by-firmware active
10	0x84000/0x4000 BIOS write-back fixed-base fixed-length set-by-firmware active
11	0x88000/0x4000 BIOS write-back fixed-base fixed-length set-by-firmware active
12	0x8c000/0x4000 BIOS write-back fixed-base fixed-length set-by-firmware active
13	0x90000/0x4000 BIOS write-back fixed-base fixed-length set-by-firmware active
14	0x94000/0x4000 BIOS write-back fixed-base fixed-length set-by-firmware active
15	0x98000/0x4000 BIOS write-back fixed-base fixed-length set-by-firmware active
16	0x9c000/0x4000 BIOS write-back fixed-base fixed-length set-by-firmware active
17	0xa0000/0x4000 BIOS uncacheable fixed-base fixed-length set-by-firmware active
18	0xa4000/0x4000 BIOS uncacheable fixed-base fixed-length set-by-firmware active
19	0xa8000/0x4000 BIOS uncacheable fixed-base fixed-length set-by-firmware active
20	0xac000/0x4000 BIOS uncacheable fixed-base fixed-length set-by-firmware active
21	0xb0000/0x4000 BIOS uncacheable fixed-base fixed-length set-by-firmware active
22	0xb4000/0x4000 BIOS uncacheable fixed-base fixed-length set-by-firmware active
23	0xb8000/0x4000 BIOS uncacheable fixed-base fixed-length set-by-firmware active
24	0xbc000/0x4000 BIOS uncacheable fixed-base fixed-length set-by-firmware active
25	0xc0000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
26	0xc1000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
27	0xc2000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
28	0xc3000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
29	0xc4000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
30	0xc5000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
31	0xc6000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
32	0xc7000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
33	0xc8000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
34	0xc9000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
35	0xca000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
36	0xcb000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
37	0xcc000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
38	0xcd000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
39	0xce000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
40	0xcf000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active

83	0xfa000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
84	0xfb000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
85	0xfc000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
86	0xfd000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
87	0xfe000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
88	0xff000/0x1000 BIOS write-protect fixed-base fixed-length set-by-firmware active
89	0x0/0x200000000 BIOS write-back set-by-firmware active
90	0x200000000/0x200000000 BIOS write-back set-by-firmware active
91	0x220000000/0x100000000 BIOS write-back set-by-firmware active
92	0xe0000000/0x200000000 BIOS uncacheable set-by-firmware active
93	0xd0000000/0x100000000 BIOS uncacheable set-by-firmware active

動作検証レポート 詳細データ

詳細データ

カーネル環境変数データ詳細一覧

```
LINES="24"
bootfile="kernel"
comconsole_pcidev=""
comconsole_port="1016"
comconsole_speed="9600"
console="vidconsole"
currdev="disk0p2:"
hint.acpi.0.oem="AMI"
hint.acpi.0.revision="2"
hint.acpi.0.rsdp="0x000f0490"
hint.acpi.0.rsdt="0xcc7f3028"
hint.acpi.0.xsdt="0x00000000cc7f3090"
hint.acpi.0.xsdt_length="36"
hint.acpi_throttle.0.disabled="1"
hint.atkbd.0.at="atkbd"
hint.atkbd.0.irq="1"
hint.atkbd.0.at="isa"
hint.atkbd.0.port="0x060"
hint.atrtc.0.at="isa"
hint.atrtc.0.irq="8"
hint.atrtc.0.port="0x70"
hint.attimer.0.at="isa"
hint.attimer.0.irq="0"
hint.attimer.0.port="0x40"
hint.fd.0.at="fdc0"
hint.fd.0.drive="0"
hint.fd.1.at="fdc0"
hint.fd.1.drive="1"
hint.fdc.0.at="isa"
hint.fdc.0.drq="2"
hint.fdc.0.irq="6"
hint.fdc.0.port="0x3F0"
hint.p4tcc.0.disabled="1"
hint.ppc.0.at="isa"
hint.ppc.0.irq="7"
hint.psm.0.at="atkbd"
hint.psm.0.irq="12"
hint.sc.0.at="isa"
hint.sc.0.flags="0x100"
hint.uart.0.at="isa"
hint.uart.0.flags="0x10"
hint.uart.0.irq="4"
hint.uart.0.port="0x3F8"
hint.uart.1.at="isa"
hint.uart.1.irq="3"
hint.uart.1.port="0x2F8"
hint.wbwd.0.at="isa"
interpret="OK"
kernel="kernel"
kernel[0]="kernel"
kernel[1]="kernel.old"
kernel_options=""
kernelname="/boot/kernel/kernel"
loaddev="disk0p2:"
loader_conf_files="/boot/device.hints /boot/loader.conf /boot/loader.conf.local"
mac_ifoff="NO"
module_path="/boot/kernel;/boot/modules"
smbios.bios.reldate="05/22/2014"
smbios.bios.vendor="American Megatrends Inc."
smbios.bios.version="4.6.0203"
smbios.chassis.maker="NEC"
smbios.chassis.serial="01"
smbios.chassis.tag="_____ "
smbios.chassis.version="856-180006-502"
smbios.memory.enabled="8388608"
smbios.planar.maker="GIGABYTE"
smbios.planar.product="GA-6LASV3"
smbios.planar.serial="0EG6U5800067"
smbios.planar.version="2.0A"
smbios.socket.enabled="1"
smbios.socket.populated="1"
smbios.system.maker="NEC"
smbios.system.product="Express5800/R110g-1E [N8100-2174Y]"
smbios.system.serial="4800112"
smbios.system.uuid="00ebd656-8e31-e411-8001-94de80ff25c3"
```

```
smbios.system.version="FR1.0"  
smbios.version="2.7"  
temp_options=""  
twiddle_divisor="1"  
vfs.root.mountfrom="ufs:/dev/mfid0p2"  
vfs.root.mountfrom.options="rw"
```

カーネルステートデータ詳細一覧

```
kern.ostype: FreeBSD
kern.osrelease: 10.2-RELEASE
kern.osrevision: 199506
kern.version: FreeBSD 10.2-RELEASE #0: Wed Nov 11 16:02:59 JST 2015
root@verify.bsdconsulting.co.jp:/usr/obj/usr/src/sys/GENERIC
kern.maxvnodes: 213347
kern.maxproc: 13492
kern.maxfiles: 259563
kern.argmax: 262144
kern.securelevel: -1
kern.hostname: verify.bsdconsulting.co.jp
kern.hostid: 1551666897
kern.clockrate: { hz = 1000, tick = 1000, profhz = 8128, stathz = 127 }
kern.posix1version: 200112
kern.ngroups: 1023
kern.job_control: 1
kern.saved_ids: 0
kern.boottime: { sec = 1447225614, usec = 529920 } Wed Nov 11 16:06:54 2015
kern.domainname:
kern.osreldate: 1002000
kern.bootfile: /boot/kernel/kernel
kern.maxfilesperproc: 233604
kern.maxprocperuid: 12142
kern.ipc.maxsockbuf: 2097152
kern.ipc.sockbuf_waste_factor: 8
kern.ipc.max_linkhdr: 16
kern.ipc.max_protohdr: 60
kern.ipc.max_hdr: 76
kern.ipc.max_datalen: 92
kern.ipc.sendfile.readahead: 1
kern.ipc.maxsockets: 259565
kern.ipc.numopensockets: 17
kern.ipc.soacceptqueue: 128
kern.ipc.shm_allow_removed: 0
kern.ipc.shm_use_phys: 0
kern.ipc.shmall: 131072
kern.ipc.shmseg: 128
kern.ipc.shmmni: 192
kern.ipc.shmmin: 1
kern.ipc.shmmax: 536870912
kern.ipc.semaem: 16384
kern.ipc.semvmx: 32767
kern.ipc.semusz: 632
kern.ipc.semume: 50
kern.ipc.semopm: 100
kern.ipc.semmsl: 340
kern.ipc.semnmnu: 150
kern.ipc.semnmns: 340
kern.ipc.semnmni: 50
kern.ipc.msgseg: 2048
kern.ipc.msgssz: 8
kern.ipc.msgtql: 40
kern.ipc.msgmnb: 2048
kern.ipc.msgmni: 40
kern.ipc.msgmax: 16384
kern.ipc.piperizeallowed: 1
kern.ipc.piperizefail: 0
kern.ipc.pipelocfail: 0
kern.ipc.piperefragretry: 0
kern.ipc.pipekva: 86016
kern.ipc.maxpipekva: 132894720
kern.ipc.nmbufs: 3228870
kern.ipc.nmbjumbo16: 168168
kern.ipc.nmbjumbo9: 224226
kern.ipc.nmbjumbo9: 252255
kern.ipc.nmbclusters: 504510
kern.ipc.maxmbufmem: 4132945920
kern.dummy: 0
kern.ps_strings: 140737488351200
kern.usrstack: 140737488351232
kern.logsigexit: 1
kern.iov_max: 1024
kern.hostuuid: 00ebd656-8e31-e411-8001-94de80ff25c3
kern.chroot_allow_open_directories: 1
kern.minvnodes: 53336
kern.metadelay: 28
kern.dirdelay: 29
kern.filedelay: 30
kern.tty_nout: 880476
kern.tty_nin: 519
kern.tty_inq_flush_secure: 1
kern.smp.forward_signal_enabled: 1
kern.smp.topology: 0
kern.smp.cpus: 2
kern.smp.disabled: 0
```

kern.smp.active: 1
kern.smp.maxcpus: 64
kern.smp.maxid: 1
kern.msgbuf_clear: 0
kern.msgbuf:
kern.always_console_output: 0
kern.log_console_add_linefeed: 0
kern.log_console_output: 1
kern.vm_guest: none
kern.sgrowsiz: 131072
kern.maxssiz: 536870912
kern.dflssiz: 8388608
kern.maxdsiz: 34359738368
kern.dfldsiz: 34359738368
kern.maxtsiz: 134217728
kern.bio_transient_maxcnt: 1024
kern.maxbcache: 0
kern.maxswzone: 0
kern.msgbufsize: 98304
kern.nswbuf: 256
kern.nbuf: 52577
kern.hz: 1000
kern.msgbuf_show_timestamp: 0
kern.log_wakeups_per_second: 5
kern.kobj_methodcount: 234
kern.hintmode: 0
kern.devstat.version: 6
kern.devstat.generation: 206
kern.devstat.numdevs: 3
kern.ccpu: 0
kern.sched.topology_spec: <groups>
kern.sched.steal_thresh: 2
kern.sched.steal_idle: 1
kern.sched.balance_interval: 127
kern.sched.balance: 1
kern.sched.affinity: 1
kern.sched.idlespinthresh: 157
kern.sched.idlespins: 10000
kern.sched.static_boost: 152
kern.sched.preempt_thresh: 80
kern.sched.interact: 30
kern.sched.slice: 12
kern.sched.quantum: 94488
kern.sched.name: ULE
kern.sched.preemption: 1
kern.sched.cpusetsize: 8
kern.callout_stat: 0
kern.ncallout: 18508
kern.threads.max_threads_hits: 0
kern.threads.max_threads_per_proc: 1500
kern.timecounter.tsc_shift: 1
kern.timecounter.smp_tsc_adjust: 0
kern.timecounter.smp_tsc: 1
kern.timecounter.invariant_tsc: 1
kern.timecounter.fast_gettime: 1
kern.timecounter.tick: 1
kern.timecounter.choice: TSC-low(1000) ACPI-fast(900) i8254(0) HPET(950) dummy(-1000000)
kern.timecounter.hardware: TSC-low
kern.timecounter.alloweddeviation: 5
kern.timecounter.stepwarnings: 0
kern.timecounter.tc.TSC-low.quality: 1000
kern.timecounter.tc.TSC-low.frequency: 1546450516
kern.timecounter.tc.TSC-low.counter: 1448348663
kern.timecounter.tc.TSC-low.mask: 4294967295
kern.timecounter.tc.ACPI-fast.quality: 900
kern.timecounter.tc.ACPI-fast.frequency: 3579545
kern.timecounter.tc.ACPI-fast.counter: 178993
kern.timecounter.tc.ACPI-fast.mask: 16777215
kern.timecounter.tc.i8254.quality: 0
kern.timecounter.tc.i8254.frequency: 1193182
kern.timecounter.tc.i8254.counter: 62311
kern.timecounter.tc.i8254.mask: 65535
kern.timecounter.tc.HPET.quality: 950
kern.timecounter.tc.HPET.frequency: 14318180
kern.timecounter.tc.HPET.counter: 3100666499
kern.timecounter.tc.HPET.mask: 4294967295
kern.fscale: 2048
kern.corefile: %N.core
kern.nodump_coredump: 0
kern.coredump: 1
kern.capmode_coredump: 0
kern.sugid_coredump: 0
kern.sigqueue.alloc_fail: 0
kern.sigqueue.overflow: 0
kern.sigqueue.preallocate: 1024
kern.sigqueue.max_pending_per_proc: 128
kern.forcesigexit: 1
kern.shutdown.dumpdevname: mfid0p3
kern.shutdown.kproc_shutdown_wait: 60
kern.shutdown.poweroff_delay: 5000
kern.shutdown.show_busybufs: 0

```

kern.sync_on_panic: 0
kern.panic_reboot_wait_time: 15
kern.racct.pcpu_threshold: 1
kern.racct.enable: 0
kern.proc_vmmmap_skip_resident_count: 0
kern.kstack_pages: 4
kern.hwpmc.softevents: 16
kern.fallback_elf_brand: -1
kern.pid_max: 99999
kern.features.ufs_quota64: 1
kern.features.ufs_quota: 1
kern.features.ufs_gjournal: 1
kern.features.ufs_acl: 1
kern.features.softupdates: 1
kern.features.ffs_snapshot: 1
kern.features.security_mac: 1
kern.features.audit: 1
kern.features.inet6: 1
kern.features.sctp: 1
kern.features.inet: 1
kern.features.posix_shm: 1
kern.features.sysv_shm: 1
kern.features.sysv_sem: 1
kern.features.sysv_msg: 1
kern.features.process_descriptors: 1
kern.features.security_capabilities: 1
kern.features.security_capability_mode: 1
kern.features.stack: 1
kern.features.rctl: 1
kern.features.racct: 1
kern.features.hwpmc_hooks: 1
kern.features.compat_freebsd7: 1
kern.features.compat_freebsd6: 1
kern.features.compat_freebsd5: 1
kern.features.compat_freebsd4: 1
kern.features.ktrace: 1
kern.features.kdtrace_hooks: 1
kern.features.kposix_priority_scheduling: 1
kern.features.geom_part_mbr: 1
kern.features.geom_part_gpt: 1
kern.features.geom_part_ebr_compat: 1
kern.features.geom_part_ebr: 1
kern.features.geom_part_bsd: 1
kern.features.geom_label: 1
kern.features.nfsd: 1
kern.features.nfscl: 1
kern.features.ata_cam: 1
kern.features.compat_freebsd_32bit: 1
kern.features.scbus: 1
kern.conftxt: options CONFIG_AUTOGENERATED
kern.supported_archs: amd64 i386
kern.maxusers: 842
kern.compiler_version: FreeBSD clang version 3.4.1 (tags/RELEASE_34/dot1-final 208032) 20140512
kern.ident: GENERIC
kern.malloc_count: 361
kern.module_path: /boot/kernel;/boot/modules
kern.ktrace.request_pool: 100
kern.ktrace.genio_size: 4096
kern.randompid: 0
kern.lastpid: 1008
kern.disallow_high_osrel: 0
kern.ps_arg_cache_limit: 256
kern.stackprot: 7
kern.kq_calloutmax: 4096
kern.eventtimer.periodic: 0
kern.eventtimer.timer: LAPIC
kern.eventtimer.idletick: 0
kern.eventtimer.singlemul: 2
kern.eventtimer.choice: LAPIC(600) HPET(550) HPET1(440) HPET2(440) HPET3(440) HPET4(440) HPET5(440) HPET6(440) i8254(100) RTC(0)
kern.eventtimer.et.i8254.quality: 100
kern.eventtimer.et.i8254.frequency: 1193182
kern.eventtimer.et.i8254.flags: 1
kern.eventtimer.et.RTC.quality: 0
kern.eventtimer.et.RTC.frequency: 32768
kern.eventtimer.et.RTC.flags: 17
kern.eventtimer.et.HPET6.quality: 440
kern.eventtimer.et.HPET6.frequency: 14318180
kern.eventtimer.et.HPET6.flags: 3
kern.eventtimer.et.HPET5.quality: 440
kern.eventtimer.et.HPET5.frequency: 14318180
kern.eventtimer.et.HPET5.flags: 3
kern.eventtimer.et.HPET4.quality: 440
kern.eventtimer.et.HPET4.frequency: 14318180
kern.eventtimer.et.HPET4.flags: 3
kern.eventtimer.et.HPET3.quality: 440
kern.eventtimer.et.HPET3.frequency: 14318180
kern.eventtimer.et.HPET3.flags: 3
kern.eventtimer.et.HPET2.quality: 440
kern.eventtimer.et.HPET2.frequency: 14318180
kern.eventtimer.et.HPET2.flags: 3
kern.eventtimer.et.HPET1.quality: 440

```

kern.eventtimer.et.HPET1.frequency: 14318180
kern.eventtimer.et.HPET1.flags: 3
kern.eventtimer.et.HPET.quality: 550
kern.eventtimer.et.HPET.frequency: 14318180
kern.eventtimer.et.HPET.flags: 7
kern.eventtimer.et.LAPIC.quality: 600
kern.eventtimer.et.LAPIC.frequency: 49885503
kern.eventtimer.et.LAPIC.flags: 7
kern.openfiles: 75
kern.vty: sc
kern.constty_wakeups_per_second: 5
kern.consmgbuf_size: 8192
kern.consmute: 0
kern.console: ttyv0,/ttyv0,uart,ttyv0,
kern.cp_times: 124 0 1142 170 52815 117 0 945 163 52600
kern.cp_time: 241 0 2087 333 105415
kern.acct_suspended: 0
kern.acct_configured: 0
kern.acct_chkfreg: 15
kern.acct_resume: 4
kern.acct_suspend: 2
kern.init_shutdown_timeout: 120
kern.init_path: /sbin/init:/sbin/oinit:/sbin/init.bak:/rescue/init
kern.elf32.read_exec: 0
kern.elf32.nxstack: 1
kern.elf32.fallback_brand: -1
kern.elf64.nxstack: 1
kern.elf64.fallback_brand: -1
kern.geom.raid.raid5.enable: 1
kern.geom.raid.raid1e.enable: 1
kern.geom.raid.raid1e.rebuild_meta_update: 1024
kern.geom.raid.raid1e.rebuild_cluster_idle: 100
kern.geom.raid.raid1e.rebuild_fair_io: 20
kern.geom.raid.raid1e.rebuild_slab_size: 1048576
kern.geom.raid.raid1.enable: 1
kern.geom.raid.raid1.rebuild_meta_update: 1024
kern.geom.raid.raid1.rebuild_cluster_idle: 100
kern.geom.raid.raid1.rebuild_fair_io: 20
kern.geom.raid.raid1.rebuild_slab_size: 1048576
kern.geom.raid.raid0.enable: 1
kern.geom.raid.concat.enable: 1
kern.geom.raid.sii.enable: 1
kern.geom.raid.promise.enable: 1
kern.geom.raid.nvidia.enable: 1
kern.geom.raid.jmicron.enable: 1
kern.geom.raid.intel.enable: 1
kern.geom.raid.ddf.enable: 1
kern.geom.raid.legacy_aliases: 1
kern.geom.raid.idle_threshold: 1000000
kern.geom.raid.name_format: 0
kern.geom.raid.disconnect_on_failure: 1
kern.geom.raid.clean_time: 5
kern.geom.raid.start_timeout: 30
kern.geom.raid.read_err_thresh: 10
kern.geom.raid.debug: 0
kern.geom.raid.aggressive_spare: 0
kern.geom.raid.enable: 1
kern.geom.part.mbr.enforce_chs: 1
kern.geom.part.check_integrity: 1
kern.geom.label.disk_ident.enable: 1
kern.geom.label.gptid.enable: 1
kern.geom.label.gpt.enable: 1
kern.geom.label.ufs.enable: 1
kern.geom.label.ufsid.enable: 1
kern.geom.label.reiserfs.enable: 1
kern.geom.label.ntfs.enable: 1
kern.geom.label.msdosfs.enable: 1
kern.geom.label.iso9660.enable: 1
kern.geom.label.ext2fs.enable: 1
kern.geom.label.debug: 0
kern.geom.collectstats: 1
kern.geom.notaste: 0
kern.geom.debugflags: 0
kern.geom.conftxt: 0 DISK cd0 0 2048 hd 0 sc 0
kern.geom.confdot: digraph geom {
kern.geom.confxml: <mesh>
kern.geom.inflight_transient_maps: 0
kern.geom.transient_map_soft_failures: 0
kern.geom.transient_map_hard_failures: 0
kern.geom.transient_map_retries: 10
kern.geom.transient_maps: 0
kern.geom.disk.cd0.led:
kern.geom.disk.mfid0.led:
kern.geom.dev.delete_max_sectors: 262144
kern.disks: cd0 mfid0
kern.vt.kbd_panic: 0
kern.vt.kbd_debug: 1
kern.vt.kbd_reboot: 1
kern.vt.kbd_poweroff: 1
kern.vt.kbd_halt: 1
kern.vt.suspendswitch: 1

```

kern.vt.deadtimer: 15
kern.vt.debug: 0
kern.vt.enable_altgr: 1
kern.random.sys.harvest.swi: 1
kern.random.sys.harvest.interrupt: 1
kern.random.sys.harvest.point_to_point: 1
kern.random.sys.harvest.ethernet: 1
kern.random.sys.seeded: 1
kern.random.yarrow.slowoverthresh: 2
kern.random.yarrow.slowthresh: 128
kern.random.yarrow.fastthresh: 96
kern.random.yarrow.bins: 10
kern.random.yarrow.gengateinterval: 10
kern.random.live_entropy_sources: Hardware, Intel Secure Key RNG
kern.random.active_adaptor: yarrow
kern.random.adaptors: yarrow,dummy
kern.cam.enc.emulate_array_devices: 1
kern.cam.da.send_ordered: 1
kern.cam.da.default_timeout: 60
kern.cam.da.retry_count: 4
kern.cam.da.poll_period: 3
kern.cam.ada.write_cache: 1
kern.cam.ada.read_ahead: 1
kern.cam.ada.spindown_suspend: 1
kern.cam.ada.spindown_shutdown: 1
kern.cam.ada.send_ordered: 1
kern.cam.ada.default_timeout: 30
kern.cam.ada.retry_count: 4
kern.cam.ada.legacy_aliases: 1
kern.cam.cd.0.minimum_cmd_size: 6
kern.cam.cd.timeout: 30000
kern.cam.cd.retry_count: 4
kern.cam.cd.poll_period: 3
kern.cam.scsi_delay: 5000
kern.cam.cam_srch_hi: 0
kern.cam.pmp.hide_special: 1
kern.cam.pmp.default_timeout: 30
kern.cam.pmp.retry_count: 1
kern.cam.debug_delay: 0
kern.cam.dflags: 0
kern.cam.num_doneqs: 1
kern.cam.boot_delay: 0
kern.cam.sort_io_queues: 1
vm.vmtotal:
System wide totals computed every five seconds: (values in kilobytes)
Processes: (RUNQ: 1 Disk Wait: 0 Page Wait: 0 Sleep: 26)
Virtual Memory: (Total: 552480K Active: 521520K)
Real Memory: (Total: 32620K Active: 29304K)
Shared Virtual Memory: (Total: 25380K Active: 5764K)
Shared Real Memory: (Total: 6032K Active: 5224K)
Free Memory: 7936348K
vm.loadavg: { 0.42 0.25 0.13 }
vm.v_free_min: 12766
vm.v_free_target: 43033
vm.v_free_reserved: 2677
vm.v_inactive_target: 64549
vm.v_cache_min: 0
vm.v_cache_max: 0
vm.v_pageout_free_min: 34
vm.swap_enabled: 1
vm.kvm_free: 2189546221568
vm.kvm_size: 2199023251456
vm.pmap.pdpe.demotions: 2
vm.pmap.pde.promotions: 46
vm.pmap.pde.p_failures: 879
vm.pmap.pde.mappings: 0
vm.pmap.pde.demotions: 28
vm.pmap.pcid_save_cnt: 0
vm.pmap.invpnid_works: 0
vm.pmap.pcid_enabled: 0
vm.pmap.pg_ps_enabled: 1
vm.pmap.pat_works: 1
vm.idlezero_enable: 0
vm.reserv.reclaimed: 0
vm.reserv.partpopq:
-1: 184036K, 98
vm.reserv.freed: 3684
vm.reserv.broken: 0
vm.ndomains: 1
vm.phys_segs:
SEGMENT 0:
start: 0x10000
end: 0x94000
domain: 0
free list: 0xffffffff8160feb8
SEGMENT 1:
start: 0x10000
end: 0x20000
domain: 0
free list: 0xffffffff8160feb8
SEGMENT 2:

```



```

start:    0x19ad000
end:      0x19c3000
domain:   0
free list: 0xffffffff8160fb10
SEGMENT 3:
start:    0x19c4000
end:      0xb9849000
domain:   0
free list: 0xffffffff8160fb10
SEGMENT 4:
start:    0xb9850000
end:      0xcc7a1000
domain:   0
free list: 0xffffffff8160fb10
SEGMENT 5:
start:    0xcf7ff000
end:      0xcf800000
domain:   0
free list: 0xffffffff8160fb10
SEGMENT 6:
start:    0x100000000
end:      0x221b9d000
domain:   0
free list: 0xffffffff8160fb10
vm.phys_free:
DOMAIN 0:
FREE LIST 0:
FREE LIST 1:
vm.max_wired: 666061
vm.pageout_lock_miss: 0
vm.disable_swapspace_pageouts: 0
vm.defer_swapspace_pageouts: 0
vm.swap_idle_enabled: 0
vm.lowmem_period: 10
vm.pageout_update_period: 600
vm.max_laundry: 32
vm.pageout_wakeup_thresh: 14036
vm.tryrelock_restart: 0
vm.boot_pages: 64
vm.old_msync: 0
vm.old_mlock: 0
vm.stats.object.bypasses: 271
vm.stats.object.collapses: 2294
vm.stats.misc.cnt_prezero: 0
vm.stats.misc.zero_page_count: 68
vm.stats.vm.v_kthreadpages: 0
vm.stats.vm.v_rforkpages: 0
vm.stats.vm.v_vforkpages: 8995
vm.stats.vm.v_forkpages: 27623
vm.stats.vm.v_kthreads: 15
vm.stats.vm.v_rforks: 0
vm.stats.vm.v_vforks: 240
vm.stats.vm.v_forks: 753
vm.stats.vm.v_interrupt_free_min: 2
vm.stats.vm.v_pageout_free_min: 34
vm.stats.vm.v_cache_max: 0
vm.stats.vm.v_cache_min: 0
vm.stats.vm.v_cache_count: 0
vm.stats.vm.v_inactive_count: 4444
vm.stats.vm.v_inactive_target: 64549
vm.stats.vm.v_active_count: 4245
vm.stats.vm.v_wire_count: 25264
vm.stats.vm.v_free_count: 1984086
vm.stats.vm.v_free_min: 12766
vm.stats.vm.v_free_target: 43033
vm.stats.vm.v_free_reserved: 2677
vm.stats.vm.v_page_count: 2018040
vm.stats.vm.v_page_size: 4096
vm.stats.vm.v_tfree: 120716
vm.stats.vm.v_pfree: 47785
vm.stats.vm.v_dfree: 0
vm.stats.vm.v_tcached: 0
vm.stats.vm.v_pdpages: 2514
vm.stats.vm.v_pdwakeup: 0
vm.stats.vm.v_reactivated: 0
vm.stats.vm.v_intrans: 3
vm.stats.vm.v_vnodepgsout: 0
vm.stats.vm.v_vnodepgsin: 3435
vm.stats.vm.v_vnodeout: 0
vm.stats.vm.v_vnodein: 493
vm.stats.vm.v_swappgsout: 0
vm.stats.vm.v_swappgsin: 0
vm.stats.vm.v_swapout: 0
vm.stats.vm.v_swapin: 0
vm.stats.vm.v_ozfod: 0
vm.stats.vm.v_zfod: 44942
vm.stats.vm.v_cow_optim: 151
vm.stats.vm.v_cow_faults: 28447
vm.stats.vm.v_io_faults: 433
vm.stats.vm.v_vm_faults: 82830
vm.stats.sys.v_soft: 1060046

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vm.stats.sys.v_intr: 14477
vm.stats.sys.v_syscall: 6866082
vm.stats.sys.v_trap: 1035042
vm.stats.sys.v_swtdch: 7549985
vm.v_free_severe: 7721
vm.max_kernel_address: 18446744073709547520
vm.min_kernel_address: 18446741874686296064
vm.exec_map_entries: 16
vm.swap_idle_threshold2: 10
vm.swap_idle_threshold1: 2
vm.kstacks: 103
vm.kstack_cache_size: 128
vm.zone_warnings: 1
vm.zone_count: 107
vm.nswapdev: 1
vm.dmmmax: 32
vm.swap_async_max: 4
vm.swap_maxpages: 32288640
vm.swzone: 290597760
vm.overcommit: 0
vm.swap_reserved: 314265600
vm.swap_total: 4227821568
vm.kmem_map_free: 8246542336
vm.kmem_map_size: 19349504
vm.kmem_size_scale: 1
vm.kmem_size_max: 1319413950874
vm.kmem_size_min: 0
vm.kmem_zmax: 65536
vm.kmem_size: 8265891840
vm.md_malloc_wait: 0
vfs.ufs.rename_restarts: 0
vfs.ufs.dirhash_reclaimage: 60
vfs.ufs.dirhash_lowmemcount: 0
vfs.ufs.dirhash_docheck: 0
vfs.ufs.dirhash_mem: 55410
vfs.ufs.dirhash_maxmem: 13451264
vfs.ufs.dirhash_minsize: 2560
vfs.nfs.downdelayinitial: 12
vfs.nfs.downdelayinterval: 30
vfs.nfs.defect: 0
vfs.nfs.iodmax: 20
vfs.nfs.iodmin: 0
vfs.nfs.iodmaxidle: 120
vfs.nfs.diskless_rootpath:
vfs.nfs.diskless_valid: 0
vfs.nfs.nfs_ip_paranoia: 1
vfs.nfs.nfs_directio_allow_mmap: 1
vfs.nfs.nfs_keep_dirty_on_error: 0
vfs.nfs.nfs_directio_enable: 0
vfs.nfs.clean_pages_on_close: 1
vfs.nfs.commit_on_close: 0
vfs.nfs.prime_access_cache: 0
vfs.nfs.access_cache_timeout: 60
vfs.nfs.debuglevel: 0
vfs.nfs.callback_addr:
vfs.nfs.realign_count: 0
vfs.nfs.realign_test: 0
vfs.nfs.skip_wcc_data_onerr: 1
vfs.nfs.nfs3_jukebox_delay: 10
vfs.nfs.reconnects: 0
vfs.nfs.bufpackets: 4
vfs.devfs.dotimes: 0
vfs.devfs.rule_depth: 1
vfs.devfs.generation: 142
vfs.ffs.compute_summary_at_mount: 0
vfs.ffs.maxclustersearch: 10
vfs.ffs.doreallocblks: 1
vfs.ffs.doasyncfree: 1
vfs.worklist_len: 2
vfs.timestamp_precision: 2
vfs.free_owe_inact: 0
vfs.reassignbufcalls: 834
vfs.recycles: 0
vfs.vlru_allow_cache_src: 0
vfs.freevnodes: 115
vfs.wantfreevnodes: 53336
vfs.vnodes_created: 701
vfs.numvnodes: 637
vfs.usermount: 0
vfs.lookup_shared: 1
vfs.tyenumhash: 1
vfs.read_min: 1
vfs.read_max: 64
vfs.write_behind: 1
vfs.cache.numfullpathfound: 9
vfs.cache.numfullpathfail4: 0
vfs.cache.numfullpathfail2: 0
vfs.cache.numfullpathfail1: 0
vfs.cache.numfullpathcalls: 9
vfs.cache.numupgrades: 2
vfs.cache.numneghits: 5904

vfs.cache.numnegzaps: 0
vfs.cache.numposhits: 32380
vfs.cache.numposzaps: 63
vfs.cache.nummisszap: 64
vfs.cache.nummiss: 1501
vfs.cache.numchecks: 38349
vfs.cache.dotdothits: 15
vfs.cache.dothits: 206
vfs.cache.numcalls: 40133
vfs.cache.numcache: 535
vfs.cache.numneg: 33
vfs.ncsizefactor: 2
vfs.ncnegfactor: 16
vfs.flushwithdeps: 0
vfs.unmapped_buf_allowed: 1
vfs.barrierwrites: 0
vfs.notbufdflushes: 0
vfs.flushbufqtarg: 100
vfs.mappingrestarts: 0
vfs.getnewbufrestarts: 0
vfs.getnewbufcalls: 2512
vfs.hifreebuffers: 5850
vfs.lofreebuffers: 2925
vfs.numfreebuffers: 52565
vfs.dirtybufthresh: 11847
vfs.hidirtybuffers: 13164
vfs.lodirtybuffers: 6582
vfs.numdirtybuffers: 12
vfs.recursiveflushes: 104
vfs.altbufferflushes: 0
vfs.bdwriteskip: 0
vfs.dirtybufferflushes: 0
vfs.hirunningspace: 13500416
vfs.lorunningspace: 9043968
vfs.bufdefragcnt: 0
vfs.buffreekvacnt: 56
vfs.bufreuscnt: 2449
vfs.hibufspace: 860766208
vfs.lobufspace: 860700672
vfs.maxmallocbufspace: 43038310
vfs.bufmallocbufspace: 0
vfs.maxbufspace: 861421568
vfs.unmapped_bufspace: 12779520
vfs.bufspace: 48185344
vfs.runningbufspace: 0
vfs.vmioldirenable: 1
vfs.acl_nfs4_old_semantics: 0
vfs.pfs.vncache.misses: 0
vfs.pfs.vncache.hits: 0
vfs.pfs.vncache.maxentries: 0
vfs.pfs.vncache.entries: 0
vfs.pfs.trace: 0
vfs.nfsd.fha.fhe_stats: No file handle entries.
vfs.nfsd.fha.max_reqs_per_nfsd: 0
vfs.nfsd.fha.max_nfsds_per_fh: 8
vfs.nfsd.fha.bin_shift: 22
vfs.nfsd.fha.enable: 1
vfs.nfsd.request_space_throttle_count: 0
vfs.nfsd.request_space_throttled: 0
vfs.nfsd.request_space_low: 31457280
vfs.nfsd.request_space_high: 47185920
vfs.nfsd.request_space_used_highest: 0
vfs.nfsd.request_space_used: 0
vfs.nfsd.groups: 1
vfs.nfsd.threads: 0
vfs.nfsd.maxthreads: 1
vfs.nfsd.minthreads: 1
vfs.nfsd.cachetcp: 1
vfs.nfsd.tpcachetimeo: 43200
vfs.nfsd.udphighwater: 500
vfs.nfsd.tcphighwater: 0
vfs.nfsd.enable_stringtoid: 0
vfs.nfsd.debuglevel: 0
vfs.nfsd.enable_locallocks: 0
vfs.nfsd.issue_delegations: 0
vfs.nfsd.commit_miss: 0
vfs.nfsd.commit_blks: 0
vfs.nfsd.mirrormnt: 1
vfs.nfsd.async: 0
vfs.nfsd.server_max_nfsvers: 4
vfs.nfsd.server_min_nfsvers: 2
vfs.nfsd.nfs_privport: 0
vfs.nfsd.v4statelimit: 500000
vfs.nfsd.sessionhashsize: 20
vfs.nfsd.fhhashsize: 20
vfs.nfsd.clienthashsize: 20
vfs.nfsd.statehashsize: 10
vfs.nfsd.enable_nogroupcheck: 1
vfs.nfsd.enable_nobodycheck: 1
vfs.nfsd.disable_checkutf8: 0
net.local.stream.recvspace: 8192

```
net.local.stream.sendspace: 8192
net.local.dgram.recvspace: 4096
net.local.dgram.maxdgram: 2048
net.local.seqpacket.recvspace: 8192
net.local.seqpacket.maxseqpacket: 8192
net.local.taskcount: 0
net.local.recycled: 0
net.local.deferred: 0
net.local.inflight: 0
net.inet.ip.portrange.randomtime: 45
net.inet.ip.portrange.randomcps: 10
net.inet.ip.portrange.randomized: 1
net.inet.ip.portrange.reservedlow: 0
net.inet.ip.portrange.reservedhigh: 1023
net.inet.ip.portrange.hilast: 65535
net.inet.ip.portrange.hifirst: 49152
net.inet.ip.portrange.last: 65535
net.inet.ip.portrange.first: 10000
net.inet.ip.portrange.lowlast: 600
net.inet.ip.portrange.lowfirst: 1023
net.inet.ip.forwarding: 0
net.inet.ip.redirect: 1
net.inet.ip.ttl: 64
net.inet.ip.rtxpire: 3600
net.inet.ip.rtmixpire: 10
net.inet.ip.rtmxocache: 128
net.inet.ip.sourceroute: 0
net.inet.ip.intr_queue_maxlen: 256
net.inet.ip.intr_queue_drops: 0
net.inet.ip.accept_sourceroute: 0
net.inet.ip.keepfaith: 0
net.inet.ip.gifttl: 30
net.inet.ip.process_options: 1
net.inet.ip.maxfragpackets: 15765
net.inet.ip.maxfragsperpacket: 16
net.inet.ip.fragpackets: 0
net.inet.ip.check_interface: 0
net.inet.ip.random_id: 0
net.inet.ip.sendsourcequench: 0
net.inet.ip.fastforwarding: 0
net.inet.ip.mcast.loop: 1
net.inet.ip.mcast.maxsocksrc: 128
net.inet.ip.mcast.maxgrpsrc: 512
net.inet.ip.random_id_total: 0
net.inet.ip.random_id_collisions: 0
net.inet.ip.random_id_period: 8192
net.inet.ip.no_same_prefix: 0
net.inet.icmp.maskrepl: 0
net.inet.icmp.icmplim: 200
net.inet.icmp.drop_redirect: 0
net.inet.icmp.tstamprepl: 1
net.inet.icmp.bmcastecho: 0
net.inet.icmp.quotelen: 8
net.inet.icmp.reply_from_interface: 0
net.inet.icmp.reply_src:
net.inet.icmp.log_redirect: 0
net.inet.icmp.maskfake: 0
net.inet.icmp.icmplim_output: 1
net.inet.igmp.gsrdelay: 10
net.inet.igmp.default_version: 3
net.inet.igmp.legacysupp: 0
net.inet.igmp.v2enable: 1
net.inet.igmp.vlenable: 1
net.inet.igmp.sendlocal: 1
net.inet.igmp.sendra: 1
net.inet.igmp.recvifkludge: 1
net.inet.tcp.rfc1323: 1
net.inet.tcp.mssdflt: 536
net.inet.tcp.keepidle: 7200000
net.inet.tcp.keepintvl: 75000
net.inet.tcp.sendspace: 32768
net.inet.tcp.recvspace: 65536
net.inet.tcp.keepinit: 75000
net.inet.tcp.delacktime: 100
net.inet.tcp.v6mssdflt: 1220
net.inet.tcp.nolocaltimewait: 0
net.inet.tcp.maxtcpw: 27767
net.inet.tcp.per_cpu_timers: 0
net.inet.tcp.v6pmtud_blackhole_mss: 1220
net.inet.tcp.pmtud_blackhole_mss: 1200
net.inet.tcp.pmtud_blackhole_failed: 0
net.inet.tcp.pmtud_blackhole_activated_min_mss: 0
net.inet.tcp.pmtud_blackhole_activated: 0
net.inet.tcp.pmtud_blackhole_detection: 0
net.inet.tcp.rexmit_drop_options: 0
net.inet.tcp.keepcnt: 8
net.inet.tcp.finwait2_timeout: 60000
net.inet.tcp.fast_finwait2_recycle: 0
net.inet.tcp.always_keepalive: 1
net.inet.tcp.rexmit_slop: 200
net.inet.tcp.rexmit_min: 30
```

```
net.inet.tcp.msl: 30000
net.inet.tcp.syncache.rst_on_sock_fail: 1
net.inet.tcp.syncache.rexmtlimit: 3
net.inet.tcp.syncache.hashsize: 512
net.inet.tcp.syncache.count: 0
net.inet.tcp.syncache.cachelimit: 15375
net.inet.tcp.syncache.bucketlimit: 30
net.inet.tcp.syncookies_only: 0
net.inet.tcp.syncookies: 1
net.inet.tcp.soreceive_stream: 0
net.inet.tcp.isn_reseed_interval: 0
net.inet.tcp.icmp_may_rst: 1
net.inet.tcp.pcbcount: 3
net.inet.tcp.do_tcpdrain: 1
net.inet.tcp.tcbhashsize: 65536
net.inet.tcp.log_debug: 0
net.inet.tcp.minmss: 216
net.inet.tcp.sack.globalholes: 0
net.inet.tcp.sack.globalmaxholes: 65536
net.inet.tcp.sack.maxholes: 128
net.inet.tcp.sack.enable: 1
net.inet.tcp.reass.overflows: 0
net.inet.tcp.reass.cursegments: 0
net.inet.tcp.reass.maxsegments: 31600
net.inet.tcp.sendbuf_max: 2097152
net.inet.tcp.sendbuf_inc: 8192
net.inet.tcp.sendbuf_auto: 1
net.inet.tcp.tso: 1
net.inet.tcp.path_mtu_discovery: 1
net.inet.tcp.recvbuf_max: 2097152
net.inet.tcp.recvbuf_inc: 16384
net.inet.tcp.recvbuf_auto: 1
net.inet.tcp.insecure_rst: 0
net.inet.tcp.ecn.maxretries: 1
net.inet.tcp.ecn.enable: 0
net.inet.tcp.abc_l_var: 2
net.inet.tcp.rfc3465: 1
net.inet.tcp.experimental_initcwnd10: 1
net.inet.tcp.rfc3390: 1
net.inet.tcp.rfc3042: 1
net.inet.tcp.drop_synfin: 0
net.inet.tcp.delayed_ack: 1
net.inet.tcp.blackhole: 0
net.inet.tcp.log_in_vain: 0
net.inet.tcp.hostcache.purge: 0
net.inet.tcp.hostcache.prune: 300
net.inet.tcp.hostcache.expire: 3600
net.inet.tcp.hostcache.count: 0
net.inet.tcp.hostcache.bucketlimit: 30
net.inet.tcp.hostcache.hashsize: 512
net.inet.tcp.hostcache.cachelimit: 15360
net.inet.tcp.cc.available: newreno
net.inet.tcp.cc.algorithm: newreno
net.inet.udp.checksum: 1
net.inet.udp.maxdgram: 9216
net.inet.udp.recvspace: 42080
net.inet.udp.blackhole: 0
net.inet.udp.log_in_vain: 0
net.inet.sctp.diag_info_code: 0
net.inet.sctp.blackhole: 0
net.inet.sctp.use_dcccecn: 1
net.inet.sctp.rttvar_steady_step: 20
net.inet.sctp.rttvar_eqret: 0
net.inet.sctp.rttvar_rtt: 5
net.inet.sctp.rttvar_bw: 4
net.inet.sctp.initial_cwnd: 3
net.inet.sctp.buffer_splitting: 0
net.inet.sctp.vtag_time_wait: 60
net.inet.sctp.nat_friendly_init: 0
net.inet.sctp.enable_sack_immediately: 0
net.inet.sctp.udp_tunneling_port: 0
net.inet.sctp.mobility_fasthandoff: 0
net.inet.sctp.mobility_base: 0
net.inet.sctp.default_frag_interleave: 1
net.inet.sctp.default_ss_module: 0
net.inet.sctp.default_cc_module: 0
net.inet.sctp.log_level: 0
net.inet.sctp.max_retran_chunk: 30
net.inet.sctp.min_residual: 1452
net.inet.sctp.strict_data_order: 0
net.inet.sctp.abort_at_limit: 0
net.inet.sctp.hb_max_burst: 4
net.inet.sctp.do_sctp_drain: 1
net.inet.sctp.max_chained_mbufs: 5
net.inet.sctp.abc_l_var: 2
net.inet.sctp.nat_friendly: 1
net.inet.sctp.cwnd_maxburst: 1
net.inet.sctp.cmt_use_dac: 0
net.inet.sctp.cmt_on_off: 0
net.inet.sctp.outgoing_streams: 10
net.inet.sctp.incoming_streams: 2048
```

```
net.inet.sctp.add_more_on_output: 1452
net.inet.sctp.path_pf_threshold: 65535
net.inet.sctp.path_rtx_max: 5
net.inet.sctp.assoc_rtx_max: 10
net.inet.sctp.init_rtx_max: 8
net.inet.sctp.valid_cookie_life: 60000
net.inet.sctp.init_rto_max: 60000
net.inet.sctp.rto_initial: 3000
net.inet.sctp.rto_min: 1000
net.inet.sctp.rto_max: 60000
net.inet.sctp.secret_lifetime: 3600
net.inet.sctp.shutdown_guard_time: 180
net.inet.sctp.pmtu_raise_time: 600
net.inet.sctp.heartbeat_interval: 30000
net.inet.sctp.asoc_resource: 10
net.inet.sctp.sys_resource: 1000
net.inet.sctp.sack_freq: 2
net.inet.sctp.delayed_sack_time: 200
net.inet.sctp.chunkscale: 10
net.inet.sctp.min_split_point: 2904
net.inet.sctp.pcbhashsize: 256
net.inet.sctp.tcbhashsize: 1024
net.inet.sctp.maxchunks: 63063
net.inet.sctp.fr_maxburst: 4
net.inet.sctp.maxburst: 4
net.inet.sctp.peer_chkoh: 256
net.inet.sctp.strict_sacks: 1
net.inet.sctp.pkt_drop_enable: 0
net.inet.sctp.nr_sack_on_off: 0
net.inet.sctp.reconfig_enable: 1
net.inet.sctp.asconf_enable: 1
net.inet.sctp.auth_disable: 0
net.inet.sctp.pr_enable: 1
net.inet.sctp.ecn_enable: 1
net.inet.sctp.auto_asconf: 1
net.inet.sctp.recvspace: 1864135
net.inet.sctp.sendspace: 1864135
net.inet.raw.recvspace: 9216
net.inet.raw.maxdgram: 9216
net.inet.accf.unloadable: 0
net.link.generic.system.ifcount: 3
net.link.ether.inet.max_log_per_second: 1
net.link.ether.inet.allow_multicast: 0
net.link.ether.inet.log_arp_permanent_modify: 1
net.link.ether.inet.log_arp_movements: 1
net.link.ether.inet.log_arp_wrong_iface: 1
net.link.ether.inet.maxhold: 1
net.link.ether.inet.wait: 20
net.link.ether.inet.proxyall: 0
net.link.ether.inet.useloopback: 1
net.link.ether.inet.maxtries: 5
net.link.ether.inet.max_age: 1200
net.link.vlan.soft_pad: 0
net.link.gif.parallel_tunnels: 0
net.link.gif.max_nesting: 1
net.link.tun.devfs_cloning: 1
net.link.log_link_state_change: 1
net.link.ifqmaxlen: 50
net.inet6.ip6.forwarding: 0
net.inet6.ip6.redirect: 1
net.inet6.ip6.hlim: 64
net.inet6.ip6.maxfragpackets: 126127
net.inet6.ip6.accept_rtadv: 0
net.inet6.ip6.keepfaith: 0
net.inet6.ip6.log_interval: 5
net.inet6.ip6.hdrnestlimit: 15
net.inet6.ip6.dad_count: 1
net.inet6.ip6.auto_flowlabel: 1
net.inet6.ip6.defmcasthlim: 1
net.inet6.ip6.gifhlim: 30
net.inet6.ip6.kame_version: FreeBSD
net.inet6.ip6.use_deprecated: 1
net.inet6.ip6.rr_prune: 5
net.inet6.ip6.v6only: 1
net.inet6.ip6.rtmxcache: 128
net.inet6.ip6.use_tempaddr: 0
net.inet6.ip6.templtime: 86400
net.inet6.ip6.tempvtime: 604800
net.inet6.ip6.auto_linklocal: 1
net.inet6.ip6.prefer_tempaddr: 0
net.inet6.ip6.use_defaultzone: 0
net.inet6.ip6.maxfrags: 126127
net.inet6.ip6.mcast_pmtu: 0
net.inet6.ip6.no_radr: 0
net.inet6.ip6.norbit_raif: 0
net.inet6.ip6.rfc6204w3: 0
net.inet6.ip6.deembed_scopeid: 1
net.inet6.ip6.dad_enhanced: 1
net.inet6.ip6.mcast_loop: 1
net.inet6.ip6.mcast_maxsocksrc: 128
net.inet6.ip6.mcast_maxgrpsrc: 512
```

```
net.inet6.icmp6.rediraccept: 1
net.inet6.icmp6.redirtimeout: 600
net.inet6.icmp6.nd6_prune: 1
net.inet6.icmp6.nd6_delay: 5
net.inet6.icmp6.nd6_umaxtries: 3
net.inet6.icmp6.nd6_mmaxtries: 3
net.inet6.icmp6.nd6_useloopback: 1
net.inet6.icmp6.nodeinfo: 3
net.inet6.icmp6.errppslimit: 100
net.inet6.icmp6.nd6_maxnudhint: 0
net.inet6.icmp6.nd6_debug: 0
net.inet6.icmp6.nd6_maxqueuelen: 1
net.inet6.icmp6.nodeinfo_oldmcprefix: 1
net.inet6.icmp6.nd6_onlink_ns_rfc4861: 0
net.inet6.icmp6.nd6_gctimer: 86400
net.inet6.mld.use_allow: 1
net.inet6.mld.vlenable: 1
net.inet6.mld.gsrdelay: 10
net.wlan.mesh.maxholding: 2
net.wlan.mesh.maxretries: 2
net.wlan.mesh.backofftimeout: 5000
net.wlan.mesh.confirmtimer: 40
net.wlan.mesh.holdingtimer: 40
net.wlan.mesh.retrytimer: 40
net.wlan.mesh.gateint: 10000
net.wlan.hwmp.inact: 5000
net.wlan.hwmp.rootconfint: 2000
net.wlan.hwmp.rannint: 1000
net.wlan.hwmp.rootint: 2000
net.wlan.hwmp.roottimeout: 5000
net.wlan.hwmp.net_diameter_traversal_time: 512
net.wlan.hwmp.maxpreq_retries: 3
net.wlan.hwmp.pathlifetime: 5000
net.wlan.hwmp.targetonly: 0
net.wlan.addba_maxtries: 3
net.wlan.addba_backoff: 10000
net.wlan.addba_timeout: 250
net.wlan.recv_bar: 1
net.wlan.ampdu_age: 500
net.wlan.debug: 0
net.wlan.cac_timeout: 60
net.wlan.nol_timeout: 1800
net.route.netisr_maxqlen: 256
net.my_fibnum: 0
net.add_addr_allfibs: 1
net.fibs: 1
net.raw.recvspace: 8192
net.raw.sendspace: 8192
net.isr.numthreads: 1
net.isr.maxprot: 16
net.isr.defaultqlimit: 256
net.isr.maxqlimit: 10240
net.isr.bindthreads: 0
net.isr.maxthreads: 1
net.isr.dispatch: direct
net.ifdescr_maxlen: 1024
net.bpf.maxbufsize: 524288
net.bpf.bufsize: 4096
net.bpf.optimize_writers: 0
net.bpf.zerocopy_enable: 0
net.bpf.maxinsns: 512
debug.minidump: 1
debug.hwpstate_verbose: 0
debug.x86bios.int: 0
debug.x86bios.call: 0
debug.fdc.settle: 0
debug.fdc.spec2: 16
debug.fdc.spec1: 175
debug.fdc.retries: 10
debug.fdc.debugflags: 0
debug.fdc.fifo: 8
debug.vesa.shadow_rom: 0
debug.psm.pkterrthresh: 2
debug.psm.usecs: 500000
debug.psm.secs: 0
debug.psm.errusecs: 0
debug.psm.errsecs: 2
debug.psm.hz: 20
debug.psm.loglevel: 0
debug.dircheck: 0
debug.dobkgrdwrite: 1
debug.bigcgs: 0
debug.softdep.print_threads: 0
debug.softdep.emptyjblocks: 2
debug.softdep.flushcache: 0
debug.softdep.cleanup_failures: 0
debug.softdep.cleanup_retries: 0
debug.softdep.cleanup_high_delay: 0
debug.softdep.cleanup_inorequests: 0
debug.softdep.cleanup_blkrequests: 0
debug.softdep.jwait_newblk: 8
```

debug.softdep.jwait_inode: 0
debug.softdep.jwait_freeblks: 0
debug.softdep.jwait_filepage: 0
debug.softdep.journal_wait: 8
debug.softdep.journal_min: 0
debug.softdep.journal_low: 0
debug.softdep.jnewblk_rollback: 2
debug.softdep.jaddrf_rollback: 5
debug.softdep.dir_entry: 2
debug.softdep.direct_blk_ptrs: 8
debug.softdep.inode_bitmap: 12
debug.softdep.indir_blk_ptrs: 0
debug.softdep.sync_limit_hit: 0
debug.softdep.ino_limit_hit: 0
debug.softdep.blk_limit_hit: 0
debug.softdep.ino_limit_push: 0
debug.softdep.blk_limit_push: 0
debug.softdep.worklist_push: 0
debug.softdep.flush_threads: 1
debug.softdep.tickdelay: 2
debug.softdep.max_softdeps: 853388
debug.softdep.write.jfsync: 0
debug.softdep.write.jtrunc: 0
debug.softdep.write.sbdep: 13
debug.softdep.write.jsegdep: 8
debug.softdep.write.jseg: 27
debug.softdep.write.jffreefrag: 0
debug.softdep.write.jfreeblk: 0
debug.softdep.write.jnewblk: 0
debug.softdep.write.jmhref: 0
debug.softdep.write.jremref: 0
debug.softdep.write.jaddrf: 0
debug.softdep.write.freedep: 0
debug.softdep.write.freework: 0
debug.softdep.write.newdirblk: 0
debug.softdep.write.dirrem: 0
debug.softdep.write.mkdir: 0
debug.softdep.write.diradd: 0
debug.softdep.write.freefile: 0
debug.softdep.write.freeblks: 30
debug.softdep.write.freefrag: 0
debug.softdep.write.allocindir: 44
debug.softdep.write.indirdep: 2
debug.softdep.write.allocdirect: 85
debug.softdep.write.newblk: 0
debug.softdep.write.bmsafemap: 34
debug.softdep.write.inodedep: 159
debug.softdep.write.pagedep: 21
debug.softdep.current.jfsync: 0
debug.softdep.current.jtrunc: 0
debug.softdep.current.sbdep: 0
debug.softdep.current.jsegdep: 22
debug.softdep.current.jseg: 11
debug.softdep.current.jffreefrag: 0
debug.softdep.current.jfreeblk: 0
debug.softdep.current.jnewblk: 4
debug.softdep.current.jmhref: 0
debug.softdep.current.jremref: 0
debug.softdep.current.jaddrf: 0
debug.softdep.current.freedep: 0
debug.softdep.current.freework: 15
debug.softdep.current.newdirblk: 0
debug.softdep.current.dirrem: 0
debug.softdep.current.mkdir: 0
debug.softdep.current.diradd: 0
debug.softdep.current.freefile: 0
debug.softdep.current.freeblks: 2
debug.softdep.current.freefrag: 2
debug.softdep.current.allocindir: 14
debug.softdep.current.indirdep: 1
debug.softdep.current.allocdirect: 5
debug.softdep.current.newblk: 0
debug.softdep.current.bmsafemap: 1
debug.softdep.current.inodedep: 4
debug.softdep.current.pagedep: 0
debug.softdep.highuse.jfsync: 0
debug.softdep.highuse.jtrunc: 0
debug.softdep.highuse.sbdep: 1
debug.softdep.highuse.jsegdep: 112
debug.softdep.highuse.jseg: 11
debug.softdep.highuse.jffreefrag: 1
debug.softdep.highuse.jfreeblk: 0
debug.softdep.highuse.jnewblk: 61
debug.softdep.highuse.jmhref: 0
debug.softdep.highuse.jremref: 14
debug.softdep.highuse.jaddrf: 17
debug.softdep.highuse.freedep: 1
debug.softdep.highuse.freework: 41
debug.softdep.highuse.newdirblk: 5
debug.softdep.highuse.dirrem: 19
debug.softdep.highuse.mkdir: 8

debug.softdep.highuse.diradd: 23
debug.softdep.highuse.freefile: 12
debug.softdep.highuse.freeblks: 17
debug.softdep.highuse.freefrag: 12
debug.softdep.highuse.allocindir: 23
debug.softdep.highuse.indirdep: 2
debug.softdep.highuse.allocdirect: 45
debug.softdep.highuse.newblk: 2
debug.softdep.highuse.bmsafemap: 6
debug.softdep.highuse.inodedep: 47
debug.softdep.highuse.pagedep: 14
debug.softdep.total.jfsync: 0
debug.softdep.total.jtrunc: 0
debug.softdep.total.sbdep: 13
debug.softdep.total.jsegdep: 395
debug.softdep.total.jseg: 27
debug.softdep.total.jffreefrag: 28
debug.softdep.total.jffreeblk: 0
debug.softdep.total.jnewblk: 238
debug.softdep.total.jmhref: 0
debug.softdep.total.jhref: 61
debug.softdep.total.jaddrf: 68
debug.softdep.total.freedep: 2
debug.softdep.total.freework: 129
debug.softdep.total.newdirblk: 9
debug.softdep.total.dirrem: 43
debug.softdep.total.mkdir: 18
debug.softdep.total.diradd: 50
debug.softdep.total.freefile: 40
debug.softdep.total.freeblks: 44
debug.softdep.total.freefrag: 28
debug.softdep.total.allocindir: 48
debug.softdep.total.indirdep: 4
debug.softdep.total.allocdirect: 191
debug.softdep.total.newblk: 239
debug.softdep.total.bmsafemap: 70
debug.softdep.total.inodedep: 107
debug.softdep.total.pagedep: 24
debug.collectsnapstats: 0
debug.snapdebug: 0
debug.dopersistence: 0
debug.fsckcmds: 0
debug.nlm_debug: 0
debug.if_tun_debug: 0
debug.vn_io_faults: 165
debug.vn_io_fault_enable: 1
debug.vnlru_nowhere: 0
debug.rush_requests: 0
debug.disablefullpath: 0
debug.disablecwd: 0
debug.vfscache: 1
debug.numcachehv: 52
debug.numcache: 535
debug.numneg: 33
debug.nchash: 262143
debug.devfs_iosize_max_clamp: 1
debug.iosize_max_clamp: 1
debug.rman_debug: 0
debug.kdb.alt_break_to_debugger: 0
debug.kdb.break_to_debugger: 0
debug.kdb.trap_code: 0
debug.kdb.trap: 0
debug.kdb.panic: 0
debug.kdb.enter: 0
debug.kdb.current:
debug.kdb.available:
debug.clocktime: 0
debug.umtx.umtx_pi_allocated: 0
debug.sx.loops: 10000
debug.sx.retries: 10
debug.ncores: 5
debug.trace_on_panic: 1
debug.debugger_on_panic: 1
debug.rwlock.loops: 10000
debug.rwlock.retry: 10
debug.stop_all_proc: 0
debug.osd: 0
debug.adaptive_machine_arch: 1
debug.sizeof.namecache: 72
debug.sizeof.devstat: 288
debug.sizeof.kinfo_proc: 1088
debug.sizeof.buf: 600
debug.sizeof.bio: 248
debug.sizeof.proc: 1256
debug.sizeof.vnode: 472
debug.sizeof.g_bioq: 56
debug.sizeof.g_consumer: 96
debug.sizeof.g_provider: 136
debug.sizeof.g_geom: 176
debug.sizeof.g_class: 176
debug.sizeof.cdev_priv: 376

debug.sizeof.cdev: 288
debug.fail_point.nlm_deny_grant: off
debug.fail_point.buf_pressure: off
debug.fail_point.sysctl_running: off
debug.cpubus.verbose: 0
debug.cpubus.lowest: 0
debug.bootverbose: 0
debug.boothowto: 0
debug.elf32_legacy_coredump: 0
debug.elf64_legacy_coredump: 0
debug.wpi: 0
debug.mddebug: 0
debug.iwi: 0
debug.ipw: 0
debug.acpi.resume_beeper: 0
debug.acpi.batt.batt_sleep_ms: 0
debug.acpi.ec.timeout: 750
debug.acpi.ec.pollled: 0
debug.acpi.ec.burst: 0
debug.acpi.cpu_unordered: 0
debug.acpi.suspend_bounce: 0
debug.acpi.reset_clock: 1
debug.acpi.default_register_width: 1
debug.acpi.interpreter_slack: 1
debug.acpi.enable_debug_objects: 0
debug.acpi.acpi_ca_version: 20150515
debug.acpi.max_threads: 3
debug.acpi.max_tasks: 256
hw.machine: amd64
hw.model: Intel(R) Pentium(R) CPU G3240 @ 3.10GHz
hw.ncpu: 2
hw.byteorder: 1234
hw.physmem: 8505372672
hw.usermem: 8401870848
hw.pagesize: 4096
hw.floatingpoint: 1
hw.machine_arch: amd64
hw.realmem: 8589934592
hw.acpi.cpu.cx_lowest: C1
hw.acpi.reset_video: 0
hw.acpi.handle_reboot: 1
hw.acpi.disable_on_reboot: 0
hw.acpi.verbose: 0
hw.acpi.s4bios: 0
hw.acpi.sleep_delay: 1
hw.acpi.suspend_state: NONE
hw.acpi.standby_state: NONE
hw.acpi.lid_switch_state: NONE
hw.acpi.sleep_button_state: S4
hw.acpi.power_button_state: S5
hw.acpi.supported_sleep_state: S4 S5
hw.mca.cmc_throttle: 60
hw.mca.force_scan: 0
hw.mca.interval: 3600
hw.mca.count: 0
hw.mca.erratum383: 0
hw.mca.intel6h_HSD131: 0
hw.mca.amd10h_L1TP: 1
hw.mca.enabled: 1
hw.apic.enable_extint: 0
hw.hv_vendor:
hw.clockrate: 3092
hw.bsdma.zone0.alignment: 4096
hw.bsdma.zone0.lowaddr: 0xffffffff
hw.bsdma.zone0.total_deferred: 0
hw.bsdma.zone0.total_bounced: 0
hw.bsdma.zone0.active_bpages: 0
hw.bsdma.zone0.reserved_bpages: 0
hw.bsdma.zone0.free_bpages: 298
hw.bsdma.zone0.total_bpages: 298
hw.bsdma.total_bpages: 298
hw.dmar.match_verbose: 0
hw.dmar.tbl_pagecnt: 0
hw.kbd.keymap_restrict_change: 0
hw.bxe.udp_rss: 0
hw.bxe.autogreen: 0
hw.bxe.mrrs: -1
hw.bxe.max_aggregation_size: 0
hw.bxe.rx_budget: -1
hw.bxe.hc_tx_ticks: 50
hw.bxe.hc_rx_ticks: 25
hw.bxe.max_rx_bufs: 0
hw.bxe.queue_count: 4
hw.bxe.interrupt_mode: 2
hw.bxe.debug: 0
hw.psm.tap_timeout: 125000
hw.psm.tap_threshold: 25
hw.psm.tap_enabled: -1
hw.via_feature_xcrypt: 0
hw.via_feature_rng: 0
hw.instruction_sse: 1

hw.bus.devctl_queue: 1000
hw.bus.devctl_disable: 0
hw.pagesizes: 4096 2097152 0
hw.availpages: 2076507
hw.intr_storm_threshold: 1000
hw.xe.debug: 0
hw.wi.debug: 0
hw.wi.txerate: 0
hw.watchdog.wd_last_u_secs: 0
hw.watchdog.wd_last_u: 0
hw.usb.ums.debug: 0
hw.usb.ukbd.pollrate: 0
hw.usb.ukbd.no_leds: 0
hw.usb.ukbd.debug: 0
hw.usb.full_ddesc: 0
hw.usb.no_cs_fail: 0
hw.usb.proc.debug: 0
hw.usb.disable_port_power: 0
hw.usb.disable_enumeration: 0
hw.usb.power_timeout: 30
hw.usb.uhub.debug: 0
hw.usb.ugen.debug: 0
hw.usb.usb_lang_mask: 255
hw.usb.usb_lang_id: 9
hw.usb.template: 0
hw.usb.dev.debug: 0
hw.usb.timings.extra_power_up_time: 20
hw.usb.timings.resume_recovery: 50
hw.usb.timings.resume_wait: 50
hw.usb.timings.resume_delay: 250
hw.usb.timings.set_address_settle: 10
hw.usb.timings.port_resume_delay: 40
hw.usb.timings.port_powerup_delay: 300
hw.usb.timings.port_reset_recovery: 250
hw.usb.timings.port_root_reset_delay: 250
hw.usb.timings.port_reset_delay: 50
hw.usb.debug: 0
hw.usb.umass.throttle: 0
hw.usb.umass.debug: 0
hw.usb.no_shutdown_wait: 0
hw.usb.no_suspend_wait: 0
hw.usb.no_boot_wait: 0
hw.usb.ctrl.debug: 0
hw.usb.xhci.dma32: 0
hw.usb.xhci.use_polling: 0
hw.usb.xhci.xhci_port_route: 0
hw.usb.xhci.debug: 0
hw.usb.xhci.streams: 0
hw.usb.uhci.loop: 0
hw.usb.uhci.debug: 0
hw.usb.ohci.debug: 0
hw.usb.ehci.lostintrbug: 0
hw.usb.ehci.iaadbug: 0
hw.usb.ehci.no_hs: 0
hw.usb.ehci.debug: 0
hw.broken_txfifo: 0
hw.syscons.sc_no_suspend_vtswitch: 0
hw.syscons.kbd_debug: 1
hw.syscons.kbd_reboot: 1
hw.syscons.bell: 1
hw.syscons.saver.keybonly: 1
hw.midi.seq.debug: 0
hw.midi.instrhoff: 0
hw.midi.dumpraw: 0
hw.midi.debug: 0
hw.midi.stat.verbose: 0
hw.snd.maxautovchans: 16
hw.snd.default_unit: -1
hw.snd.version: 2009061500/amd64
hw.snd.default_auto: -1
hw.snd.verbose: 0
hw.snd.vpc_mixer_bypass: 1
hw.snd.feeder_rate_quality: 1
hw.snd.feeder_rate_round: 25
hw.snd.feeder_rate_max: 2016000
hw.snd.feeder_rate_min: 1
hw.snd.feeder_rate_polyphase_max: 183040
hw.snd.feeder_rate_presets: 100:8:0.85 100:36:0.92 100:164:0.97
hw.snd.feeder_eq_exact_rate: 0
hw.snd.feeder_eq_presets: PEQ:16000,0.2500,62,0.2500:-9,9,1.0:44100,48000,88200,96000,176400,192000
hw.snd.basename_clone: 1
hw.snd.compat_linux_mmap: 0
hw.snd.vpc_reset: 0
hw.snd.vpc_0db: 45
hw.snd.vpc_autoreset: 1
hw.snd.latency_profile: 1
hw.snd.latency: 5
hw.snd.report_soft_matrix: 1
hw.snd.report_soft_formats: 1
hw.sdhci.enable_msi: 1
hw.sdhci.debug: 0

hw.pci.host_mem_start: 2147483648
hw.pci.mcfg: 1
hw.pci.default_vgapci_unit: 0
hw.pci.clear_pcib: 0
hw.pci.enable_ari: 1
hw.pci.clear_buses: 0
hw.pci.clearBars: 0
hw.pci.usb_early_takeover: 1
hw.pci.honor_msi_blacklist: 1
hw.pci.enable_msix: 1
hw.pci.enable_msi: 1
hw.pci.do_power_suspend: 1
hw.pci.do_power_resume: 1
hw.pci.do_power_nodriver: 0
hw.pci.realloc_bars: 0
hw.pci.enable_io_modes: 1
hw.pci.pd6722_vsense: 1
hw.pci.intr_mask: 57016
hw.cbb.debug: 0
hw.cbb.start_32_io: 4096
hw.cbb.start_16_io: 256
hw.cbb.start_memory: 2281701376
hw.pccard.cis_debug: 0
hw.pccard.debug: 0
hw.mwl.rxdm_low: 3
hw.mwl.rxquota: 640
hw.mwl.txcoalesce: 8
hw.mwl.txbuf: 256
hw.mwl.rxbuf: 640
hw.mwl.rxdesc: 256
hw.mmc.debug: 0
hw.mfi.mrsas_enable: 0
hw.mfi.msi: 1
hw.mfi.cmd_timeout: 30
hw.mfi.polled_cmd_timeout: 60
hw.mfi.detect_jbod_change: 1
hw.mfi.max_cmds: 128
hw.mfi.event_class: 0
hw.mfi.event_locale: 65535
hw.malo.pci.msi_disable: 0
hw.malo.txbuf: 256
hw.malo.rxquota: 256
hw.malo.rxbuf: 256
hw.malo.txcoalesce: 8
hw.ixlv.tx_itr: 122
hw.ixlv.rx_itr: 62
hw.ixlv.dynamic_tx_itr: 0
hw.ixlv.dynamic_rx_itr: 0
hw.ixlv.txbr_size: 16777216
hw.ixlv.max_queues: 0
hw.ixlv.ring_size: 1024
hw.ixl.tx_itr: 122
hw.ixl.rx_itr: 62
hw.ixl.dynamic_tx_itr: 0
hw.ixl.dynamic_rx_itr: 0
hw.ixl.max_queues: 0
hw.ixl.ring_size: 1024
hw.ixl.enable_msix: 1
hw.ix.rxd: 2048
hw.ix.txd: 2048
hw.ix.num_queues: 0
hw.ix.enable_msix: 1
hw.ix.tx_process_limit: 256
hw.ix.rx_process_limit: 256
hw.ix.max_interrupt_rate: 31250
hw.ix.enable_aim: 1
hw.igb.rx_process_limit: 100
hw.igb.num_queues: 0
hw.igb.header_split: 0
hw.igb.buf_ring_size: 4096
hw.igb.max_interrupt_rate: 8000
hw.igb.enable_msix: 1
hw.igb.enable_aim: 1
hw.igb.txd: 1024
hw.igb.rxd: 1024
hw.em.eee_setting: 1
hw.em.rx_process_limit: 100
hw.em.enable_msix: 1
hw.em.sbp: 0
hw.em.smart_pwr_down: 0
hw.em.txd: 1024
hw.em.rxd: 1024
hw.em.rx_abs_int_delay: 66
hw.em.tx_abs_int_delay: 66
hw.em.rx_int_delay: 0
hw.em.tx_int_delay: 66
hw.cs.recv_delay: 570
hw.cs.ignore_checksum_failure: 0
hw.cardbus.cis_debug: 0
hw.cardbus.debug: 0
hw.bge.allow_asf: 1

hw.bce.rx_ticks: 18
hw.bce.rx_ticks_int: 18
hw.bce.rx_quick_cons_trip: 6
hw.bce.rx_quick_cons_trip_int: 6
hw.bce.tx_ticks: 80
hw.bce.tx_ticks_int: 80
hw.bce.tx_quick_cons_trip: 20
hw.bce.tx_quick_cons_trip_int: 20
hw.bce.strict_rx_mtu: 0
hw.bce.hdr_split: 1
hw.bce.tx_pages: 2
hw.bce.rx_pages: 2
hw.bce.msi_enable: 1
hw.bce.tso_enable: 1
hw.bce.verbose: 1
hw.ath.bstuck: 4
hw.ath.txbuf_mgmt: 32
hw.ath.txbuf: 512
hw.ath.rxbuf: 512
hw.ath.anical: 100
hw.ath.resetcal: 1200
hw.ath.shortcal: 100
hw.ath.longcal: 30
hw.ata.ata_dma_check_80pin: 1
hw.an.an_cache_iponly: 1
hw.an.an_cache_mcastonly: 0
hw.an.an_cache_mode: dbm
hw.an.an_dump: off
hw.amr.force_sg32: 0
hw.aac.enable_msi: 1
machdep.tsc_freq: 3092901032
machdep.disable_tsc_calibration: 0
machdep.disable_tsc: 0
machdep.i8254_freq: 1193182
machdep.acpi_root: 984208
machdep.uprintf_signal: 0
machdep.prot_fault_translation: 0
machdep.panic_on_nmi: 1
machdep.kdb_on_nmi: 1
machdep.max_ldt_segment: 1024
machdep.nkpt: 22
machdep.smap:
machdep.bootmethod: BIOS
machdep.idle: acpi
machdep.idle_available: spin, mwait, hlt, acpi
machdep.idle_mwait: 1
machdep.disable_mtrrs: 0
machdep.disable_rtc_set: 0
machdep.wall_cmos_clock: 1
machdep.adjkerntz: -32400
machdep.rtc_save_period: 1800
machdep.enable_panic_key: 0
machdep.acpi_timer_freq: 3579545
user.cs_path: /usr/bin:/bin:/usr/sbin:/sbin
user.bc_base_max: 99
user.bc_dim_max: 2048
user.bc_scale_max: 99
user.bc_string_max: 1000
user.coll_weights_max: 0
user.expr_nest_max: 32
user.line_max: 2048
user.re_dup_max: 255
user.posix2_version: 199212
user.posix2_c_bind: 0
user.posix2_c_dev: 0
user.posix2_char_term: 0
user.posix2_fort_dev: 0
user.posix2_fort_run: 0
user.posix2_localedef: 0
user.posix2_sw_dev: 0
user.posix2_upe: 0
user.stream_max: 20
user.tzname_max: 255
p1003_1b.asynchronous_io: 0
p1003_1b.mapped_files: 200112
p1003_1b.memlock: 0
p1003_1b.memlock_range: 0
p1003_1b.memory_protection: 0
p1003_1b.message_passing: 0
p1003_1b.prioritized_io: 0
p1003_1b.priority_scheduling: 200112
p1003_1b.realtime_signals: 200112
p1003_1b.semaphores: 0
p1003_1b.fsync: 200112
p1003_1b.shared_memory_objects: 200112
p1003_1b.synchronized_io: 0
p1003_1b.timers: 200112
p1003_1b.aio_listio_max: -1
p1003_1b.aio_max: -1
p1003_1b.aio_prio_delta_max: -1
p1003_1b.delaytimer_max: 2147483647

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p1003_1b.mq_open_max: 0
p1003_1b.pagesize: 4096
p1003_1b.rtsig_max: 62
p1003_1b.sem_nsems_max: 0
p1003_1b.sem_value_max: 0
p1003_1b.sigqueue_max: 128
p1003_1b.timer_max: 32
hptmv.status: RocketRAID 18xx SATA Controller driver Version v1.16
dev.umass.%parent:
dev.ums.0.parseinfo: i1: Z:r0, p40, s8; B1:r0, p0, s1; B2:r0, p1, s1; B3:r0, p2, s1;
dev.ums.0.%parent: uhub1
dev.ums.0.%pnpinfo: vendor=0x046b product=0xff10 devclass=0x00 devsubclass=0x00 sernum="" release=0x0100 mode=host intclass=0x03
intsubclass=0x01 intprotocol=0x02
dev.ums.0.%location: bus=0 hubaddr=1 port=7 devaddr=4 interface=1 ugen=ugen0.4
dev.ums.0.%driver: ums
dev.ums.0.%desc: Mouse Interface
dev.ums.%parent:
dev.ukbd.1.%parent: uhub1
dev.ukbd.1.%pnpinfo: vendor=0x046b product=0xff10 devclass=0x00 devsubclass=0x00 sernum="" release=0x0100 mode=host intclass=0x03
intsubclass=0x01 intprotocol=0x01
dev.ukbd.1.%location: bus=0 hubaddr=1 port=7 devaddr=4 interface=0 ugen=ugen0.4
dev.ukbd.1.%driver: ukbd
dev.ukbd.1.%desc: Keyboard Interface
dev.ukbd.0.%parent: uhub3
dev.ukbd.0.%pnpinfo: vendor=0x0853 product=0x0100 devclass=0x00 devsubclass=0x00 sernum="" release=0x0102 mode=host intclass=0x03
intsubclass=0x01 intprotocol=0x01
dev.ukbd.0.%location: bus=0 hubaddr=2 port=1 devaddr=3 interface=0 ugen=ugen0.3
dev.ukbd.0.%driver: ukbd
dev.ukbd.0.%desc: Topre Corporation HHKB Professional, class 0/0, rev 1.10/1.02, addr 2
dev.ukbd.%parent:
dev.mfid.0.%parent: mfi0
dev.mfid.0.%pnpinfo:
dev.mfid.0.%location:
dev.mfid.0.%driver: mfid
dev.mfid.0.%desc:
dev.mfid.%parent:
dev.uhub.5.disable_port_power: 0
dev.uhub.5.disable_enumeration: 0
dev.uhub.5.%parent: uhub2
dev.uhub.5.%pnpinfo: vendor=0x8087 product=0x8000 devclass=0x09 devsubclass=0x00 sernum="" release=0x0004 mode=host intclass=0x09
intsubclass=0x00 intprotocol=0x00
dev.uhub.5.%location: bus=2 hubaddr=1 port=1 devaddr=2 interface=0 ugen=ugen2.2
dev.uhub.5.%driver: uhub
dev.uhub.5.%desc: vendor 0x8087 product 0x8000, class 9/0, rev 2.00/0.04, addr 2
dev.uhub.4.disable_port_power: 0
dev.uhub.4.disable_enumeration: 0
dev.uhub.4.%parent: uhub0
dev.uhub.4.%pnpinfo: vendor=0x8087 product=0x8008 devclass=0x09 devsubclass=0x00 sernum="" release=0x0004 mode=host intclass=0x09
intsubclass=0x00 intprotocol=0x00
dev.uhub.4.%location: bus=1 hubaddr=1 port=1 devaddr=2 interface=0 ugen=ugen1.2
dev.uhub.4.%driver: uhub
dev.uhub.4.%desc: vendor 0x8087 product 0x8008, class 9/0, rev 2.00/0.04, addr 2
dev.uhub.3.disable_port_power: 0
dev.uhub.3.disable_enumeration: 0
dev.uhub.3.%parent: uhub1
dev.uhub.3.%pnpinfo: vendor=0x0409 product=0x005a devclass=0x09 devsubclass=0x00 sernum="" release=0x0100 mode=host intclass=0x09
intsubclass=0x00 intprotocol=0x00
dev.uhub.3.%location: bus=0 hubaddr=1 port=6 devaddr=2 interface=0 ugen=ugen0.2
dev.uhub.3.%driver: uhub
dev.uhub.3.%desc: vendor 0x0409 product 0x005a, class 9/0, rev 2.00/1.00, addr 1
dev.uhub.2.disable_port_power: 0
dev.uhub.2.disable_enumeration: 0
dev.uhub.2.%parent: usb2
dev.uhub.2.%pnpinfo:
dev.uhub.2.%location:
dev.uhub.2.%driver: uhub
dev.uhub.2.%desc: Intel EHCI root HUB, class 9/0, rev 2.00/1.00, addr 1
dev.uhub.1.disable_port_power: 0
dev.uhub.1.disable_enumeration: 0
dev.uhub.1.%parent: usb0
dev.uhub.1.%pnpinfo:
dev.uhub.1.%location:
dev.uhub.1.%driver: uhub
dev.uhub.1.%desc: 0x8086 XHCI root HUB, class 9/0, rev 3.00/1.00, addr 1
dev.uhub.0.disable_port_power: 0
dev.uhub.0.disable_enumeration: 0
dev.uhub.0.%parent: usb1
dev.uhub.0.%pnpinfo:
dev.uhub.0.%location:
dev.uhub.0.%driver: uhub
dev.uhub.0.%desc: Intel EHCI root HUB, class 9/0, rev 2.00/1.00, addr 1
dev.uhub.%parent:
dev.cpubfreq.1.%parent: cpu1
dev.cpubfreq.1.%pnpinfo:
dev.cpubfreq.1.%location:
dev.cpubfreq.1.%driver: cpufreq
dev.cpubfreq.1.%desc:
dev.cpubfreq.0.%parent: cpu0
dev.cpubfreq.0.%pnpinfo:
dev.cpubfreq.0.%location:
dev.cpubfreq.0.%driver: cpufreq

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dev.cpubfreq.0.%desc:
dev.cpubfreq.%parent:
dev.est.1.freq_settings: 3100/53000 2900/48172 2800/46082 2600/41518 2500/39557 2300/35732 2200/33398 2000/29812 1900/28070 1700/24245
1600/22615 1400/19464 1300/17511 1100/14571 1000/13151 800/10012
dev.est.1.%parent: cpu1
dev.est.1.%pnpinfo:
dev.est.1.%location:
dev.est.1.%driver: est
dev.est.1.%desc: Enhanced SpeedStep Frequency Control
dev.est.0.freq_settings: 3100/53000 2900/48172 2800/46082 2600/41518 2500/39557 2300/35732 2200/33398 2000/29812 1900/28070 1700/24245
1600/22615 1400/19464 1300/17511 1100/14571 1000/13151 800/10012
dev.est.0.%parent: cpu0
dev.est.0.%pnpinfo:
dev.est.0.%location:
dev.est.0.%driver: est
dev.est.0.%desc: Enhanced SpeedStep Frequency Control
dev.est.%parent:
dev.acpi_perf.1.%parent: cpu1
dev.acpi_perf.1.%pnpinfo:
dev.acpi_perf.1.%location:
dev.acpi_perf.1.%driver: acpi_perf
dev.acpi_perf.1.%desc:
dev.acpi_perf.0.%parent: cpu0
dev.acpi_perf.0.%pnpinfo:
dev.acpi_perf.0.%location:
dev.acpi_perf.0.%driver: acpi_perf
dev.acpi_perf.0.%desc:
dev.acpi_perf.%parent:
dev.vga.0.%parent: isa0
dev.vga.0.%pnpinfo:
dev.vga.0.%location:
dev.vga.0.%driver: vga
dev.vga.0.%desc: Generic ISA VGA
dev.vga.%parent:
dev.sc.0.%parent: isa0
dev.sc.0.%pnpinfo:
dev.sc.0.%location:
dev.sc.0.%driver: sc
dev.sc.0.%desc: System console
dev.sc.%parent:
dev.orm.0.%parent: isa0
dev.orm.0.%pnpinfo:
dev.orm.0.%location:
dev.orm.0.%driver: orm
dev.orm.0.%desc: ISA Option ROMs
dev.orm.%parent:
dev.apic.0.%parent: nexus0
dev.apic.0.%pnpinfo:
dev.apic.0.%location:
dev.apic.0.%driver: apic
dev.apic.0.%desc: APIC resources
dev.apic.%parent:
dev.uart.1.wake: 0
dev.uart.1.%parent: acpi0
dev.uart.1.%pnpinfo: _HID=PNP0501 _UID=2
dev.uart.1.%location: handle=_SB_.PCI0.LPCB.UAR2
dev.uart.1.%driver: uart
dev.uart.1.%desc: 16550 or compatible
dev.uart.0.wake: 0
dev.uart.0.%parent: acpi0
dev.uart.0.%pnpinfo: _HID=PNP0501 _UID=1
dev.uart.0.%location: handle=_SB_.PCI0.LPCB.UAR1
dev.uart.0.%driver: uart
dev.uart.0.%desc: 16550 or compatible
dev.uart.%parent:
dev.fpupnp.0.%parent: acpi0
dev.fpupnp.0.%pnpinfo: _HID=PNP0C04 _UID=0
dev.fpupnp.0.%location: handle=_SB_.PCI0.LPCB.MATH
dev.fpupnp.0.%driver: fpupnp
dev.fpupnp.0.%desc: Legacy ISA coprocessor support
dev.fpupnp.%parent:
dev.atdma.0.%parent: acpi0
dev.atdma.0.%pnpinfo: _HID=PNP0200 _UID=0
dev.atdma.0.%location: handle=_SB_.PCI0.LPCB.DMAC
dev.atdma.0.%driver: atdma
dev.atdma.0.%desc: AT DMA controller
dev.atdma.%parent:
dev.acpi_button.1.%parent: acpi0
dev.acpi_button.1.%pnpinfo: _HID=PNP0C0C _UID=0
dev.acpi_button.1.%location: handle=_SB_.PWRB
dev.acpi_button.1.%driver: acpi_button
dev.acpi_button.1.%desc: Power Button
dev.acpi_button.0.%parent: acpi0
dev.acpi_button.0.%pnpinfo: _HID=PNP0C0E _UID=0
dev.acpi_button.0.%location: handle=_SB_.SLPB
dev.acpi_button.0.%driver: acpi_button
dev.acpi_button.0.%desc: Sleep Button
dev.acpi_button.%parent:
dev.ata.5.%parent: atapci1
dev.ata.5.%pnpinfo:
dev.ata.5.%location: channel=1

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dev.ata.5.%driver: ata
dev.ata.5.%desc: ATA channel
dev.ata.4.%parent: atapci1
dev.ata.4.%pnpinfo:
dev.ata.4.%location: channel=0
dev.ata.4.%driver: ata
dev.ata.4.%desc: ATA channel
dev.ata.3.%parent: atapci0
dev.ata.3.%pnpinfo:
dev.ata.3.%location: channel=1
dev.ata.3.%driver: ata
dev.ata.3.%desc: ATA channel
dev.ata.2.%parent: atapci0
dev.ata.2.%pnpinfo:
dev.ata.2.%location: channel=0
dev.ata.2.%driver: ata
dev.ata.2.%desc: ATA channel
dev.ata.%parent:
dev.atapci.1.%parent: pci0
dev.atapci.1.%pnpinfo: vendor=0x8086 device=0x8c08 subvendor=0x1bcf subdevice=0x8038 class=0x010185
dev.atapci.1.%location: slot=31 function=5 handle=_SB_.PCI0.SAT1
dev.atapci.1.%driver: atapci
dev.atapci.1.%desc: Intel Lynx Point SATA300 controller
dev.atapci.0.%parent: pci0
dev.atapci.0.%pnpinfo: vendor=0x8086 device=0x8c00 subvendor=0x1bcf subdevice=0x8038 class=0x01018f
dev.atapci.0.%location: slot=31 function=2 handle=_SB_.PCI0.SAT0
dev.atapci.0.%driver: atapci
dev.atapci.0.%desc: Intel Lynx Point SATA300 controller
dev.atapci.%parent:
dev.isa.0.%parent: isab0
dev.isa.0.%pnpinfo:
dev.isa.0.%location:
dev.isa.0.%driver: isa
dev.isa.0.%desc: ISA bus
dev.isa.%parent:
dev.isab.0.%parent: pci0
dev.isab.0.%pnpinfo: vendor=0x8086 device=0x8c54 subvendor=0x1bcf subdevice=0x8038 class=0x060100
dev.isab.0.%location: slot=31 function=0 handle=_SB_.PCI0.LPCB
dev.isab.0.%driver: isab
dev.isab.0.%desc: PCI-ISA bridge
dev.isab.%parent:
dev.brgphy.1.%parent: miibus1
dev.brgphy.1.%pnpinfo: oui=0x1be9 model=0x20 rev=0x0
dev.brgphy.1.%location: phyno=2
dev.brgphy.1.%driver: brgphy
dev.brgphy.1.%desc: BCM5717C 1000BASE-T media interface
dev.brgphy.0.%parent: miibus0
dev.brgphy.0.%pnpinfo: oui=0x1be9 model=0x20 rev=0x0
dev.brgphy.0.%location: phyno=1
dev.brgphy.0.%driver: brgphy
dev.brgphy.0.%desc: BCM5717C 1000BASE-T media interface
dev.brgphy.%parent:
dev.miibus.1.%parent: bge1
dev.miibus.1.%pnpinfo:
dev.miibus.1.%location:
dev.miibus.1.%driver: miibus
dev.miibus.1.%desc: MII bus
dev.miibus.0.%parent: bge0
dev.miibus.0.%pnpinfo:
dev.miibus.0.%location:
dev.miibus.0.%driver: miibus
dev.miibus.0.%desc: MII bus
dev.miibus.%parent:
dev.bge.1.stats.tx.BroadcastPkts: 0
dev.bge.1.stats.tx.MulticastPkts: 0
dev.bge.1.stats.tx.UnicastPkts: 0
dev.bge.1.stats.tx.LateCollisions: 0
dev.bge.1.stats.tx.ExcessiveCollisions: 0
dev.bge.1.stats.tx.DeferredTransmissions: 0
dev.bge.1.stats.tx.MultipleCollisionFrames: 0
dev.bge.1.stats.tx.SingleCollisionFrames: 0
dev.bge.1.stats.tx.InternalMacTransmitErrors: 0
dev.bge.1.stats.tx.XoffSent: 0
dev.bge.1.stats.tx.XonSent: 0
dev.bge.1.stats.tx.Collisions: 0
dev.bge.1.stats.tx.ifHCOutOctets: 0
dev.bge.1.stats.rx.UndersizePkts: 0
dev.bge.1.stats.rx.Jabbers: 0
dev.bge.1.stats.rx.FramesTooLong: 0
dev.bge.1.stats.rx.xoffStateEntered: 0
dev.bge.1.stats.rx.ControlFramesReceived: 0
dev.bge.1.stats.rx.xoffPauseFramesReceived: 0
dev.bge.1.stats.rx.xonPauseFramesReceived: 0
dev.bge.1.stats.rx.AlignmentErrors: 0
dev.bge.1.stats.rx.FCSErrors: 0
dev.bge.1.stats.rx.BroadcastPkts: 0
dev.bge.1.stats.rx.MulticastPkts: 0
dev.bge.1.stats.rx.UnicastPkts: 0
dev.bge.1.stats.rx.Fragments: 0
dev.bge.1.stats.rx.ifHCInOctets: 0
dev.bge.1.stats.RecvThresholdHit: 0

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dev.bge.1.stats.InputErrors: 0
dev.bge.1.stats.InputDiscards: 0
dev.bge.1.stats.NoMoreRxBDs: 0
dev.bge.1.stats.DmaWriteHighPriQueueFull: 0
dev.bge.1.stats.DmaWriteQueueFull: 0
dev.bge.1.stats.FramesDroppedDueToFilters: 0
dev.bge.1.forced_udpcsum: 0
dev.bge.1.msi: 1
dev.bge.1.forced_collapse: 0
dev.bge.1.%parent: pci58
dev.bge.1.%pnpinfo: vendor=0x14e4 device=0x1656 subvendor=0x1bcf subdevice=0x8038 class=0x020000
dev.bge.1.%location: slot=0 function=1 handle=_\SB_.PCI0.RP06.LAN2
dev.bge.1.%driver: bge
dev.bge.1.%desc: Broadcom NetXtreme Gigabit Ethernet Controller, ASIC rev. 0x5717100
dev.bge.0.stats.tx.BroadcastPkts: 3
dev.bge.0.stats.tx.MulticastPkts: 0
dev.bge.0.stats.tx.UnicastPkts: 13
dev.bge.0.stats.tx.LateCollisions: 0
dev.bge.0.stats.tx.ExcessiveCollisions: 0
dev.bge.0.stats.tx.DeferredTransmissions: 0
dev.bge.0.stats.tx.MultipleCollisionFrames: 0
dev.bge.0.stats.tx.SingleCollisionFrames: 0
dev.bge.0.stats.tx.InternalMacTransmitErrors: 0
dev.bge.0.stats.tx.XoffSent: 0
dev.bge.0.stats.tx.XonSent: 0
dev.bge.0.stats.tx.Collisions: 0
dev.bge.0.stats.tx.ifHCOutOctets: 1749
dev.bge.0.stats.rx.UndersizePkts: 0
dev.bge.0.stats.rx.Jabbers: 0
dev.bge.0.stats.rx.FramesTooLong: 0
dev.bge.0.stats.rx.xoffStateEntered: 0
dev.bge.0.stats.rx.ControlFramesReceived: 0
dev.bge.0.stats.rx.xoffPauseFramesReceived: 0
dev.bge.0.stats.rx.xonPauseFramesReceived: 0
dev.bge.0.stats.rx.AlignmentErrors: 0
dev.bge.0.stats.rx.FCSErrors: 0
dev.bge.0.stats.rx.BroadcastPkts: 2329
dev.bge.0.stats.rx.MulticastPkts: 4770
dev.bge.0.stats.rx.UnicastPkts: 15
dev.bge.0.stats.rx.Fragments: 0
dev.bge.0.stats.rx.ifHCInOctets: 1155336
dev.bge.0.stats.RecvThresholdHit: 0
dev.bge.0.stats.InputErrors: 0
dev.bge.0.stats.InputDiscards: 0
dev.bge.0.stats.NoMoreRxBDs: 0
dev.bge.0.stats.DmaWriteHighPriQueueFull: 0
dev.bge.0.stats.DmaWriteQueueFull: 0
dev.bge.0.stats.FramesDroppedDueToFilters: 0
dev.bge.0.forced_udpcsum: 0
dev.bge.0.msi: 1
dev.bge.0.forced_collapse: 0
dev.bge.0.%parent: pci58
dev.bge.0.%pnpinfo: vendor=0x14e4 device=0x1656 subvendor=0x1bcf subdevice=0x8038 class=0x020000
dev.bge.0.%location: slot=0 function=0 handle=_\SB_.PCI0.RP06.LAN1
dev.bge.0.%driver: bge
dev.bge.0.%desc: Broadcom NetXtreme Gigabit Ethernet Controller, ASIC rev. 0x5717100
dev.bge.%parent:
dev.vgapci.0.wake: 0
dev.vgapci.0.%parent: pci57
dev.vgapci.0.%pnpinfo: vendor=0x102b device=0x0522 subvendor=0x1bcf subdevice=0x8038 class=0x030000
dev.vgapci.0.%location: slot=0 function=0 handle=_\SB_.PCI0.RP05.PXSX
dev.vgapci.0.%driver: vgapci
dev.vgapci.0.%desc: VGA-compatible display
dev.vgapci.%parent:
dev.mfi.0.wake: 0
dev.mfi.0.keep_deleted_volumes: 0
dev.mfi.0.delete_busy_volumes: 0
dev.mfi.0.%parent: pci33
dev.mfi.0.%pnpinfo: vendor=0x1000 device=0x005b subvendor=0x1000 subdevice=0x9272 class=0x010400
dev.mfi.0.%location: slot=0 function=0 handle=_\SB_.PCI0.RP01.PXSX
dev.mfi.0.%driver: mfi
dev.mfi.0.%desc: ThunderBolt
dev.mfi.%parent:
dev.ehci.1.wake: 0
dev.ehci.1.%parent: pci0
dev.ehci.1.%pnpinfo: vendor=0x8086 device=0x8c26 subvendor=0x1bcf subdevice=0x8038 class=0x0c0320
dev.ehci.1.%location: slot=29 function=0 handle=_\SB_.PCI0.EHC1
dev.ehci.1.%driver: ehci
dev.ehci.1.%desc: Intel Lynx Point USB 2.0 controller USB-A
dev.ehci.0.wake: 0
dev.ehci.0.%parent: pci0
dev.ehci.0.%pnpinfo: vendor=0x8086 device=0x8c2d subvendor=0x1bcf subdevice=0x8038 class=0x0c0320
dev.ehci.0.%location: slot=26 function=0 handle=_\SB_.PCI0.EHC2
dev.ehci.0.%driver: ehci
dev.ehci.0.%desc: Intel Lynx Point USB 2.0 controller USB-B
dev.ehci.%parent:
dev.usbus.2.%parent: ehci1
dev.usbus.2.%pnpinfo:
dev.usbus.2.%location:
dev.usbus.2.%driver: usbus
dev.usbus.2.%desc:

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dev.usbus.1.%parent: ehci0
dev.usbus.1.%pnpinfo:
dev.usbus.1.%location:
dev.usbus.1.%driver: usbhub
dev.usbus.1.%desc:
dev.usbus.0.%parent: xhci0
dev.usbus.0.%pnpinfo:
dev.usbus.0.%location:
dev.usbus.0.%driver: usbhub
dev.usbus.0.%desc:
dev.usbus.%parent:
dev.xhci.0.wake: 0
dev.xhci.0.%parent: pci0
dev.xhci.0.%pnpinfo: vendor=0x8086 device=0x8c31 subvendor=0x1bcf subdevice=0x8038 class=0x0c0330
dev.xhci.0.%location: slot=20 function=0 handle=\_SB_.PCI0.XHC_
dev.xhci.0.%driver: xhci
dev.xhci.0.%desc: Intel Lynx Point USB 3.0 controller
dev.xhci.%parent:
dev.hostb.0.%parent: pci0
dev.hostb.0.%pnpinfo: vendor=0x8086 device=0x0c00 subvendor=0x1bcf subdevice=0x8038 class=0x060000
dev.hostb.0.%location: slot=0 function=0
dev.hostb.0.%driver: hostb
dev.hostb.0.%desc: Host to PCI bridge
dev.hostb.%parent:
dev.pci.58.wake: 0
dev.pci.58.%parent: pcib3
dev.pci.58.%pnpinfo:
dev.pci.58.%location:
dev.pci.58.%driver: pci
dev.pci.58.%desc: ACPI PCI bus
dev.pci.57.wake: 0
dev.pci.57.%parent: pcib2
dev.pci.57.%pnpinfo:
dev.pci.57.%location:
dev.pci.57.%driver: pci
dev.pci.57.%desc: ACPI PCI bus
dev.pci.33.wake: 0
dev.pci.33.%parent: pcib1
dev.pci.33.%pnpinfo:
dev.pci.33.%location:
dev.pci.33.%driver: pci
dev.pci.33.%desc: ACPI PCI bus
dev.pci.0.%parent: pcib0
dev.pci.0.%pnpinfo:
dev.pci.0.%location:
dev.pci.0.%driver: pci
dev.pci.0.%desc: ACPI PCI bus
dev.pci.%parent:
dev.pcib.3.wake: 0
dev.pcib.3.subbus: 64
dev.pcib.3.secbus: 58
dev.pcib.3.pribus: 0
dev.pcib.3.domain: 0
dev.pcib.3.%parent: pci0
dev.pcib.3.%pnpinfo: vendor=0x8086 device=0x8c1a subvendor=0x1bcf subdevice=0x8038 class=0x060400
dev.pcib.3.%location: slot=28 function=5 handle=\_SB_.PCI0.RP06
dev.pcib.3.%driver: pcib
dev.pcib.3.%desc: ACPI PCI-PCI bridge
dev.pcib.2.wake: 0
dev.pcib.2.subbus: 57
dev.pcib.2.secbus: 57
dev.pcib.2.pribus: 0
dev.pcib.2.domain: 0
dev.pcib.2.%parent: pci0
dev.pcib.2.%pnpinfo: vendor=0x8086 device=0x8c18 subvendor=0x1bcf subdevice=0x8038 class=0x060400
dev.pcib.2.%location: slot=28 function=4 handle=\_SB_.PCI0.RP05
dev.pcib.2.%driver: pcib
dev.pcib.2.%desc: ACPI PCI-PCI bridge
dev.pcib.1.wake: 0
dev.pcib.1.subbus: 56
dev.pcib.1.secbus: 33
dev.pcib.1.pribus: 0
dev.pcib.1.domain: 0
dev.pcib.1.%parent: pci0
dev.pcib.1.%pnpinfo: vendor=0x8086 device=0x8c10 subvendor=0x1bcf subdevice=0x8038 class=0x060400
dev.pcib.1.%location: slot=28 function=0 handle=\_SB_.PCI0.RP01
dev.pcib.1.%driver: pcib
dev.pcib.1.%desc: ACPI PCI-PCI bridge
dev.pcib.0.%parent: acpi0
dev.pcib.0.%pnpinfo: _HID=PNP0A08 _UID=0
dev.pcib.0.%location: handle=\_SB_.PCI0
dev.pcib.0.%driver: pcib
dev.pcib.0.%desc: ACPI Host-PCI bridge
dev.pcib.%parent:
dev.pci_link.7.%parent: acpi0
dev.pci_link.7.%pnpinfo: _HID=PNP0C0F _UID=8
dev.pci_link.7.%location: handle=\_SB_.LNKH
dev.pci_link.7.%driver: pci_link
dev.pci_link.7.%desc: ACPI PCI Link LNKH
dev.pci_link.6.%parent: acpi0
dev.pci_link.6.%pnpinfo: _HID=PNP0C0F _UID=7

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dev.pci_link.6.%location: handle=\_SB_.LNKG
dev.pci_link.6.%driver: pci_link
dev.pci_link.6.%desc: ACPI PCI Link LNKG
dev.pci_link.5.%parent: acpi0
dev.pci_link.5.%pnpinfo: _HID=PNP0C0F _UID=6
dev.pci_link.5.%location: handle=\_SB_.LNKF
dev.pci_link.5.%driver: pci_link
dev.pci_link.5.%desc: ACPI PCI Link LNKF
dev.pci_link.4.%parent: acpi0
dev.pci_link.4.%pnpinfo: _HID=PNP0C0F _UID=5
dev.pci_link.4.%location: handle=\_SB_.LNKE
dev.pci_link.4.%driver: pci_link
dev.pci_link.4.%desc: ACPI PCI Link LNKE
dev.pci_link.3.%parent: acpi0
dev.pci_link.3.%pnpinfo: _HID=PNP0C0F _UID=4
dev.pci_link.3.%location: handle=\_SB_.LNKD
dev.pci_link.3.%driver: pci_link
dev.pci_link.3.%desc: ACPI PCI Link LNKD
dev.pci_link.2.%parent: acpi0
dev.pci_link.2.%pnpinfo: _HID=PNP0C0F _UID=3
dev.pci_link.2.%location: handle=\_SB_.LNKC
dev.pci_link.2.%driver: pci_link
dev.pci_link.2.%desc: ACPI PCI Link LNKC
dev.pci_link.1.%parent: acpi0
dev.pci_link.1.%pnpinfo: _HID=PNP0C0F _UID=2
dev.pci_link.1.%location: handle=\_SB_.LNKB
dev.pci_link.1.%driver: pci_link
dev.pci_link.1.%desc: ACPI PCI Link LNKB
dev.pci_link.0.%parent: acpi0
dev.pci_link.0.%pnpinfo: _HID=PNP0C0F _UID=1
dev.pci_link.0.%location: handle=\_SB_.LNKA
dev.pci_link.0.%driver: pci_link
dev.pci_link.0.%desc: ACPI PCI Link LNKA
dev.pci_link.%parent:
dev.acpi_timer.0.%parent: acpi0
dev.acpi_timer.0.%pnpinfo: unknown
dev.acpi_timer.0.%location: unknown
dev.acpi_timer.0.%driver: acpi_timer
dev.acpi_timer.0.%desc: 24-bit timer at 3.579545MHz
dev.acpi_timer.%parent:
dev.attimer.0.%parent: acpi0
dev.attimer.0.%pnpinfo: _HID=PNP0100 _UID=0
dev.attimer.0.%location: handle=\_SB_.PCI0.LPCB.TIMR
dev.attimer.0.%driver: attimer
dev.attimer.0.%desc: AT timer
dev.attimer.%parent:
dev.atrtc.0.%parent: acpi0
dev.atrtc.0.%pnpinfo: _HID=PNP0B00 _UID=0
dev.atrtc.0.%location: handle=\_SB_.PCI0.LPCB.RTC_
dev.atrtc.0.%driver: atrtc
dev.atrtc.0.%desc: AT realtime clock
dev.atrtc.%parent:
dev.hpet.0.%mmap_allow_write: 1
dev.hpet.0.%mmap_allow: 1
dev.hpet.0.%parent: acpi0
dev.hpet.0.%pnpinfo: _HID=PNP0103 _UID=0
dev.hpet.0.%location: handle=\_SB_.PCI0.LPCB.HPET
dev.hpet.0.%driver: hpet
dev.hpet.0.%desc: High Precision Event Timer
dev.hpet.%parent:
dev.cpu.1.%cx_usage: 100.00% last 1158us
dev.cpu.1.%cx_lowest: C1
dev.cpu.1.%cx_supported: C1/1/1
dev.cpu.1.%parent: acpi0
dev.cpu.1.%pnpinfo: _HID=none _UID=0
dev.cpu.1.%location: handle=\_PR_.CPU1
dev.cpu.1.%driver: cpu
dev.cpu.1.%desc: ACPI CPU
dev.cpu.0.%cx_usage: 100.00% 0.00% last 55us
dev.cpu.0.%cx_lowest: C1
dev.cpu.0.%cx_supported: C1/1/1 C2/2/117
dev.cpu.0.%freq_levels: 3100/53000 2900/48172 2800/46082 2600/41518 2500/39557 2300/35732 2200/33398 2000/29812 1900/28070 1700/24245
1600/22615 1400/19464 1300/17511 1100/14571 1000/13151 800/10012
dev.cpu.0.%freq: 3100
dev.cpu.0.%parent: acpi0
dev.cpu.0.%pnpinfo: _HID=none _UID=0
dev.cpu.0.%location: handle=\_PR_.CPU0
dev.cpu.0.%driver: cpu
dev.cpu.0.%desc: ACPI CPU
dev.cpu.%parent:
dev.acpi_sysresource.8.%parent: acpi0
dev.acpi_sysresource.8.%pnpinfo: _HID=PNP0C02 _UID=1
dev.acpi_sysresource.8.%location: handle=\_SB_.PCI0.PDR0
dev.acpi_sysresource.8.%driver: acpi_sysresource
dev.acpi_sysresource.8.%desc: System Resource
dev.acpi_sysresource.7.%parent: acpi0
dev.acpi_sysresource.7.%pnpinfo: _HID=PNP0C01 _UID=241
dev.acpi_sysresource.7.%location: handle=\_SB_.PCI0.RP05.PPF1
dev.acpi_sysresource.7.%driver: acpi_sysresource
dev.acpi_sysresource.7.%desc: System Resource
dev.acpi_sysresource.6.%parent: acpi0

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dev.acpi_sysresource.6.%npninfo: _HID=PNP0C02 _UID=31
dev.acpi_sysresource.6.%location: handle=\_SB_.PCI0.LPCB.MBRD
dev.acpi_sysresource.6.%driver: acpi_sysresource
dev.acpi_sysresource.6.%desc: System Resource
dev.acpi_sysresource.5.%parent: acpi0
dev.acpi_sysresource.5.%npninfo: _HID=PNP0C02 _UID=16
dev.acpi_sysresource.5.%location: handle=\_SB_.PCI0.LPCB.RMCS
dev.acpi_sysresource.5.%driver: acpi_sysresource
dev.acpi_sysresource.5.%desc: System Resource
dev.acpi_sysresource.4.%parent: acpi0
dev.acpi_sysresource.4.%npninfo: _HID=PNP0C02 _UID=78
dev.acpi_sysresource.4.%location: handle=\_SB_.PCI0.LPCB.SIO1
dev.acpi_sysresource.4.%driver: acpi_sysresource
dev.acpi_sysresource.4.%desc: System Resource
dev.acpi_sysresource.3.%parent: acpi0
dev.acpi_sysresource.3.%npninfo: _HID=INT3F0D _UID=0
dev.acpi_sysresource.3.%location: handle=\_SB_.PCI0.LPCB.CWDT
dev.acpi_sysresource.3.%driver: acpi_sysresource
dev.acpi_sysresource.3.%desc: System Resource
dev.acpi_sysresource.2.%parent: acpi0
dev.acpi_sysresource.2.%npninfo: _HID=PNP0C02 _UID=5
dev.acpi_sysresource.2.%location: handle=\_SB_.PCI0.LPCB.LDR2
dev.acpi_sysresource.2.%driver: acpi_sysresource
dev.acpi_sysresource.2.%desc: System Resource
dev.acpi_sysresource.1.%parent: acpi0
dev.acpi_sysresource.1.%npninfo: _HID=PNP0C02 _UID=2
dev.acpi_sysresource.1.%location: handle=\_SB_.PCI0.LPCB.LDRC
dev.acpi_sysresource.1.%driver: acpi_sysresource
dev.acpi_sysresource.1.%desc: System Resource
dev.acpi_sysresource.0.%parent: acpi0
dev.acpi_sysresource.0.%npninfo: _HID=PNP0C01 _UID=1
dev.acpi_sysresource.0.%location: handle=\_SB_.PCI0.TPMX
dev.acpi_sysresource.0.%driver: acpi_sysresource
dev.acpi_sysresource.0.%desc: System Resource
dev.acpi_sysresource.%parent:
dev.acpi.0.%parent: nexus0
dev.acpi.0.%npninfo:
dev.acpi.0.%location:
dev.acpi.0.%driver: acpi
dev.acpi.0.%desc: AMI 06A0
dev.acpi.%parent:
dev.ram.0.%parent: nexus0
dev.ram.0.%npninfo:
dev.ram.0.%location:
dev.ram.0.%driver: ram
dev.ram.0.%desc: System RAM
dev.ram.%parent:
dev.nexus.0.%parent: root0
dev.nexus.0.%npninfo:
dev.nexus.0.%location:
dev.nexus.0.%driver: nexus
dev.nexus.0.%desc:
dev.nexus.%parent:
dev.hv_kvq_log: 0
dev.xen.xsd_kva: 0
dev.xen.xsd_port: 0
dev.xen.balloon.high_mem: 0
dev.xen.balloon.low_mem: 0
dev.xen.balloon.hard_limit: 0
dev.xen.balloon.driver_pages: 0
dev.xen.balloon.target: 0
dev.xen.balloon.current: 0
security.mac.mmmap_revocation_via_cow: 0
security.mac.mmmap_revocation: 1
security.mac.labeled: 0
security.mac.max_slots: 4
security.mac.version: 4
security.bsd.stack_guard_page: 0
security.bsd.unprivileged_get_quota: 0
security.bsd.hardlink_check_gid: 0
security.bsd.hardlink_check_uid: 0
security.bsd.unprivileged_read_msgbuf: 1
security.bsd.unprivileged_idprio: 0
security.bsd.unprivileged_proc_debug: 1
security.bsd.conservative_signals: 1
security.bsd.see_other_gids: 1
security.bsd.see_other_uids: 1
security.bsd.unprivileged_mlock: 1
security.bsd.suser_enabled: 1
security.bsd.map_at_zero: 0
security.jail.param.allow.mount.zfs: 0
security.jail.param.allow.mount.tmpfs: 0
security.jail.param.allow.mount.procfs: 0
security.jail.param.allow.mount.nullfs: 0
security.jail.param.allow.mount.fdescfs: 0
security.jail.param.allow.mount.devfs: 0
security.jail.param.allow.mount.: 0
security.jail.param.allow.socket_af: 0
security.jail.param.allow.quotas: 0
security.jail.param.allow.chflags: 0
security.jail.param.allow.raw_sockets: 0

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security.jail.param.allow.sysvipc: 0
security.jail.param.allow.set_hostname: 0
security.jail.param.ip6.saddrsel: 0
security.jail.param.ip6.: 0
security.jail.param.ip4.saddrsel: 0
security.jail.param.ip4.: 0
security.jail.param.cpuset.id: 0
security.jail.param.host.hostid: 0
security.jail.param.host.hostuid: 64
security.jail.param.host.domainname: 256
security.jail.param.host.hostname: 256
security.jail.param.host.: 0
security.jail.param.children.max: 0
security.jail.param.children.cur: 0
security.jail.param.dying: 0
security.jail.param.persist: 0
security.jail.param.devfs_ruleset: 0
security.jail.param.enforce_statfs: 0
security.jail.param.osrelease: 32
security.jail.param.osreldate: 0
security.jail.param.securelevel: 0
security.jail.param.path: 1024
security.jail.param.name: 256
security.jail.param.parent: 0
security.jail.param.jid: 0
security.jail.devfs_ruleset: 0
security.jail.enforce_statfs: 2
security.jail.mount_zfs_allowed: 0
security.jail.mount_tmpfs_allowed: 0
security.jail.mount_procfs_allowed: 0
security.jail.mount_nullfs_allowed: 0
security.jail.mount_fdscfs_allowed: 0
security.jail.mount_devfs_allowed: 0
security.jail.mount_allowed: 0
security.jail.chflags_allowed: 0
security.jail.allow_raw_sockets: 0
security.jail.sysvipc_allowed: 0
security.jail.socket_unixiproute_only: 1
security.jail.set_hostname_allowed: 1
security.jail.jail_max_af_ips: 255
security.jail.vnet: 0
security.jail.jailed: 0
compat.ia32.maxvmem: 0
compat.ia32.maxssiz: 67108864
compat.ia32.maxdsiz: 536870912

システム起動時メッセージ

```
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FreeBSD 10.2-RELEASE #: Wed Nov 11 16:02:59 JST 2015
    root@verify.bsdc consulting.co.jp:/usr/obj/usr/src/sys/GENERIC amd64
FreeBSD clang version 3.4.1 (tags/RELEASE_34/dot1-final 208032) 20140512
CPU: Intel(R) Pentium(R) CPU G3240 @ 3.10GHz (3092.90-MHz K8-class CPU)
    Origin="GenuineIntel" Id=0x306c3 Family=0x6 Model=0x3c Stepping=3

Features=0xbfebfbff<FPU,VME,DE,PSE,TSC,MSR,PAE,MCE,CX8,APIC,SEP,MTRR,PGE,MCA,CMOV,PAT,PSE36,CLFLUSH,DTS,ACPI,MMX,FXSR,SSE,SSE2,SS,HTT,TM,PBE>
    Features2=0x4ddaebbf<SSE3,PCLMULQDQ,DTES64,MON,DS_CPL,VMX,EST,TM2,SSSE3,
<b11>,CX16,xTPR,PDCM,PCID,SSE4.1,SSE4.2,MOVBE,POPCNT,TSCDLT,XSAVE,OSXSAVE,RDRAND>
    AMD Features=0x2c100800<SYSCALL,NX,Page1GB,RDTSCP,LM>
    AMD Features2=0x21<LAHF,ABM>
    Structured Extended Features=0x2603<FSGSBASE,TSCADJ,ERMS,INVPCID,NFPUSG>
    XSAVE Features=0x1<XSAVEOPT>
    VT-x: PAT,HLT,MTF,PAUSE,EPT,UG,VPID
    TSC: P-state invariant, performance statistics
real memory = 8589934592 (8192 MB)
avail memory = 821211360 (7831 MB)
Event timer "LAPIC" quality 600
ACPI APIC Table: <AMI 06A0 >
FreeBSD/SMP: Multiprocessor System Detected: 2 CPUs
FreeBSD/SMP: 1 package(s) x 2 core(s)
cpu0 (BSP): APIC ID: 0
cpu1 (AP): APIC ID: 2
ioapic0 <Version 2.0> irqs 0-23 on motherboard
random: <Software, Yarrow> initialized
kbd1 at kbdmux0
acpi0: <AMI 06A0> on motherboard
acpi0: Power Button (fixed)
cpu0: <ACPI CPU> on acpi0
cpu1: <ACPI CPU> on acpi0
hpet0: <High Precision Event Timer> iomem 0xfed00000-0xfed003ff on acpi0
Timecounter "HPET" frequency 14318180 Hz quality 950
Event timer "HPET" frequency 14318180 Hz quality 550
Event timer "HPET1" frequency 14318180 Hz quality 440
Event timer "HPET2" frequency 14318180 Hz quality 440
Event timer "HPET3" frequency 14318180 Hz quality 440
Event timer "HPET4" frequency 14318180 Hz quality 440
Event timer "HPET5" frequency 14318180 Hz quality 440
Event timer "HPET6" frequency 14318180 Hz quality 440
atrtc0: <AT realtime clock> port 0x70-0x77 irq 8 on acpi0
Event timer "RTC" frequency 32768 Hz quality 0
attimer0: <AT timer> port 0x40-0x43,0x50-0x53 irq 0 on acpi0
Timecounter "i8254" frequency 1193182 Hz quality 0
Event timer "i8254" frequency 1193182 Hz quality 100
Timecounter "ACPI-fast" frequency 3579545 Hz quality 900
acpi_timer0: <24-bit timer at 3.579545MHz> port 0x1808-0x180b on acpi0
pcib0: <ACPI Host-PCI bridge> port 0xcfc8-0xcfff on acpi0
pci0: <ACPI PCI bus> on pcib0
xhci0: <Intel Lynx Point USB 3.0 controller> mem 0xdfa00000-0xdfaf0fff irq 16 at device 20.0 on pci0
xhci0: 32 bytes context size, 64-bit DMA
xhci0: Port routing mask set to 0xffffffff
usb0 on xhci0
pci0: <simple comms> at device 22.0 (no driver attached)
pci0: <simple comms> at device 22.1 (no driver attached)
ehci0: <Intel Lynx Point USB 2.0 controller USB-B> mem 0xdfa12000-0xdfa123ff irq 16 at device 26.0 on pci0
usb1: EHCI version 1.0
usb1 on ehci0
pcib1: <ACPI PCI-PCI bridge> irq 16 at device 28.0 on pci0
pci33: <ACPI PCI bus> on pcib1
AVAGO MegaRAID SAS FreeBSD mrsas driver version: 06.707.04.03-fbsd
mfi0: <ThunderBolt> port 0xe000-0xe0ff mem 0xdf960000-0xdf963fff,0xdf900000-0xdf93ffff irq 16 at device 0.0 on pci33
mfi0: Using MSI
mfi0: Megaraid SAS driver Ver 4.23
mfi0: FW MaxCmds = 1008, limiting to 128
mfi0: MaxCmd = 1008, Drv MaxCmd = 128, MaxSgl = 70, state = 0xb75003f0
pcib2: <ACPI PCI-PCI bridge> irq 16 at device 28.4 on pci0
pcib2: failed to allocate initial memory window: 0xde000000-0xdf8ffff
pci57: <ACPI PCI bus> on pcib2
vgapci0: <VGA-compatible display> mem 0xde000000-0xdeffffff,0xdf000000-0xdf7ffff irq 16 at device 0.0 on pci57
vgapci0: Boot video device
pcib3: <ACPI PCI-PCI bridge> irq 17 at device 28.5 on pci0
pci58: <ACPI PCI bus> on pcib3
bge0: <Broadcom NetXtreme Gigabit Ethernet Controller, ASIC rev. 0x5717100> mem
0xdfe50000-0xdfe5ffff,0xdfe40000-0xdfe4ffff,0xdfe30000-0xdfe3ffff irq 17 at device 0.0 on pci58
bge0: APE FW version: NCSI v1.2.33.0
bge0: CHIP ID 0x05717100; ASIC REV 0x5717; CHIP REV 0x5717; PCI-E
miibus0: <MII bus> on bge0
brgphy0: <BCM5717C 1000BASE-T media interface> PHY 1 on miibus0
brgphy0: 10baseT, 10baseT-FDX, 100baseTX, 100baseTX-FDX, 1000baseT, 1000baseT-master, 1000baseT-FDX, 1000baseT-FDX-master, auto, auto-flow
bge0: Using defaults for TSO: 65518/35/2048
bge0: Ethernet address: 94:de:80:ff:25:96
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bge1: <Broadcom NetXtreme Gigabit Ethernet Controller, ASIC rev. 0x5717100> mem 0xdfe20000-0xdfef0000,0xdfef0000-0xdfef0000,0xdfef0000-0xdfef0000 irq 18 at device 0.1 on pci58
bge1: APE FW version: NCSI v1.2.33.0
bge1: CHIP ID 0x05717100; ASIC REV 0x5717; CHIP REV 0x57171; PCI-E
miibus1: <MII bus> on bge1
brgphy1: <BCM5717C 1000BASE-T media interface> PHY 2 on miibus1
brgphy1: 10baseT, 10baseT-FDX, 100baseTX, 100baseTX-FDX, 1000baseT, 1000baseT-master, 1000baseT-FDX, 1000baseT-FDX-master, auto, auto-flow
bge1: Using defaults for TSO: 65518/35/2048
bge1: Ethernet address: 94:de:80:ff:25:97
ehci1: <Intel Lynx Point USB 2.0 controller USB-A> mem 0xdfa11000-0xdfa113ff irq 23 at device 29.0 on pci0
usb2: EHCI version 1.0
usb2 on ehci1
isab0: <PCI-ISA bridge> at device 31.0 on pci0
isa0: <ISA bus> on isab0
atapci0: <Intel Lynx Point SATA300 controller> port 0xf0d0-0xf0d7,0xf0c0-0xf0c3,0xf0b0-0xf0b7,0xf0a0-0xf0a3,0xf090-0xf09f,0xf080-0xf08f irq 19 at device 31.2 on pci0
ata2: <ATA channel> at channel 0 on atapci0
ata3: <ATA channel> at channel 1 on atapci0
atapci1: <Intel Lynx Point SATA300 controller> port 0xf070-0xf077,0xf060-0xf063,0xf050-0xf057,0xf040-0xf043,0xf030-0xf03f,0xf020-0xf02f irq 19 at device 31.5 on pci0
ata4: <ATA channel> at channel 0 on atapci1
ata5: <ATA channel> at channel 1 on atapci1
acpi_button0: <Sleep Button> on acpi0
acpi_button1: <Power Button> on acpi0
uart0: <16550 or compatible> port 0x3f8-0x3ff irq 4 flags 0x10 on acpi0
uart1: <16550 or compatible> port 0x2f8-0x2ff irq 3 on acpi0
orm0: <ISA Option ROMs> at iomem 0xc0000-0xc7fff,0xc7800-0xd27ff on isa0
sc0: <System console> at flags 0x100 on isa0
sc0: VGA <16 virtual consoles, flags=0x300>
vga0: <Generic ISA VGA> at port 0x3c0-0x3df iomem 0xa0000-0xbffff on isa0
ppc0: cannot reserve I/O port range
est0: <Enhanced SpeedStep Frequency Control> on cpu0
est1: <Enhanced SpeedStep Frequency Control> on cpu1
mfi0: 2260 (500573121s/0x0020/info) - Shutdown command received from host
mfi0: 2261 (boot + 8s/0x0020/info) - Firmware initialization started (PCI ID 005b/1000/9272/1000)
mfi0: 2262 (boot + 8s/0x0020/info) - Firmware version 3.230.115-3241
mfi0: 2263 (boot + 10s/0x0008/info) - Battery Present
mfi0: 2264 (boot + 10s/0x0008/info) - Battery Present
mfi0: 2265 (boot + 25s/0x0020/info) - Package version 23.11.0-0038
mfi0: 2266 (boot + 25s/0x0020/info) - Board Revision 10D
mfi0: 2267 (boot + 43s/0x0002/info) - Inserted: PD 08(e0xfc/s0)
mfi0: 2268 (boot + 43s/0x0002/info) - Inserted: PD 08(e0xfc/s0) Info: enclPd=fc, scsiType=0, portMap=00, sasAddr=5000cca045069231,0000000000000000
mfi0: 2269 (500573171s/0x0020/info) - Time established as 11/11/15 16:06:11; (47 seconds since power on)
mfi0: 2270 (500573204s/0x0008/info) - Battery temperature is normal
mfi0: 2271 (500573204s/0x0008/WARN) - Battery requires reconditioning; please initiate a LEARN cycle
mfi0: 2272 (500573213s/0x0020/info) - Host driver is loaded and operational
random: unblocking device.
usb0: 5.0Gbps Super Speed USB v3.0
Timecounters tick every 1.000 msec
usb1: 480Mbps High Speed USB v2.0
usb2: 480Mbps High Speed USB v2.0
ugen1.1: <Intel> at usb1
uhub0: <Intel EHCI root HUB, class 9/0, rev 2.00/1.00, addr 1> on usb1
ugen0.1: <0x8086> at usb0
uhub1: <0x8086 XHCI root HUB, class 9/0, rev 3.00/1.00, addr 1> on usb0
ugen2.1: <Intel> at usb2
uhub2: <Intel EHCI root HUB, class 9/0, rev 2.00/1.00, addr 1> on usb2
mfi0 on mfi0
mfi0: 139200MB (285081600 sectors) RAID volume 'array0' is optimal
uhub0: 2 ports with 2 removable, self powered
uhub2: 2 ports with 2 removable, self powered
uhub1: 21 ports with 21 removable, self powered
ugen0.2: <vendor 0x0409> at usb0
uhub3: <vendor 0x0409 product 0x005a, class 9/0, rev 2.00/1.00, addr 1> on usb0
ugen1.2: <vendor 0x8087> at usb1
uhub4: <vendor 0x8087 product 0x8008, class 9/0, rev 2.00/0.04, addr 2> on usb1
ugen2.2: <vendor 0x8087> at usb2
uhub5: <vendor 0x8087 product 0x8000, class 9/0, rev 2.00/0.04, addr 2> on usb2
cd0 at ata4 bus 0 scbus2 target 0 lun 0
cd0: <MATSHITA DVD-ROM UJ8E0 1.00> Removable CD-ROM SCSI device
cd0: Serial Number SP00I2R02764
cd0: 150.000MB/s transfers (SATA 1.x, UDMA5, ATAPI 12bytes, PIO 8192bytes)
cd0: Attempt to query device size failed: NOT READY, Medium not present - tray closed
uhub4: 6 ports with 6 removable, self powered
uhub5: 6 ports with 6 removable, self powered
uhub3: 3 ports with 2 removable, bus powered
SMP: AP CPU #1 Launched!
Timecounter "TSC-low" frequency 1546450516 Hz quality 1000
Root mount waiting for: usb0
ugen0.3: <Topre Corporation> at usb0
ukbd0: <Topre Corporation HHKB Professional, class 0/0, rev 1.10/1.02, addr 2> on usb0
kbd0 at ukbd0
Root mount waiting for: usb0
ugen0.4: <American Megatrends Inc.> at usb0
ukbd1: <Keyboard Interface> on usb0
kbd2 at ukbd1
Trying to mount root from ufs:/dev/mfid0p2 [rw]...
ums0: <Mouse Interface> on usb0
ums0: 3 buttons and [Z] coordinates ID=0

カーネルデバイスツリー

```
nexus0
  apic0
    I/O memory addresses:
      0xfec00000-0xfec0001f
  ram0
    I/O memory addresses:
      0x0-0x97fff
      0x100000-0xb9848fff
      0xb9850000-0xcc7a0fff
      0xcf7f000-0xcf7fffff
      0x10000000-0x22ffffff
  acpi0
    Interrupt request lines:
      0x9
    I/O ports:
      0x10-0x1f
      0x22-0x3f
      0x44-0x5f
      0x62-0x63
      0x65-0x6f
      0x72-0x7f
      0x80
      0x84-0x86
      0x88
      0x8c-0x8e
      0x90-0x9f
      0xa2-0xbf
      0xe0-0xef
      0x4d0-0x4d1
      0x800-0x87f
      0xca4-0xca5
      0x1854-0x1857
    I/O memory addresses:
      0xdf800000-0xdf87ffff
      0xdf894000-0xdf894fff
      0xdf8fe000-0xdf8fefff
      0xe0000000-0xefffffff
      0xfed10000-0xfed17fff
      0xfed18000-0xfed18fff
      0xfed19000-0xfed19fff
      0xfed1c000-0xfed1ffff
      0xfed20000-0xfed3ffff
      0xfed40000-0xfed44fff
      0xfed45000-0xfed8ffff
      0xfed90000-0xfed93fff
      0xfef00000-0xfef0ffff
      0xff000000-0xffffffff
  cpu0
    I/O ports:
      0x1815
  acpi_perf0
  est0
  cpufreq0
  cpu1
  acpi_perf1
  est1
  cpufreq1
  pcib0
    I/O ports:
      0xcf8-0xcff
  pci0
    PCI domain 0 bus numbers:
      0
  hostb0
  xhci0
    Interrupt request lines:
      0x108
    I/O memory addresses:
      0xdfa00000-0xdfa0ffff
  usb0
  uhub1
  uhub3
  ukbd0
  ukbd1
  ums0
  ehci0
    Interrupt request lines:
      0x10
    I/O memory addresses:
      0xdfa12000-0xdfa123ff
  usb1
  uhub0
  uhub4
  pcib1
    I/O ports:
```

```

    0xe000-0xefff
I/O memory addresses:
0xdf900000-0xdf9fffff
PCI domain 0 bus numbers:
33-56
pci33
  pcib1 bus numbers:
    33
  mfi0
    Interrupt request lines:
      0x109
    pcib1 I/O port window:
      0xe000-0xe0ff
    pcib1 memory window:
      0xdf900000-0xdf93ffff
      0xdf960000-0xdf963fff
  mfid0
pci32
  I/O memory addresses:
    0xde000000-0xdf7fffff
  PCI domain 0 bus numbers:
    57
pci57
  pcib2 bus numbers:
    57
  vgapci0
    pcib2 memory window:
      0xde000000-0xdeffffff
      0xdf000000-0xdf7fffff
pci3
  I/O memory addresses:
    0xdfe00000-0xdfefffff
  PCI domain 0 bus numbers:
    58-64
pci58
  pcib3 bus numbers:
    58
  bge0
    Interrupt request lines:
      0x10a
    pcib3 prefetch window:
      0xdfe30000-0xdfe3ffff
      0xdfe40000-0xdfe4ffff
      0xdfe50000-0xdfe5ffff
  miibus0
  brgphy0
  bge1
    Interrupt request lines:
      0x10b
    pcib3 prefetch window:
      0xdfe00000-0xdfe0ffff
      0xdfe10000-0xdfe1ffff
      0xdfe20000-0xdfe2ffff
  miibus1
  brgphy1
ehci1
  Interrupt request lines:
    0x17
  I/O memory addresses:
    0xdfa11000-0xdfa113ff
usb2
  uhub2
  uhub5
isab0
  isa0
  sc0
  vga0
    I/O ports:
      0x3c0-0x3df
    I/O memory addresses:
      0xa0000-0xbffff
  orm0
    I/O memory addresses:
      0xc0000-0xc7fff
      0xcf800-0xd27ff
atapci0
  Interrupt request lines:
    0x13
  I/O ports:
    0xf080-0xf08f
    0xf090-0xf09f
    0xf0a0-0xf0a3
    0xf0b0-0xf0b7
    0xf0c0-0xf0c3
    0xf0d0-0xf0d7
  ata2
  ata3
atapci1
  Interrupt request lines:
    0x13
  I/O ports:

```

```
        0xf020-0xf02f
        0xf030-0xf03f
        0xf040-0xf043
        0xf050-0xf057
        0xf060-0xf063
        0xf070-0xf077
    ata4
    ata5
acpi_sysresource0
atdma0
    DMA request lines:
        4
    I/O ports:
        0xc0-0xdf
hpet0
    Interrupt request lines:
        0x100
        0x101
        0x102
        0x103
        0x104
        0x105
        0x106
        0x107
    I/O memory addresses:
        0xfed0000-0xfed003ff
fpupnp0
    I/O ports:
        0xf0
acpi_sysresource1
acpi_sysresource2
atrtc0
    Interrupt request lines:
        0x8
    I/O ports:
        0x70-0x71
attimer0
    Interrupt request lines:
        0x0
    I/O ports:
        0x40-0x43
acpi_sysresource3
acpi_sysresource4
uart0
    Interrupt request lines:
        0x4
    I/O ports:
        0x3f8-0x3ff
uart1
    Interrupt request lines:
        0x3
    I/O ports:
        0x2f8-0x2ff
acpi_sysresource5
acpi_sysresource6
acpi_sysresource7
acpi_sysresource8
pci_link0
pci_link1
pci_link2
pci_link3
pci_link4
pci_link5
pci_link6
pci_link7
acpi_button0
acpi_button1
acpi_timer0
    I/O ports:
        0x1808-0x180b
```

PCIバスデータ詳細一覧

```
hostb0@pci:0:0:0: class=0x060000 card=0x80381bcf chip=0x0c008086 rev=0x06 hdr=0x00
vendor      = 'Intel Corporation'
device      = 'Haswell DRAM Controller'
class       = bridge
subclass    = HOST-PCI
cap 09[e0] = vendor (length 12) Intel cap 0 version 1
xhci@pci:0:0:20:0: class=0x0c0330 card=0x80381bcf chip=0x8c318086 rev=0x04 hdr=0x00
vendor      = 'Intel Corporation'
device      = 'Lynx Point USB xHCI Host Controller'
class       = serial bus
subclass    = USB
cap 01[70] = powerspec 2 supports D0 D3 current D0
cap 05[80] = MSI supports 8 messages, 64 bit enabled with 1 message
none0@pci:0:0:22:0: class=0x078000 card=0x80381bcf chip=0x8c3a8086 rev=0x04 hdr=0x00
vendor      = 'Intel Corporation'
device      = 'Lynx Point MEI Controller'
class       = simple comms
cap 01[50] = powerspec 3 supports D0 D3 current D0
cap 05[8c] = MSI supports 1 message, 64 bit
nonel@pci:0:0:22:1: class=0x078000 card=0x80381bcf chip=0x8c3b8086 rev=0x04 hdr=0x00
vendor      = 'Intel Corporation'
device      = 'Lynx Point MEI Controller'
class       = simple comms
cap 01[50] = powerspec 3 supports D0 D3 current D0
cap 05[8c] = MSI supports 1 message, 64 bit
ehci0@pci:0:0:26:0: class=0x0c0320 card=0x80381bcf chip=0x8c2d8086 rev=0x04 hdr=0x00
vendor      = 'Intel Corporation'
device      = 'Lynx Point USB Enhanced Host Controller'
class       = serial bus
subclass    = USB
cap 01[50] = powerspec 2 supports D0 D3 current D0
cap 0a[58] = EHCI Debug Port at offset 0xa0 in map 0x14
cap 13[98] = PCI Advanced Features: FLR TP
pcib1@pci:0:0:28:0: class=0x060400 card=0x80381bcf chip=0x8c108086 rev=0xd4 hdr=0x01
vendor      = 'Intel Corporation'
device      = 'Lynx Point PCI Express Root Port'
class       = bridge
subclass    = PCI-PCI
cap 10[40] = PCI-Express 2 root port slot max data 128(128) link x4(x4)
              speed 5.0(5.0) ASPM disabled(L0s/L1)
cap 05[80] = MSI supports 1 message
cap 0d[90] = PCI Bridge card=0x80381bcf
cap 01[a0] = powerspec 3 supports D0 D3 current D0
pcib2@pci:0:0:28:4: class=0x060400 card=0x80381bcf chip=0x8c188086 rev=0xd4 hdr=0x01
vendor      = 'Intel Corporation'
device      = 'Lynx Point PCI Express Root Port'
class       = bridge
subclass    = PCI-PCI
cap 10[40] = PCI-Express 2 root port slot max data 128(128) link x1(x1)
              speed 2.5(5.0) ASPM disabled(L0s/L1)
cap 05[80] = MSI supports 1 message
cap 0d[90] = PCI Bridge card=0x80381bcf
cap 01[a0] = powerspec 3 supports D0 D3 current D0
pcib3@pci:0:0:28:5: class=0x060400 card=0x80381bcf chip=0x8c1a8086 rev=0xd4 hdr=0x01
vendor      = 'Intel Corporation'
device      = 'Lynx Point PCI Express Root Port'
class       = bridge
subclass    = PCI-PCI
cap 10[40] = PCI-Express 2 root port slot max data 128(128) link x1(x1)
              speed 5.0(5.0) ASPM disabled(L0s/L1)
cap 05[80] = MSI supports 1 message
cap 0d[90] = PCI Bridge card=0x80381bcf
cap 01[a0] = powerspec 3 supports D0 D3 current D0
ehci1@pci:0:0:29:0: class=0x0c0320 card=0x80381bcf chip=0x8c268086 rev=0x04 hdr=0x00
vendor      = 'Intel Corporation'
device      = 'Lynx Point USB Enhanced Host Controller'
class       = serial bus
subclass    = USB
cap 01[50] = powerspec 2 supports D0 D3 current D0
cap 0a[58] = EHCI Debug Port at offset 0xa0 in map 0x14
cap 13[98] = PCI Advanced Features: FLR TP
isab0@pci:0:0:31:0: class=0x060100 card=0x80381bcf chip=0x8c548086 rev=0x04 hdr=0x00
vendor      = 'Intel Corporation'
device      = 'Lynx Point LPC Controller'
class       = bridge
subclass    = PCI-ISA
cap 09[e0] = vendor (length 12) Intel cap 1 version 0
              features: AMT, 4 PCI-e x1 slots
atapci@pci:0:0:31:2: class=0x01018f card=0x80381bcf chip=0x8c008086 rev=0x04 hdr=0x00
vendor      = 'Intel Corporation'
device      = 'Lynx Point 4-port SATA Controller 1 [IDE mode]'
class       = mass storage
subclass    = ATA
cap 01[70] = powerspec 3 supports D0 D3 current D0
none2@pci:0:0:31:3: class=0x0c0500 card=0x80381bcf chip=0x8c228086 rev=0x04 hdr=0x00
```

```

vendor      = 'Intel Corporation'
device      = 'Lynx Point SMBus Controller'
class       = serial bus
subclass    = SMBus
atapci1@pci:0:31:5:   class=0x010185 card=0x80381bcf chip=0x8c088086 rev=0x04 hdr=0x00
vendor      = 'Intel Corporation'
device      = 'Lynx Point 2-port SATA Controller 2 [IDE mode]'
class       = mass storage
subclass    = ATA
cap 01[70] = powerspec 3 supports D0 D3 current D0
mfi0@pci:33:0:0:     class=0x010400 card=0x92721000 chip=0x005b1000 rev=0x05 hdr=0x00
vendor      = 'LSI Logic / Symbios Logic'
device      = 'MegaRAID SAS 2208 [Thunderbolt]'
class       = mass storage
subclass    = RAID
cap 01[50] = powerspec 3 supports D0 D1 D2 D3 current D0
cap 10[68] = PCI-Express 2 endpoint max data 128(4096) FLR link x4(x8)
             speed 5.0(8.0) ASPM disabled(L0s)
cap 03[d0] = VPD
cap 05[a8] = MSI supports 1 message, 64 bit enabled with 1 message
cap 11[c0] = MSI-X supports 16 messages
             Table in map 0x14[0x2000], PBA in map 0x14[0x3000]
ecap 0001[100] = AER 2 0 fatal 0 non-fatal 1 corrected
ecap 0019[1e0] = PCIe Sec 1 lane errors 0
ecap 0004[1c0] = Power Budgeting 1
ecap 0016[190] = DPA 1
ecap 000e[148] = ARI 1
vgapci0@pci:57:0:0:   class=0x030000 card=0x80381bcf chip=0x0522102b rev=0x02 hdr=0x00
vendor      = 'Matrox Electronics Systems Ltd.'
device      = 'MGA G200e [Pilot] ServerEngines (SEP1)'
class       = display
subclass    = VGA
cap 01[dc] = powerspec 2 supports D0 D3 current D0
cap 10[e4] = PCI-Express 1 legacy endpoint max data 128(256) link x1(x1)
             speed 2.5(2.5) ASPM disabled(L0s)
cap 05[54] = MSI supports 1 message
bge0@pci:58:0:0:     class=0x020000 card=0x80381bcf chip=0x165614e4 rev=0x10 hdr=0x00
vendor      = 'Broadcom Corporation'
device      = 'NetXtreme BCM5718 Gigabit Ethernet PCIe'
class       = network
subclass    = ethernet
cap 01[48] = powerspec 3 supports D0 D3 current D0
cap 03[50] = VPD
cap 05[58] = MSI supports 8 messages, 64 bit enabled with 1 message
cap 11[a0] = MSI-X supports 17 messages
             Table in map 0x20[0x0], PBA in map 0x20[0x1000]
cap 10[ac] = PCI-Express 2 endpoint max data 128(256) FLR link x1(x1)
             speed 5.0(5.0) ASPM disabled(L1)
ecap 0001[100] = AER 1 0 fatal 0 non-fatal 1 corrected
ecap 0003[13c] = Serial 1 000094de80ff2596
ecap 0004[150] = Power Budgeting 1
ecap 0002[160] = VC 1 max VC0
bge1@pci:58:0:1:     class=0x020000 card=0x80381bcf chip=0x165614e4 rev=0x10 hdr=0x00
vendor      = 'Broadcom Corporation'
device      = 'NetXtreme BCM5718 Gigabit Ethernet PCIe'
class       = network
subclass    = ethernet
cap 01[48] = powerspec 3 supports D0 D3 current D0
cap 03[50] = VPD
cap 05[58] = MSI supports 8 messages, 64 bit enabled with 1 message
cap 11[a0] = MSI-X supports 17 messages
             Table in map 0x20[0x0], PBA in map 0x20[0x1000]
cap 10[ac] = PCI-Express 2 endpoint max data 128(256) FLR link x1(x1)
             speed 5.0(5.0) ASPM disabled(L1)
ecap 0001[100] = AER 1 0 fatal 0 non-fatal 1 corrected
ecap 0003[13c] = Serial 1 000094de80ff2597
ecap 0004[150] = Power Budgeting 1
ecap 0002[160] = VC 1 max VC0

```

ACPI電源管理データ詳細一覧

```
RSD PTR: OEM=AMI, ACPI_Rev=2.0x (2)
XSDT=0x00000000cc7f3090, length=36, cksum=209
XSDT: Length=156, Revision=1, Checksum=23,
OEMID=AMI, OEM Table ID=06A0, OEM Revision=0x1072009,
Creator ID=AMI, Creator Revision=0x10013
Entries={ 0x00000000cc8017e8, 0x00000000cc8018f8, 0x00000000cc801960, 0x00000000cc8019a8, 0x00000000cc801f30, 0x00000000cc802ab0,
0x00000000cc802d90, 0x00000000cc8030d8, 0x00000000cc803118, 0x00000000cc8031d8, 0x00000000cc803210, 0x00000000cc8036a8,
0x00000000cc809208, 0x00000000cc809408, 0x00000000cc809458 }
FACP: Length=268, Revision=5, Checksum=215,
OEMID=AMI, OEM Table ID=06A0, OEM Revision=0x1072009,
Creator ID=AMI, Creator Revision=0x10013
FACS=0xccfa7080, DSDT=0xcc7f31b8
INT_MODEL=APIC
Preferred_PM_Profile=Desktop (1)
SCI_INT=9
SMI_CMD=0xb2, ACPI_ENABLE=0xa0, ACPI_DISABLE=0xa1, S4BIOS_REQ=0x0
PSTATE_CNT=0x0
PM1a_EVT_BLK=0x1800-0x1803
PM1a_CNT_BLK=0x1804-0x1805
PM2_CNT_BLK=0x1850-0x1850
PM_TMR_BLK=0x1808-0x180b
GPE0_BLK=0x1820-0x182f
P_LVL2_LAT=101 us, P_LVL3_LAT=1001 us
FLUSH_SIZE=1024, FLUSH_STRIDE=16
DUTY_OFFSET=0, DUTY_WIDTH=0
DAY_ALRM=13, MON_ALRM=0, CENTURY=50
IAPC_BOOT_ARCH={NO_ASPM}
Flags={WBINVD,C1_SUPPORTED,C2_MP_SUPPORTED,SLEEP_BUTTON,S4_RTC_WAKE,RESET_REGISTER,PLATFORM_CLOCK,S4_RTC_VALID,REMOTE_POWER_ON}
RESET_REG=0x539:0[8] (IO), RESET_VALUE=0xf7
FACS: Length=64, HwSig=0x0000001a, FirmWake_Vec=0x00000000
Global_Lock=
Flags=
Version=2
DSDT: Length=58925, Revision=2, Checksum=227,
OEMID=AMI, OEM Table ID=06A0, OEM Revision=0x0,
Creator ID=INTL, Creator Revision=0x20091112
APIC: Length=98, Revision=3, Checksum=163,
OEMID=AMI, OEM Table ID=06A0, OEM Revision=0x1072009,
Creator ID=AMI, Creator Revision=0x10013
Local APIC ADDR=0xfe000000
Flags={PC-AT}
Type=Local APIC
ACPI CPU=1
Flags={ENABLED}
APIC ID=0
Type=Local APIC
ACPI CPU=2
Flags={ENABLED}
APIC ID=2
Type=IO APIC
APIC ID=8
INT BASE=0
ADDR=0x00000000fec00000
Type=INT Override
BUS=0
IRQ=0
INTR=2
Flags={Polarity=conforming, Trigger=conforming}
Type=INT Override
BUS=0
IRQ=9
INTR=9
Flags={Polarity=active-hi, Trigger=level}
Type=Local APIC NMI
ACPI CPU=ALL
LINT Pin=1
Flags={Polarity=active-hi, Trigger=edge}
FPDT: Length=68, Revision=1, Checksum=28,
OEMID=AMI, OEM Table ID=06A0, OEM Revision=0x1072009,
Creator ID=AMI, Creator Revision=0x10013
SSDT: Length=1411, Revision=1, Checksum=30,
OEMID=PmRef, OEM Table ID=Cpu0Ist, OEM Revision=0x3000,
Creator ID=INTL, Creator Revision=0x20051117
SSDT: Length=2937, Revision=1, Checksum=202,
OEMID=CpuRef, OEM Table ID=CpuSsdT, OEM Revision=0x3000,
Creator ID=INTL, Creator Revision=0x20051117
SSDT: Length=734, Revision=1, Checksum=191,
OEMID=PmRef, OEM Table ID=Cpu0Tst, OEM Revision=0x3000,
Creator ID=INTL, Creator Revision=0x20051117
SSDT: Length=840, Revision=1, Checksum=74,
OEMID=PmRef, OEM Table ID=ApTst, OEM Revision=0x3000,
Creator ID=INTL, Creator Revision=0x20051117
MCFG: Length=60, Revision=1, Checksum=55,
OEMID=AMI, OEM Table ID=06A0, OEM Revision=0x1072009,
Creator ID=MSFT, Creator Revision=0x97
```

```

Base Address=0x00000000e0000000
Segment Group=0x0000
Start Bus=0
End Bus=255
PRAD: Length=190, Revision=2, Checksum=119,
OEMID=PRADID, OEM Table ID=PRADTID, OEM Revision=0x1,
Creator ID=MSFT, Creator Revision=0x3000001
HPET: Length=56, Revision=1, Checksum=236,
OEMID=AMI, OEM Table ID=06A0, OEM Revision=0x1072009,
Creator ID=AMI., Creator Revision=0x5
HPET Number=0
ADDR=0x00000000fed00000:0[64] (Memory) HW Rev=0x1
Comparators=7
Counter Size=1
Legacy IRQ routing capable={TRUE}
PCI Vendor ID=0x8086
Minimal Tick=14318
SSDT: Length=1173, Revision=1, Checksum=1,
OEMID=IdeRef, OEM Table ID=IdeTable, OEM Revision=0x1000,
Creator ID=INTL, Creator Revision=0x20120711
SSDT: Length=23390, Revision=1, Checksum=21,
OEMID=SaSsdT, OEM Table ID=SaSsdT, OEM Revision=0x3000,
Creator ID=INTL, Creator Revision=0x20120711
SSDT: Length=505, Revision=1, Checksum=51,
OEMID=SgRef, OEM Table ID=SgPeg, OEM Revision=0x1000,
Creator ID=INTL, Creator Revision=0x20120711
SPCR: Length=80, Revision=1, Checksum=99,
OEMID=A M I, OEM Table ID=APTI04, OEM Revision=0x1072009,
Creator ID=AMI., Creator Revision=0x5
BERT: Length=48, Revision=1, Checksum=156,
OEMID=AMI, OEM Table ID=AMI BERT, OEM Revision=0x0,
Creator ID=AMI, Creator Revision=0x10013
If ((EP_B == Zero))
    EP_B = (EPBR << 0x0C)
If ((MH_B == Zero))
    MH_B = (MHR << 0x0F)
If ((PC_B == Zero))
    PC_B = (PXBR << 0x1A)
If ((PC_L == Zero))
    PC_L = (0x10000000 >> PXSZ) /* \_SB_.PCI0.PXSZ */
If ((DM_B == Zero))
    DM_B = (DIBR << 0x0C)
Local0 = GPCL ()
PBMX = ((Local0 >> 0x14) - 0x02)
PBLN = ((Local0 >> 0x14) - One)
C0LN = Zero
If ((PM1L == One))
    C0RW = Zero
    C4LN = Zero
If ((PM1H == One))
    C4RW = Zero
    C8LN = Zero
If ((PM2L == One))
    C8RW = Zero
    CCLN = Zero
If ((PM2H == One))
    CCRW = Zero
    D0LN = Zero
If ((PM3L == One))
    D0RW = Zero
    D4LN = Zero
If ((PM3H == One))
    D4RW = Zero
    D8LN = Zero
If ((PM4L == One))
    D8RW = Zero
    DCLN = Zero
If ((PM4H == One))
    DCRW = Zero
    E0LN = Zero
If ((PM5L == One))
    E0RW = Zero
    E4LN = Zero
If ((PM5H == One))
    E4RW = Zero
    E8LN = Zero
If ((PM6L == One))
    E8RW = Zero
    ECLN = Zero
If ((PM6H == One))
    ECRW = Zero
    F0LN = Zero
If ((PM0H == One))
    F0RW = Zero
M1MN = (TLUD << 0x14)
M1LN = ((M1MX - M1MN) + One)
If ((M64L == Zero))
    MSLN = Zero
    M2LN = M64L /* \M64L */
    M2MN = M64B /* \M64B */
    M2MX = ((M2MN + M2LN) - One)

```

```

Local0 = Arg3
If ((OSYS >= 0x07DC))
    If ((XCNT == Zero))
If ((Arg0 == GUID))
    SUPP = CDW2 /* \_SB_.PCI0._OSC.CDW2 */
    CTRL = CDW3 /* \_SB_.PCI0._OSC.CDW3 */
    If ((NEXP == Zero))
        CTRL &= 0xFFFFFFFF8
    If ((Arg1 != One))
        CDW1 |= 0x08
    If ((CDW3 != CTRL))
        CDW1 |= 0x10
    CDW3 = CTRL /* \_SB_.PCI0.CTRL */
    OSCC = CTRL /* \_SB_.PCI0.CTRL */
    CDW1 |= 0x04
    If ((VDID == 0xFFFFFFFF))
    While ((PSPX != Zero))
        PSPX = One
        Local0 = 0xC8
        PMSX = One
        Local0 = Zero
    If ((VDID == 0xFFFFFFFF))
    While ((PSPX != Zero))
        PSPX = One
        Local0 = 0xC8
        PMSX = One
        Local0 = Zero
    If ((VDID == 0xFFFFFFFF))
    While ((PSPX != Zero))
        PSPX = One
        Local0 = 0xC8
        PMSX = One
        Local0 = Zero
        If ((HPAS == One))
            HPT0 = 0xFED01000
        If ((HPAS == 0x02))
            HPT0 = 0xFED02000
        If ((HPAS == 0x03))
            HPT0 = 0xFED03000
    If ((PCHS == One))
    If ((PCHS == One))
    INDX = 0x5A
    LDN = Arg0
    INDX = 0xA5
    Local0 = ACTR /* \_SB_.PCI0.LPCB.SIO1.ACTR */
    If ((Local0 == 0xFF))
    Local0 &= One
    IOST |= (Local0 << Arg0)
    If (((DMCH < 0x04) && ((Local1 = (DMCH & 0x03)) != Zero)))
    ACTR = Arg1
    Local1 = (IOAH << 0x08)
    Local1 |= IOAL
    IO11 = (IOAH << 0x08)
    IO11 |= IOAL /* \_SB_.PCI0.LPCB.SIO1.IO11 */
    IO12 = IO11 /* \_SB_.PCI0.LPCB.SIO1.IO11 */
    Local0 = (FindSetRightBit (IO11) - One)
    LEN1 = (One << Local0)
    IRQM = (One << INTR) /* \_SB_.PCI0.LPCB.SIO1.INTR */
    IRQM = Zero
    If (((DMCH > 0x03) || (Arg1 == Zero)))
        DMAM = Zero
        Local1 = (DMCH & 0x03)
        DMAM = (One << Local1)
    Local1 = (IOAH << 0x08)
    Local1 |= IOAL
    IOAL = (IO11 & 0xFF)
    IOAH = (IO11 >> 0x08)
    INTR = (Local0 - One)
    INTR = Zero
    DMCH = (Local0 - One)
    DMCH = 0x04
    If ((VDID == 0xFFFFFFFF))
    While ((PSPX != Zero))
        PSPX = One
        Local0 = 0xC8
        PMSX = One
        Local0 = Zero
    If ((VDID == 0xFFFFFFFF))
    While ((PSPX != Zero))
        PSPX = One
        Local0 = 0xC8
        PMSX = One
        Local0 = Zero
    If ((VDID == 0xFFFFFFFF))
    While ((PSPX != Zero))
        PSPX = One
        Local0 = 0xC8
        PMSX = One
        Local0 = Zero
    If ((VDID == 0xFFFFFFFF))
    While ((PSPX != Zero))
        PSPX = One
        Local0 = 0xC8
        PMSX = One
        Local0 = Zero

```



```

        PSPX = One
        Local0 = 0xC8
        PMSX = One
        Local0 = Zero
    If ((VDID == 0xFFFFFFFF))
    While ((PSPX != Zero))
        PSPX = One
        Local0 = 0xC8
        PMSX = One
        Local0 = Zero
    If ((MECS != 0x05))
If (((PCHG == One) && (PCHS == One)))
If (((PCHG == One) && (PCHS == 0x02)))
If (((PCHG == 0x02) && (PCHS == 0x02)))
    ^PCI0.LPCB.PARC |= 0x80
    IRQ0 = Zero
    IRQ0 = (One << (^PCI0.LPCB.PARC & 0x0F))
    ^PCI0.LPCB.PARC = Local0
    ^PCI0.LPCB.PBRC |= 0x80
    IRQ0 = Zero
    IRQ0 = (One << (^PCI0.LPCB.PBRC & 0x0F))
    ^PCI0.LPCB.PBRC = Local0
    ^PCI0.LPCB.PCRC |= 0x80
    IRQ0 = Zero
    IRQ0 = (One << (^PCI0.LPCB.PCRC & 0x0F))
    ^PCI0.LPCB.PCRC = Local0
    ^PCI0.LPCB.PDRC |= 0x80
    IRQ0 = Zero
    IRQ0 = (One << (^PCI0.LPCB.PDRC & 0x0F))
    ^PCI0.LPCB.PDRC = Local0
    ^PCI0.LPCB.PERC |= 0x80
    IRQ0 = Zero
    IRQ0 = (One << (^PCI0.LPCB.PERC & 0x0F))
    ^PCI0.LPCB.PERC = Local0
    ^PCI0.LPCB.PFRC |= 0x80
    IRQ0 = Zero
    IRQ0 = (One << (^PCI0.LPCB.PFRC & 0x0F))
    ^PCI0.LPCB.PFRC = Local0
    ^PCI0.LPCB.PGRC |= 0x80
    IRQ0 = Zero
    IRQ0 = (One << (^PCI0.LPCB.PGRC & 0x0F))
    ^PCI0.LPCB.PGRC = Local0
    ^PCI0.LPCB.PHRC |= 0x80
    IRQ0 = Zero
    IRQ0 = (One << (^PCI0.LPCB.PHRC & 0x0F))
    ^PCI0.LPCB.PHRC = Local0
If ((PMBV == Zero))
    PMBV = (\_SB.PCI0.LPCB.ACBA << 0x07)
If ((GPBV == Zero))
    GPBV = (\_SB.PCI0.LPCB.GPBA << 0x07)
If ((RCBV == Zero))
    RCBV = (\_SB.PCI0.LPCB.RCBA << 0x0E)
If ((Arg0 <= 0x5E))
    Local0 = ((GPBS () + 0x0100) + (Arg0 * 0x08))
If ((Arg0 <= 0x5E))
    Local0 = ((GPBS () + 0x0100) + (Arg0 * 0x08))
If ((Arg0 <= 0x5E))
    Local0 = ((GPBS () + 0x0100) + (Arg0 * 0x08))
    TEMP = Arg1
If ((Arg0 <= 0x5E))
    Local0 = ((GPBS () + 0x0100) + (Arg0 * 0x08))
    TEMP = Arg1
If ((Arg0 <= 0x5E))
    Local0 = ((GPBS () + 0x0104) + (Arg0 * 0x08))
    TEMP = Arg1
If ((Arg0 <= 0x5E))
    Local0 = ((GPBS () + 0x0100) + (Arg0 * 0x08))
    TEMP = Arg1
If ((Arg0 <= 0x5E))
    Local0 = ((GPBS () + 0x0104) + (Arg0 * 0x08))
    If ((Arg1 == One))
        GPIS = Zero
        GPWP = Zero
        GPWP = 0x02
        GPIS = One
    Local0 = (GPBS () + 0x10)
    If ((Arg0 >= 0x2D))
        Local1 = (Arg0 - 0x28)
        If ((Arg0 <= 0x0A))
            Local1 = (Arg0 - 0x08)
            Local1 = (Arg0 - 0x0A)
        Local2 = (One << Local1)
        TEMP |= Local2
        TEMP &= ~Local2
If ((Arg0 <= 0x5E))
    Local0 = (GPBS () + 0x10)
    If ((Arg0 >= 0x2D))
        Local1 = (Arg0 - 0x28)
        If ((Arg0 <= 0x0A))
            Local1 = (Arg0 - 0x08)
            Local1 = (Arg0 - 0x0A)

```

```

Local2 = (One << Local1)
TEMP |= Local2
TEMP &= ~Local2
PMEE = Arg0
If ((DVID == 0xFFFF))
    PWST = One
    PMES = One
PMEE = Arg0
If ((DVID == 0xFFFF))
    PMES = One
        If ((Arg0 == ToUUID ("a5fc708f-8775-4ba6-bd0c-ba90a1ec72f8")))
            _T_0 = ToInteger (Arg2)
            If ((_T_0 == Zero))
                If ((Arg1 == One))
                    If ((_T_0 == One))
                        If ((SDGV == 0xFF))
                            If ((_T_0 == 0x02))
            If ((Arg0 == ToUUID ("a5fc708f-8775-4ba6-bd0c-ba90a1ec72f8")))
                _T_0 = ToInteger (Arg2)
                If ((_T_0 == Zero))
                    If ((Arg1 == One))
                        If ((_T_0 == One))
                            If ((SDGV == 0xFF))
                                If ((_T_0 == 0x02))
            If ((Arg0 == ToUUID ("a5fc708f-8775-4ba6-bd0c-ba90a1ec72f8")))
                _T_0 = ToInteger (Arg2)
                If ((_T_0 == Zero))
                    If ((Arg1 == One))
                        If ((_T_0 == One))
                            If ((SDGV == 0xFF))
                                If ((_T_0 == 0x02))
            If ((Arg0 == ToUUID ("a5fc708f-8775-4ba6-bd0c-ba90a1ec72f8")))
                _T_0 = ToInteger (Arg2)
                If ((_T_0 == Zero))
                    If ((Arg1 == One))
                        If ((_T_0 == One))
                            If ((SDGV == 0xFF))
                                If ((_T_0 == 0x02))

PMEE = Arg0
If ((DVID == 0xFFFF))
    PMES = One
        If ((Arg0 == ToUUID ("a5fc708f-8775-4ba6-bd0c-ba90a1ec72f8")))
            _T_0 = ToInteger (Arg2)
            If ((_T_0 == Zero))
                If ((Arg1 == One))
                    If ((_T_0 == One))
                        If ((SDGV == 0xFF))
                            If ((_T_0 == 0x02))
            If ((Arg0 == ToUUID ("a5fc708f-8775-4ba6-bd0c-ba90a1ec72f8")))
                _T_0 = ToInteger (Arg2)
                If ((_T_0 == Zero))
                    If ((Arg1 == One))
                        If ((_T_0 == One))
                            If ((SDGV == 0xFF))
                                If ((_T_0 == 0x02))

PMEE = Arg0
If ((DVID == 0xFFFF))
Local0 = PMES /* _SB_.PCI0.XHC_.PMES */
PMES = One
If ((Arg0 <= XHPC))
    If ((PCHV () == LPTH))
        _T_0 = Arg0
        If ((_T_0 == One))
            If ((_T_0 == 0x02))
                If ((_T_0 == 0x03))
                    If ((_T_0 == 0x04))
                        If ((_T_0 == 0x05))
                            If ((_T_0 == 0x06))
                                If ((_T_0 == 0x07))
                                    If ((_T_0 == 0x08))
                                        If ((_T_0 == 0x09))
                                            If ((_T_0 == 0x0A))
                                                If ((_T_0 == 0x0B))
                                                    If ((_T_0 == 0x0C))
                                                        If ((_T_0 == 0x0D))
                                                            If ((_T_0 == 0x0E))

        _T_1 = Arg0
        If ((_T_1 == One))
            If ((_T_1 == 0x02))
                If ((_T_1 == 0x03))
                    If ((_T_1 == 0x04))
                        If ((_T_1 == 0x05))
                            If ((_T_1 == 0x06))
                                If ((_T_1 == 0x07))
                                    If ((_T_1 == 0x08))
                                        If ((_T_1 == 0x09))
                                            If ((_T_1 == 0x0A))

If (((Arg0 == (XHPC + One)) && (XRPC == One)))
    If ((PCHV () == LPTH))
    If ((PCHV () == LPTL))
    If ((PCHV () == WPTL))

```

```

If ((Arg0 >= XSPA))
    Local0 = (Arg0 - XSPA) /* \XSPA */
    _T_2 = Local0
    If ((_T_2 == Zero))
        If ((_T_2 == One))
            If ((_T_2 == 0x02))
                If ((_T_2 == 0x03))
                    If ((_T_2 == 0x04))
                        If ((_T_2 == 0x05))

If ((DVID == 0xFFFF))
    Local2 = MEMB /* \_SB_.PCI0.XHC_.MEMB */
    Local1 = PDBM /* \_SB_.PCI0.XHC_.PDBM */
    PDBM &= 0xFFFFFFFFFFFF9
    Local3 = D0D3 /* \_SB_.PCI0.XHC_.D0D3 */
    D0D3 = Zero
    MEMB = SRMB /* \SRMB */
    PDBM = (Local1 | 0x02)
    If (((PCHV () == LPTL) || ((PCHV () == WPTL) && (PCHP == 0x40))))
        If ((PCHG == One))
            MB13 = Zero
            MB14 = Zero
        If ((PCHG == 0x02))
            MW13 = Zero
            MW14 = Zero
        CLK0 = Zero
        CLK1 = Zero
    If ((PCHG == One) || ((PCHV () == WPTL) && (PCHP == 0x40)))
        CLK2 = One
    If ((PCHS == 0x02))
        Local3 = SRMB /* \SRMB */
        If ((PCHG == One))
            Local3 += 0x0510
        If ((PCHG == 0x02))
            Local3 += 0x0530
        While (((((PSC1 & 0x03F8) == 0x02E0) || ((PSC2 &
            0x03F8) == 0x02E0)) || ((PSC3 & 0x03F8) == 0x02E0) || ((PSC4 &
            0x03F8) == 0x02E0))))
            Local4 = Zero
            Local0 = (PSC1 & 0xFFFFFFFFFFFFFFD)
            If (((Local0 & 0x000203F9) == 0x02A0))
                PSC1 = (Local0 | 0x80000000)
                Local4 |= One
            Local0 = (PSC2 & 0xFFFFFFFFFFFFFFD)
            If (((Local0 & 0x000203F9) == 0x02A0))
                PSC2 = (Local0 | 0x80000000)
                Local4 |= 0x02
            Local0 = (PSC3 & 0xFFFFFFFFFFFFFFD)
            If (((Local0 & 0x000203F9) == 0x02A0))
                PSC3 = (Local0 | 0x80000000)
                Local4 |= 0x04
            Local0 = (PSC4 & 0xFFFFFFFFFFFFFFD)
            If (((Local0 & 0x000203F9) == 0x02A0))
                PSC4 = (Local0 | 0x80000000)
                Local4 |= 0x08
            Local0 = (PSC1 & 0xFFFFFFFFFFFFFFD)
            PSC1 = (Local0 | 0x00FE0000)
            Local0 = (PSC2 & 0xFFFFFFFFFFFFFFD)
            PSC2 = (Local0 | 0x00FE0000)
            Local0 = (PSC3 & 0xFFFFFFFFFFFFFFD)
            PSC3 = (Local0 | 0x00FE0000)
            Local0 = (PSC4 & 0xFFFFFFFFFFFFFFD)
            PSC4 = (Local0 | 0x00FE0000)
        AX15 = One
    If (((PCHG == One) || ((PCHV () == WPTL) && (PCHP ==
    0x40) || (PCHP == 0x41))))
        SWAI = Zero
        SAIP = Zero
    PDBM &= 0xFFFFFFFFFFFFFFD
    MEMB = Local2
    PDBM = Local1
    Local1 = PDBM /* \_SB_.PCI0.XHC_.PDBM */
    Local2 = MEMB /* \_SB_.PCI0.XHC_.MEMB */
    PDBM &= 0xFFFFFFFFFFFF9
    D0D3 = Zero
    MEMB = SRMB /* \SRMB */
    PDBM = (Local1 | 0x02)
    PMES = One
    PMEE = One
    If ((PCHV () == LPTL) || ((PCHV () == WPTL) && (PCHP == 0x40)))
        If ((PCHG == One))
            MB13 = One
            MB14 = One
        If ((PCHG == 0x02))
            MW13 = One
            MW14 = One
        CLK0 = One
        CLK1 = One
    If ((PCHG == One) || ((PCHV () == WPTL) && (PCHP == 0x40)))
        CLK2 = Zero
    If ((PCHS == 0x02))
        AX15 = Zero

```

```

If ((PCHG == One) || ((PCHV () == WPTL) && ((PCHP ==
0x40) || (PCHP == 0x41))))
    SWAI = One
    SAIP = One
PDBM &= 0xFFFFFFFFFFFFD
D0D3 = 0x03
MEMB = Local2
PDBM = Local1
If ((Arg0 == ToUUID ("7c9512a9-1705-4cb4-af7d-506a2423ab71")))
If ((XHCI == Zero))
    CDW1 |= 0x02
If (((XHCI == 0x02) || (XHCI == 0x03)))
    ^^LPCB.XUSB = One
    XRST = One
    Local0 = Zero
    Local0 = (PR3 & 0xFFFFFFFF0)
    Local1 = (Local0 | PR3M) /* \_SB_.PCI0.XHC_.PR3M */
    Local1 &= 0xFFFFFFFF3
    PR3 = Local1
    Local0 = Zero
    Local0 = (PR2 & 0xFFFF8000)
    PR2 = (Local0 | PR2M) /* \_SB_.PCI0.XHC_.PR2M */
If (((XHCI == 0x02) || (XHCI == 0x03)))
    PR3 &= 0xFFFFFFFF0
    PR2 &= 0xFFFF8000
    ^^LPCB.XUSB = Zero
    XRST = Zero
If ((^^LPCB.XUSB == One) || (XRST == One))
    If ((DVID == 0xFFFF))
If ((XRPC >= One))
    PMEE = Arg0
    If ((DVID == 0xFFFF))
        PMES = One
    If ((PCHG == 0x02))
    If ((PCHG == 0x02))
    B0VL = ADB0 /* \ADB0 */
    B1VL = ADB1 /* \ADB1 */
    If ((ADI0 != Zero))
        IRQN = ADI0 /* \ADI0 */
    EOD = One
    If ((ADB0 == Zero))
    If ((EOD == Zero))
    If ((S0ID == One))
    If ((ANCS == One))
    EOD = Zero
    If ((Arg4 == Zero))
        If ((Arg4 == 0x02))
            CAIR = Arg1
            If ((Arg4 == One))
                Local0 = (Arg2 & RPCD) /* \_SB_.PCI0.SAT0.RDCA.RPCD */
                Local0 |= Arg3
                RPCD = Local0
            If ((Arg4 == 0x03))
                CAIR = Arg1
                Local0 = (Arg2 & CADR) /* \_SB_.PCI0.SAT0.RDCA.CADR */
                Local0 |= Arg3
                CADR = Local0
    If ((PCIT == Zero))
    If ((PMSI != Zero))
    If ((PLIP != Zero))
    If ((PLTP != Zero))
    While (((RDCA (Zero, 0x52, Zero, Zero, Zero) & 0x2000) == Zero))
        If ((PCIT == Zero))
            PCMD = RDCA (Zero, 0x04, Zero, Zero, 0x02)
            If ((PCIT == One))
                PRBI = 0x24
                PRBD = RDCA (Zero, 0x24, Zero, Zero, 0x02)
                If ((PCIT == 0x02))
                    PRBI = 0x10
                    PRBD = RDCA (Zero, 0x10, Zero, Zero, 0x02)
                If ((PCIT == Zero))
                    Local0 = RDCA (Zero, (PMCP + 0x04), Zero, Zero, 0x02)
                    If (((Local0 & 0x08) == Zero))
I2CE = Zero
HSTS = 0xBF
TXSA = Arg0
HCOM = Arg1
HCON = 0x48
    HSTS |= 0xFF
I2CE = Zero
HSTS = 0xBF
TXSA = (Arg0 | One)
HCON = 0x44
    HSTS |= 0xFF
I2CE = Zero
HSTS = 0xBF
TXSA = Arg0
HCOM = Arg1
DAT0 = Arg2
HCON = 0x48
    HSTS |= 0xFF

```

```

I2CE = Zero
HSTS = 0xBF
TXSA = (Arg0 | One)
HCOM = Arg1
HCON = 0x48
    HSTS |= 0xFF
I2CE = Zero
HSTS = 0xBF
TXSA = Arg0
HCOM = Arg1
DAT1 = (Arg2 & 0xFF)
DAT0 = ((Arg2 >> 0x08) & 0xFF)
HCON = 0x4C
    HSTS |= 0xFF
I2CE = Zero
HSTS = 0xBF
TXSA = (Arg0 | One)
HCOM = Arg1
HCON = 0x4C
    HSTS |= 0xFF
I2CE = Arg3
HSTS = 0xBF
TXSA = Arg0
HCOM = Arg1
DAT0 = SizeOf (Arg2)
Local1 = Zero
HBDR = DerefOf (Index (Arg2, Zero))
HCON = 0x54
    Local0 = 0x0FA0
    HSTS = 0x80
        HBDR = DerefOf (Index (Arg2, Local1))
    HSTS |= 0xFF
I2CE = Arg2
HSTS = 0xBF
TXSA = (Arg0 | One)
HCOM = Arg1
HCON = 0x54
Local0 = 0x0FA0
Index (TBUF, Zero) = DAT0 /* \_SB_.PCI0.SBUS.DAT0 */
HSTS = 0x80
Local1 = One
    Local0 = 0x0FA0
    Index (TBUF, Local1) = HBDR /* \_SB_.PCI0.SBUS.HBDR */
    HSTS = 0x80
    HSTS |= 0xFF
Local0 = 0xC8
    If ((Local0 == Zero))
        Local0 = Zero
Local0 = 0x0FA0
    If ((Local0 == Zero))
        Local0 = 0x0FA0
    If ((Local0 == Zero))
        Local0 = 0x0FA0
HCON |= 0x02
HSTS |= 0xFF
SLPX = One
SLPE = One
SLPE = Zero
LTRE = LTR1 /* \LTR1 */
LMSL = PML1 /* \PML1 */
LNSL = PNL1 /* \PNL1 */
OBFF = OBF1 /* \OBF1 */
LTRE = LTR2 /* \LTR2 */
LMSL = PML2 /* \PML2 */
LNSL = PNL2 /* \PNL2 */
OBFF = OBF2 /* \OBF2 */
LTRE = LTR3 /* \LTR3 */
LMSL = PML3 /* \PML3 */
LNSL = PNL3 /* \PNL3 */
OBFF = OBF3 /* \OBF3 */
LTRE = LTR4 /* \LTR4 */
LMSL = PML4 /* \PML4 */
LNSL = PNL4 /* \PNL4 */
OBFF = OBF4 /* \OBF4 */
LTRE = LTR5 /* \LTR5 */
LMSL = PML5 /* \PML5 */
LNSL = PNL5 /* \PNL5 */
OBFF = OBF5 /* \OBF5 */
LTRE = LTR6 /* \LTR6 */
LMSL = PML6 /* \PML6 */
LNSL = PNL6 /* \PNL6 */
OBFF = OBF6 /* \OBF6 */
LTRE = LTR7 /* \LTR7 */
LMSL = PML7 /* \PML7 */
LNSL = PNL7 /* \PNL7 */
OBFF = OBF7 /* \OBF7 */
LTRE = LTR8 /* \LTR8 */
LMSL = PML8 /* \PML8 */
LNSL = PNL8 /* \PNL8 */
OBFF = OBF8 /* \OBF8 */
Local0 = 0xFF
_T_0 = (Arg0 + Zero)

```

```

If ((_T_0 == 0x03F8))
  Local0 = Zero
  If ((_T_0 == 0x02F8))
    Local0 = One
    If ((_T_0 == 0x0220))
      Local0 = 0x02
      If ((_T_0 == 0x0228))
        Local0 = 0x03
        If ((_T_0 == 0x0238))
          Local0 = 0x04
          If ((_T_0 == 0x02E8))
            Local0 = 0x05
            If ((_T_0 == 0x0338))
              Local0 = 0x06
              If ((_T_0 == 0x03E8))
                Local0 = 0x07
_T_0 = (Arg0 + Zero)
If ((_T_0 == Zero))
  CALE = Zero
  Local0 = UXDV (Arg2)
  If ((Local0 != 0xFF))
    CARN = Local0
    CALE = One
  If ((_T_0 == One))
    CBLE = Zero
    Local0 = UXDV (Arg2)
    If ((Local0 != 0xFF))
      CBDR = Local0
      CBLE = One
  If ((_T_0 == 0x02))
    LTLE = Zero
    If ((Arg2 == 0x0378))
      LTDR = Zero
    If ((Arg2 == 0x0278))
      LTDR = One
    If ((Arg2 == 0x03BC))
      LTDR = 0x02
      LTLE = One
  If ((_T_0 == 0x03))
    FDLE = Zero
    If ((Arg2 == 0x03F0))
      FDDR = Zero
    If ((Arg2 == 0x0370))
      FDDR = One
      FDLE = One
    If ((_T_0 == 0x08))
      If ((Arg2 == 0x0200))
        GLE = One
        GLE = Zero
      If ((Arg2 == 0x0208))
        GHLE = One
        GHLE = Zero
    If ((_T_0 == 0x09))
      If ((Arg2 == 0x0200))
        GLE = One
        GLE = Zero
      If ((Arg2 == 0x0208))
        GHLE = One
        GHLE = Zero
    If ((_T_0 == 0x0A))
      If (((Arg2 == 0x60) || (Arg2 == 0x64)))
        KCLE = One
        KCLE = Zero
    If ((_T_0 == 0x0B))
      If (((Arg2 == 0x62) || (Arg2 == 0x66)))
        MCLE = One
        MCLE = Zero
    If ((_T_0 == 0x0C))
      If ((Arg2 == 0x2E))
        C1LE = One
        C1LE = Zero
      If ((Arg2 == 0x4E))
        C2LE = One
        C2LE = Zero
    If ((_T_0 == 0x0D))
      If ((Arg2 == 0x2E))
        C1LE = One
        C1LE = Zero
      If ((Arg2 == 0x4E))
        C2LE = One
        C2LE = Zero

PA0H = PM0H /* \_SB_.PCI0.PM0H */
PA1H = PM1H /* \_SB_.PCI0.PM1H */
PA1L = PM1L /* \_SB_.PCI0.PM1L */
PA2H = PM2H /* \_SB_.PCI0.PM2H */
PA2L = PM2L /* \_SB_.PCI0.PM2L */
PA3H = PM3H /* \_SB_.PCI0.PM3H */
PA3L = PM3L /* \_SB_.PCI0.PM3L */
PA4H = PM4H /* \_SB_.PCI0.PM4H */
PA4L = PM4L /* \_SB_.PCI0.PM4L */
PA5H = PM5H /* \_SB_.PCI0.PM5H */

```

```

PA5L = PM5L /* \_SB_.PCI0.PM5L */
PA6H = PM6H /* \_SB_.PCI0.PM6H */
PA6L = PM6L /* \_SB_.PCI0.PM6L */
PM0H = PA0H /* \_SB_.PCI0.PA0H */
PM1H = PA1H /* \_SB_.PCI0.PA1H */
PM1L = PA1L /* \_SB_.PCI0.PA1L */
PM2H = PA2H /* \_SB_.PCI0.PA2H */
PM2L = PA2L /* \_SB_.PCI0.PA2L */
PM3H = PA3H /* \_SB_.PCI0.PA3H */
PM3L = PA3L /* \_SB_.PCI0.PA3L */
PM4H = PA4H /* \_SB_.PCI0.PA4H */
PM4L = PA4L /* \_SB_.PCI0.PA4L */
PM5H = PA5H /* \_SB_.PCI0.PA5H */
PM5L = PA5L /* \_SB_.PCI0.PA5L */
PM6H = PA6H /* \_SB_.PCI0.PA6H */
PM6L = PA6L /* \_SB_.PCI0.PA6L */
If ((NFCE == 0x02))
    If ((NFCE == 0x03))
If (((NFCE == 0x02) || (NFCE == 0x03)))
If (((BID == 0x43) || (BID == 0x4A)))
    DFUP = 0x42
    DFUP = 0x46
    _T_0 = BID /* \BID_ */
    If ((_T_0 == 0x30))
        If ((DFUE == 0x03))
If ((Arg0 == ToUUID ("5630831c-06c9-4856-b327-f5d32586e060")))
    If ((Zero == ToInteger (Arg1)))
        _T_0 = ToInteger (Arg2)
        If ((_T_0 == Zero))
            If ((_T_0 == One))
                Local0 = Derefof (Index (Arg3, Zero))
                If ((Local0 == One))
                    If ((PCHS == One))
                        If ((BID == 0x43))
                            GL08 |= 0x04
                            GL08 |= 0x40
                        If ((PCHS == One))
                            If ((BID == 0x43))
                                GL08 &= 0xFB
                                GL08 &= 0xBF
                    If ((_T_0 == 0x02))
                        If ((PCHS == One))
                            If ((BID == 0x43))
                                Local0 = ((GL08 & 0x04) >> 0x02)
                                Local0 = ((GL08 & 0x40) >> 0x06)
                                Local0 = \_SB_.RDGP (DFUP)
If ((Arg0 == Zero))
    P80D = ((P80D & 0xFFFFF00) | Arg1)
If ((Arg0 == One))
    P80D = ((P80D & 0xFFFF00FF) | (Arg1 << 0x08))
If ((Arg0 == 0x02))
    P80D = ((P80D & 0xFF00FFFF) | (Arg1 << 0x10))
If ((Arg0 == 0x03))
    P80D = ((P80D & 0x00FFFFFF) | (Arg1 << 0x18))
P80H = P80D /* \P80D */
GPIC = Arg0
PICM = Arg0
P80D = Zero
If (((Arg0 == 0x03) || (Arg0 == 0x04) || (Arg0 == 0x05)))
    If ((PFLV == 0x02))
        GP27 = One
If ((BID == 0x31))
If (((BID == 0x80) || (BID == 0x81) || (BID ==
0x82) || (BID == 0x83)))
If (((BID == 0x84) || (BID == 0x85) || (BID == 0x86)))
    If ((BID == 0x31))
        If (((Arg0 == 0x03) || (Arg0 == 0x04)))
            If (((\_SB_.PCI0.B0D3.ABAR & 0xFFFFC004) != 0xFFFFC004) && ((
\_SB_.PCI0.B0D3.ABAR & 0xFFFFC000) != Zero)))
                \_SB_.PCI0.B0D3.BARA = \_SB_.PCI0.B0D3.ABAR
                \_SB_.PCI0.GFX0.STAT = ((\_SB_.PCI0.GFX0.STAT & 0xFFFFFFFFFFFFFFFC) | One)
                \_SB_.PCI0.GFX0.STAT = (\_SB_.PCI0.GFX0.STAT & 0xFFFFFFFFFFFFFFFC)
            If ((\_SB_.IAOE.ITMR == Zero))
                \_SB_.IAOE.ECTM = Zero
                \_SB_.IAOE.RCTM = Zero
If ((Arg0 == 0x03))
    If ((Zero == ACTT))
        If ((ECON == One))
If (((Arg0 == 0x03) || (Arg0 == 0x04)))
    If ((ECON == One))
        If (((Arg0 == 0x03) || (Arg0 == 0x04)))
            LIDS = \_SB_.PCI0.LPCB.H_EC.ECRD (Refof (\_SB_.PCI0.LPCB.H_EC.LSTE))
            If ((LIDS == Zero))
                \_SB_.PCI0.GFX0.CLID = 0x80000000
            If ((LIDS == One))
                \_SB_.PCI0.GFX0.CLID = 0x80000003
        If ((\_SB_.PCI0.LPCB.H_EC.ECRD (Refof (\_SB_.PCI0.LPCB.H_EC.DOCK)) != DSTS))
            DSTS = \_SB_.PCI0.LPCB.H_EC.ECRD (Refof (\_SB_.PCI0.LPCB.H_EC.DOCK))
            \_SB_.PCI0.HDEF.DCKA = DSTS /* \DSTS */
        If ((DSTS == One))
            If ((Arg0 == 0x03))

```

```

        SSMP = DKSM /* \DKSM */
    If ((BNUM == Zero))
        If ((\_SB.PCI0.LPCB.H_EC.ECRD (RefOf (\_SB.PCI0.LPCB.H_EC.VPWR)) != PWRS))
            PWRS = \_SB.PCI0.LPCB.H_EC.ECRD (RefOf (\_SB.PCI0.LPCB.H_EC.VPWR))
        If ((\_SB.PCI0.LPCB.H_EC.ECRD (RefOf (\_SB.PCI0.LPCB.H_EC.RPWR)) != PWRS))
            PWRS = \_SB.PCI0.LPCB.H_EC.ECRD (RefOf (\_SB.PCI0.LPCB.H_EC.RPWR))
    If ((RP1D == Zero))
    If ((RP2D == Zero))
    If ((RP3D == Zero))
    If ((RP4D == Zero))
    If ((RP5D == Zero))
    If ((RP6D == Zero))
    If ((RP7D == Zero))
        If ((DSTS == Zero))
    If ((RP8D == Zero))
        If ((DSTS == Zero))
    If (((Arg0 == 0x03) || (Arg0 == 0x04)))
    Local0 = (Arg0 * 0x08)
    Local1 = (Arg1 * 0x08)
    If ((ECON == One))
        B1SC = \_SB.PCI0.LPCB.H_EC.ECRD (RefOf (\_SB.PCI0.LPCB.H_EC.B1CC))
        B1SS = \_SB.PCI0.LPCB.H_EC.ECRD (RefOf (\_SB.PCI0.LPCB.H_EC.B1ST))
        B2SC = \_SB.PCI0.LPCB.H_EC.ECRD (RefOf (\_SB.PCI0.LPCB.H_EC.B2CC))
        B2SS = \_SB.PCI0.LPCB.H_EC.ECRD (RefOf (\_SB.PCI0.LPCB.H_EC.B2ST))
    If ((CSEM == One))
        CSEM = One
        PLSV = PPL1 /* \PPL1 */
        PLEN = PL1E /* \PL1E */
        CLMP = CLP1 /* \CLP1 */
    If ((PWRU == Zero))
        PPUU = One
        PPUU = (PWRU-- << 0x02)
    Local0 = (PLVL * PPUU) /* \SPL1.PPUU */
    Local1 = (Local0 / 0x03E8)
    PPL1 = Local1
    PL1E = One
    CLP1 = One
    PPL1 = PLSV /* \PLSV */
    PL1E = PLEN /* \PLEN */
    CLP1 = CLMP /* \CLMP */
    CSEM = Zero
    If ((Arg0 != DDPS))
        DDPS = Arg0
        UAMS = (Arg0 && !PWRS)
            \_PR.CPU0._PPC = (SizeOf (\_PR.CPU0._PSS) - One)
            \_PR.CPU0._PPC = Zero
    If ((OSYS == 0x07DC))
    SMIF = Arg1
    If ((Arg0 == 0x02))
        \_PR.DTSF = Arg1
        \_PR.TRPD = Zero
    If ((Arg0 == 0x03))
        TRPH = Zero
    If ((Arg0 == 0x04))
        \_PR.TRPF = Zero
        OSYS = 0x07D9
            OSYS = 0x07D9
            OSYS = 0x07DC
            OSYS = 0x07DD
        ^RP01.HPEX = Zero
        ^RP02.HPEX = Zero
        ^RP03.HPEX = Zero
        ^RP04.HPEX = Zero
        ^RP05.HPEX = Zero
        ^RP06.HPEX = Zero
        ^RP07.HPEX = Zero
        ^RP08.HPEX = Zero
        ^RP01.HPSX = One
        ^RP02.HPSX = One
        ^RP03.HPSX = One
        ^RP04.HPSX = One
        ^RP05.HPSX = One
        ^RP06.HPSX = One
        ^RP07.HPSX = One
        ^RP08.HPSX = One
        ^RP01.PMEX = Zero
        ^RP02.PMEX = Zero
        ^RP03.PMEX = Zero
        ^RP04.PMEX = Zero
        ^RP05.PMEX = Zero
        ^RP06.PMEX = Zero
        ^RP07.PMEX = Zero
        ^RP08.PMEX = Zero
        ^RP01.PMSX = One
        ^RP02.PMSX = One
        ^RP03.PMSX = One
        ^RP04.PMSX = One
        ^RP05.PMSX = One
        ^RP06.PMSX = One
        ^RP07.PMSX = One
        ^RP08.PMSX = One

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```

Index (PRWP, Zero) = Arg0
Local0 |= (SS1 << One)
Local0 |= (SS2 << 0x02)
Local0 |= (SS3 << 0x03)
Local0 |= (SS4 << 0x04)
    Index (PRWP, One) = Arg1
    Local0 >>= One
    RBR0 = (^LPCB.RCBA << 0x0E)
    SNR0 = SRMB /* \SRMB */
    MBR0 = GMHB ()
    DBR0 = GDMB ()
    EBR0 = GEPB ()
    XBR0 = GPCB ()
    XSZ0 = GPCL ()
If ((DIDX & 0xF00) == 0x0400)
    If ((RP1D == Zero))
    If ((RP2D == Zero))
    If ((RP3D == Zero))
    If ((RP4D == Zero))
    If ((RP5D == Zero))
    If ((RP6D == Zero))
    If ((RP7D == Zero))
    If ((RP8D == Zero))
    If ((\_SB.PCI0.D1F0 == One))
    If ((\_SB.PCI0.D1F1 == One))
    If ((\_SB.PCI0.D1F2 == One))
    L01C += One
    If (((RP1D == Zero) && \_SB.PCI0.RP01.HPSX))
        \_SB.PCI0.RP01.PDCX = One
        \_SB.PCI0.RP01.HPSX = One
        \_SB.PCI0.RP01.L0SE = Zero
        \_SB.PCI0.RP01.HPSX = One
    If (((RP2D == Zero) && \_SB.PCI0.RP02.HPSX))
        \_SB.PCI0.RP02.PDCX = One
        \_SB.PCI0.RP02.HPSX = One
        \_SB.PCI0.RP02.L0SE = Zero
        \_SB.PCI0.RP02.HPSX = One
    If (((RP3D == Zero) && \_SB.PCI0.RP03.HPSX))
        \_SB.PCI0.RP03.PDCX = One
        \_SB.PCI0.RP03.HPSX = One
        \_SB.PCI0.RP03.L0SE = Zero
        \_SB.PCI0.RP03.HPSX = One
    If (((RP4D == Zero) && \_SB.PCI0.RP04.HPSX))
        \_SB.PCI0.RP04.PDCX = One
        \_SB.PCI0.RP04.HPSX = One
        \_SB.PCI0.RP04.L0SE = Zero
        \_SB.PCI0.RP04.HPSX = One
    If (((RP5D == Zero) && \_SB.PCI0.RP05.HPSX))
        If (((TBTS != One) || (TBSE != 0x05)))
            \_SB.PCI0.RP05.PDCX = One
            \_SB.PCI0.RP05.HPSX = One
            \_SB.PCI0.RP05.L0SE = Zero
            If (((TBTS != One) || (TBSE != 0x05)))
                \_SB.PCI0.RP05.HPSX = One
    If (((RP6D == Zero) && \_SB.PCI0.RP06.HPSX))
        If (((TBTS != One) || (TBSE != 0x06)))
            \_SB.PCI0.RP06.PDCX = One
            \_SB.PCI0.RP06.HPSX = One
            \_SB.PCI0.RP06.L0SE = Zero
            If (((TBTS != One) || (TBSE != 0x06)))
                \_SB.PCI0.RP06.HPSX = One
    If (((RP7D == Zero) && \_SB.PCI0.RP07.HPSX))
        If (((TBTS != One) || (TBSE != 0x07)))
            \_SB.PCI0.RP07.PDCX = One
            \_SB.PCI0.RP07.HPSX = One
            \_SB.PCI0.RP07.L0SE = Zero
            If ((PFLV == 0x02))
                If (((TBTS != One) || (TBSE != 0x07)))
                    If ((\_SB.PCI0.LPCB.H_EC.ECRD (RefOf (\_SB.PCI0.LPCB.H_EC.DOCK)) == Zero))
                        \_SB.PCI0.RP07.HPSX = One
    If (((RP8D == Zero) && \_SB.PCI0.RP08.HPSX))
        If (((TBTS != One) || (TBSE != 0x08)))
            \_SB.PCI0.RP08.PDCX = One
            \_SB.PCI0.RP08.HPSX = One
            \_SB.PCI0.RP08.L0SE = Zero
            If ((PFLV == 0x02))
                If (((TBTS != One) || (TBSE != 0x08)))
                    If ((\_SB.PCI0.LPCB.H_EC.ECRD (RefOf (\_SB.PCI0.LPCB.H_EC.DOCK)) == Zero))
                        \_SB.PCI0.RP08.HPSX = One
\_SB.PCI0.SBUS.HSTS = 0x20
If ((BID == 0x35))
If ((Arg0 == ToUUID ("1730e71d-e5dd-4a34-be57-4d76b6a2fe37")))
    If ((Arg2 == Zero))
        If ((Arg1 == Zero))
        If ((Arg2 == One))
            \_T_0 = DerefOf (Index (Arg3, Zero))
            If ((\_T_0 == Zero)) {}
            If ((\_T_0 == One))
                If ((\_T_0 == 0x02)) {}
                If ((\_T_0 == 0x03)) {}
    If ((Arg0 == ToUUID ("7574eb17-d1a2-4cc2-9929-4a08fcc29107")))

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```

    _T_1 = Arg2
    If ((_T_1 == Zero))
        If ((Arg1 == Zero))
            If ((_T_1 == One))
                If ((_T_1 == 0x02))
If ((Arg0 == ToUUID ("1730e71d-e5dd-4a34-be57-4d76b6a2fe37")))
    If ((Arg2 == Zero))
        If ((Arg1 == Zero))
            If ((Arg2 == One))
                _T_0 = Deref0f (Index (Arg3, Zero))
                If ((_T_0 == Zero)) {}
                If ((_T_0 == One))
                    If ((_T_0 == 0x02)) {}
                    If ((_T_0 == 0x03)) {}
                If ((Arg0 == ToUUID ("7574eb17-d1a2-4cc2-9929-4a08fcc29107")))
                    _T_1 = Arg2
                    If ((_T_1 == Zero))
                        If ((Arg1 == Zero))
                            If ((_T_1 == One))
                                If ((_T_1 == 0x02))
If ((Arg0 == ToUUID ("1730e71d-e5dd-4a34-be57-4d76b6a2fe37")))
    If ((Arg2 == Zero))
        If ((Arg1 == Zero))
            If ((Arg2 == One))
                _T_0 = Deref0f (Index (Arg3, Zero))
                If ((_T_0 == Zero)) {}
                If ((_T_0 == One))
                    If ((_T_0 == 0x02)) {}
                    If ((_T_0 == 0x03)) {}
                If ((Arg0 == ToUUID ("7574eb17-d1a2-4cc2-9929-4a08fcc29107")))
                    _T_1 = Arg2
                    If ((_T_1 == Zero))
                        If ((Arg1 == Zero))
                            If ((_T_1 == One))
                                If ((_T_1 == 0x02))
If ((Arg0 == ToUUID ("1730e71d-e5dd-4a34-be57-4d76b6a2fe37")))
    If ((Arg2 == Zero))
        If ((Arg1 == Zero))
            If ((Arg2 == One))
                _T_0 = Deref0f (Index (Arg3, Zero))
                If ((_T_0 == Zero)) {}
                If ((_T_0 == One))
                    If ((_T_0 == 0x02)) {}
                    If ((_T_0 == 0x03)) {}
                If ((Arg0 == ToUUID ("7574eb17-d1a2-4cc2-9929-4a08fcc29107")))
                    _T_1 = Arg2
                    If ((_T_1 == Zero))
                        If ((Arg1 == Zero))
                            If ((_T_1 == One))
                                If ((_T_1 == 0x02))
If ((Arg0 == ToUUID ("1730e71d-e5dd-4a34-be57-4d76b6a2fe37")))
    If ((Arg2 == Zero))
        If ((Arg1 == Zero))
            If ((Arg2 == One))
                _T_0 = Deref0f (Index (Arg3, Zero))
                If ((_T_0 == Zero)) {}
                If ((_T_0 == One))
                    If ((_T_0 == 0x02)) {}
                    If ((_T_0 == 0x03)) {}
                If ((Arg0 == ToUUID ("7574eb17-d1a2-4cc2-9929-4a08fcc29107")))
                    _T_1 = Arg2
                    If ((_T_1 == Zero))
                        If ((Arg1 == Zero))
                            If ((_T_1 == One))
                                If ((_T_1 == 0x02))
If ((Arg0 == ToUUID ("1730e71d-e5dd-4a34-be57-4d76b6a2fe37")))
    If ((Arg2 == Zero))
        If ((Arg1 == Zero))
            If ((Arg2 == One))
                _T_0 = Deref0f (Index (Arg3, Zero))
                If ((_T_0 == Zero)) {}
                If ((_T_0 == One))
                    If ((_T_0 == 0x02)) {}
                    If ((_T_0 == 0x03)) {}
                If ((Arg0 == ToUUID ("7574eb17-d1a2-4cc2-9929-4a08fcc29107")))
                    _T_1 = Arg2
                    If ((_T_1 == Zero))
                        If ((Arg1 == Zero))
                            If ((_T_1 == One))
                                If ((_T_1 == 0x02))
If ((Arg0 == ToUUID ("1730e71d-e5dd-4a34-be57-4d76b6a2fe37")))
    If ((Arg2 == Zero))
        If ((Arg1 == Zero))
            If ((Arg2 == One))
                _T_0 = Deref0f (Index (Arg3, Zero))
                If ((_T_0 == Zero)) {}
                If ((_T_0 == One))
                    If ((_T_0 == 0x02)) {}
                    If ((_T_0 == 0x03)) {}
                If ((Arg0 == ToUUID ("7574eb17-d1a2-4cc2-9929-4a08fcc29107")))
                    _T_1 = Arg2
                    If ((_T_1 == Zero))
                        If ((Arg1 == Zero))
                            If ((_T_1 == One))
                                If ((_T_1 == 0x02))

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```

        If ((_T_1 == Zero))
            If ((Arg1 == Zero))
                If ((_T_1 == One))
                    If ((_T_1 == 0x02))
If ((Arg0 == ToUUID ("1730e71d-e5dd-4a34-be57-4d76b6a2fe37")))
    If ((Arg2 == Zero))
        If ((Arg1 == Zero))
    If ((Arg2 == One))
        _T_0 = Deref0f (Index (Arg3, Zero))
        If ((_T_0 == Zero)) {}
        If ((_T_0 == One))
            If ((_T_0 == 0x02)) {}
            If ((_T_0 == 0x03)) {}
    If ((Arg0 == ToUUID ("7574eb17-d1a2-4cc2-9929-4a08fcc29107")))
        _T_1 = Arg2
        If ((_T_1 == Zero))
            If ((Arg1 == Zero))
            If ((_T_1 == One))
                If ((_T_1 == 0x02))

If ((SHTP == One))
If ((SYST == One))
    POS0 = One
    POS1 = Zero
If ((PCHS == 0x02))
    VIS &= Zero
        If ((OSTB >= OWLH))
If ((Arg0 == ToUUID ("0811b06e-4a27-44f9-8d60-3cbbc22e7b48")) /* Platform-wide Capabilities */)
    Local0 = Arg3
    PURE = One
        PURE = One
    Local0 = NMEN /* \_SB_.PCI0.HEC2.NMEN */
    Local1 = RPS7 /* \_SB_.PCI0.HEC2.RPS7 */
    Local2 = RTS7 /* \_SB_.PCI0.HEC2.RTS7 */
    Local3 = CPS7 /* \_SB_.PCI0.HEC2.CPS7 */
    Local4 = CTS7 /* \_SB_.PCI0.HEC2.CTS7 */
        PURE = One
    DBG8 = 0x11
    HRD = One
    DBG8 = 0x12
    HIE = One
    DBG8 = 0x13
    HIG = One
    DBG8 = 0x14
    DBG8 = 0x15
    HIE = Zero
    HRD = Zero
    HIG = One
    DBG8 = 0x16
    Local0 = ((Arg2 << 0x10) | (Arg1 << 0x08))
    Local0 |= Arg0
    CBWW = 0x80040011
    CBWW = Local0
    HIG = One
    CBWW = 0x80040011
    CBWW = Arg0
    HIG = One
    DBG8 = 0x17
    DBG8 = 0x18
    \_SB_.PCI0.HEC2.DSCI = One
    DBG8 = 0x19
    \_SB_.PCI0.HEC2.HIS = One
        DBG8 = 0x20
        \_SB_.PCI0.HEC2.HRS = One
        \_SB_.PCI0.HEC2.HIG = One
        DBG8 = 0x21
        DBG8 = 0x22
            DBG8 = 0x23
            \_SB_.PCI0.HEC2.HRS = Zero
            \_SB_.PCI0.HEC2.HRD = One
            \_SB_.PCI0.HEC2.HIG = One
    If ((\_SB_.PCI0.HEC2.MWP != \_SB_.PCI0.HEC2.MRP))
        DBG8 = 0x24
        Local1 = \_SB_.PCI0.HEC2.CBRW
        Local0 = \_SB_.PCI0.HEC2.CBRW
        \_SB_.PCI0.HEC2.HIG = One
        If (((Local0 & 0xFF) == Zero))
            PSTE = ((Local0 >> 0x10) & 0xFF)
            TSTE = ((Local0 >> 0x18) & 0xFF)
            PST0 = \_SB_.PCI0.HEC2.RPS0
            TST0 = \_SB_.PCI0.HEC2.RTS0
            PST1 = \_SB_.PCI0.HEC2.RPS1
            TST1 = \_SB_.PCI0.HEC2.RTS1
            PST2 = \_SB_.PCI0.HEC2.RPS2
            TST2 = \_SB_.PCI0.HEC2.RTS2
            PST3 = \_SB_.PCI0.HEC2.RPS3
            TST3 = \_SB_.PCI0.HEC2.RTS3
            PST4 = \_SB_.PCI0.HEC2.RPS4
            TST4 = \_SB_.PCI0.HEC2.RTS4
            PST5 = \_SB_.PCI0.HEC2.RPS5
            TST5 = \_SB_.PCI0.HEC2.RTS5
            PST6 = \_SB_.PCI0.HEC2.RPS6

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TST6 = \_SB.PCI0.HEC2.RTS6
PST7 = \_SB.PCI0.HEC2.RPS7
TST7 = \_SB.PCI0.HEC2.RTS7
PETE |= 0x80
\_PR.CPU0._PPC = \_SB.PCI0.HEC2.RPS0
PETE |= 0x40
\_PR.CPU0._TPC = \_SB.PCI0.HEC2.RTS0
\_PR.CPU1._TPC = \_SB.PCI0.HEC2.RTS0
\_PR.CPU2._TPC = \_SB.PCI0.HEC2.RTS0
\_PR.CPU3._TPC = \_SB.PCI0.HEC2.RTS0
\_PR.CPU4._TPC = \_SB.PCI0.HEC2.RTS0
\_PR.CPU5._TPC = \_SB.PCI0.HEC2.RTS0
\_PR.CPU6._TPC = \_SB.PCI0.HEC2.RTS0
\_PR.CPU7._TPC = \_SB.PCI0.HEC2.RTS0
DBG8 = 0x25
\_SB.PCI0.HEC2.CTS0 = TST0 /* \TST0 */
\_SB.PCI0.HEC2.CTS1 = TST1 /* \TST1 */
\_SB.PCI0.HEC2.CTS2 = TST2 /* \TST2 */
\_SB.PCI0.HEC2.CTS3 = TST3 /* \TST3 */
\_SB.PCI0.HEC2.CTS4 = TST4 /* \TST4 */
\_SB.PCI0.HEC2.CTS5 = TST5 /* \TST5 */
\_SB.PCI0.HEC2.CTS6 = TST6 /* \TST6 */
\_SB.PCI0.HEC2.CTS7 = TST7 /* \TST7 */
\_SB.PCI0.HEC2.CPS0 = PST0 /* \PST0 */
\_SB.PCI0.HEC2.CPS1 = PST1 /* \PST1 */
\_SB.PCI0.HEC2.CPS2 = PST2 /* \PST2 */
\_SB.PCI0.HEC2.CPS3 = PST3 /* \PST3 */
\_SB.PCI0.HEC2.CPS4 = PST4 /* \PST4 */
\_SB.PCI0.HEC2.CPS5 = PST5 /* \PST5 */
\_SB.PCI0.HEC2.CPS6 = PST6 /* \PST6 */
\_SB.PCI0.HEC2.CPS7 = PST7 /* \PST7 */
DBG8 = 0x26
\_SB.PCI0.HEC2.CBWW = Local1
\_SB.PCI0.HEC2.CBWW = (Local10 | (PETE & 0xFF))
\_SB.PCI0.HEC2.HIG = One
If ((Local10 & 0xFF) == 0x03)
    PURA = Local10
If ((VIDT == 0x8086))
If ((Arg0 == ToUUID ("3dddfaa6-361b-4eb4-a424-8d10089d1653") /* Physical Presence Interface */))
    _T_0 = ToInteger (Arg2)
    If ((_T_0 == Zero))
        If ((_T_0 == One))
            If ((_T_0 == 0x02))
                TMF1 = 0x12
                DAT = TMF1 /* \TMF1 */
                INQ = OFST /* \OFST */
                If ((DAT == 0xFF))
                    DAT = TMF2 /* \TMF2 */
                    INQ = OFST /* \OFST */
                If ((DAT == 0xFF))
                    If ((DAT == 0xF1))
                        If ((_T_0 == 0x03))
                            DAT = 0x11
                            INQ = OFST /* \OFST */
                            If ((DAT == 0xFF))
                                Index (PPI1, One) = DAT /* \_SB_.PCI0.LPCB.TPM_.DAT_ */
                            If ((_T_0 == 0x04))
                                If ((_T_0 == 0x05))
                                    DAT = 0x21
                                    INQ = OFST /* \OFST */
                                    Index (PPI2, One) = DAT /* \_SB_.PCI0.LPCB.TPM_.DAT_ */
                                    If ((DAT == 0xFF))
                                        Index (PPI2, One) = DAT /* \_SB_.PCI0.LPCB.TPM_.DAT_ */
                                    DAT = 0x31
                                    INQ = OFST /* \OFST */
                                    If ((DAT == 0xFF))
                                        If ((DAT == 0xFFF0))
                                            Index (PPI2, 0x02) = 0xFFFFFFFF0
                                        If ((DAT == 0xFFF1))
                                            Index (PPI2, 0x02) = 0xFFFFFFFF1
                                        Index (PPI2, 0x02) = DAT /* \_SB_.PCI0.LPCB.TPM_.DAT_ */
                                If ((_T_0 == 0x06))
                                    If ((_T_0 == 0x07))
                                        TMF1 = 0x12
                                        DAT = TMF1 /* \TMF1 */
                                        INQ = OFST /* \OFST */
                                        If ((DAT == 0xFF))
                                            DAT = TMF2 /* \TMF2 */
                                            INQ = OFST /* \OFST */
                                        If ((DAT == 0xFF))
                                            If ((DAT == 0xF1))
                                                If ((_T_0 == 0x08))
                                                    TMF1 = 0x43
                                                    DAT = TMF1 /* \TMF1 */
                                                    INQ = OFST /* \OFST */
                                                    DAT = TMF2 /* \TMF2 */
                                                    INQ = OFST /* \OFST */
                If ((Arg0 == ToUUID ("376054ed-cc13-4675-901c-4756d7f2d45d")))
                    _T_1 = ToInteger (Arg2)
                    If ((_T_1 == Zero))
                        If ((_T_1 == One))

```

```

        TMF1 = 0x22
        DAT = TMF1 /* \TMF1 */
        INQ = OFST /* \OFST */
        If ((DAT == 0xFF))
        DAT = TMF1 /* \TMF1 */
        INQ = OFST /* \OFST */
        If ((DAT == 0xFF))
If ((ACC0 != 0xFF))
    If ((VIDT == 0x8086))
If ((Arg0 == ToUUID ("3dddfaa6-361b-4eb4-a424-8d10089d1653")) /* Physical Presence Interface */)
    _T_0 = ToInteger (Arg2)
    If ((_T_0 == Zero))
        If ((_T_0 == One))
            If ((_T_0 == 0x02))
                TMF1 = 0x12
                DAT = TMF1 /* \TMF1 */
                INQ = OFST /* \OFST */
                If ((DAT == 0xFF))
                DAT = TMF2 /* \TMF2 */
                INQ = OFST /* \OFST */
                If ((DAT == 0xFF))
                If ((_T_0 == 0x03))
                    DAT = 0x11
                    INQ = OFST /* \OFST */
                    If ((DAT == 0xFF))
                    Index (PPI1, One) = DAT /* \_SB_.PCI0.ITPM.DAT_ */
                    If ((_T_0 == 0x04))
                        If ((_T_0 == 0x05))
                            DAT = 0x21
                            INQ = OFST /* \OFST */
                            Index (PPI2, One) = DAT /* \_SB_.PCI0.ITPM.DAT_ */
                            If ((DAT == 0xFF))
                            Index (PPI2, One) = DAT /* \_SB_.PCI0.ITPM.DAT_ */
                            DAT = 0x31
                            INQ = OFST /* \OFST */
                            If ((DAT == 0xFF))
                            If ((DAT == 0xFFFF))
                                Index (PPI2, 0x02) = 0xFFFFFFFF0
                                If ((DAT == 0xFFFF1))
                                    Index (PPI2, 0x02) = 0xFFFFFFFF1
                                    Index (PPI2, 0x02) = DAT /* \_SB_.PCI0.ITPM.DAT_ */
                                If ((_T_0 == 0x06))
                If ((Arg0 == ToUUID ("376054ed-cc13-4675-901c-4756d7f2d45d")))
                    _T_1 = ToInteger (Arg2)
                    If ((_T_1 == Zero))
                        If ((_T_1 == One))
                            TMF1 = 0x22
                            DAT = TMF1 /* \TMF1 */
                            INQ = OFST /* \OFST */
                            If ((DAT == 0xFF))
                            DAT = TMF1 /* \TMF1 */
                            INQ = OFST /* \OFST */
                            If ((DAT == 0xFF))
SMIC = BSMI /* \BSMI */
SMIC = (Arg0 + BSMI) /* \BSMI */
SMIC = BSMI /* \BSMI */
MMBS = PMMB /* \_SB_.PCI0.RP05.PPF1.PMMB */
MMLN = PMMS /* \_SB_.PCI0.RP05.PPF1.PMMS */
MSBS = PMSB /* \_SB_.PCI0.RP05.PPF1.PMSB */
MSLN = PMSS /* \_SB_.PCI0.RP05.PPF1.PMSS */
If ((COSTB == 0xFFFFFFFF))
    OSTB = OW12 /* \_SB_.OW12 */
    OSTB = OW7 /* \_SB_.OW7 */
    OSTB = OWLH /* \_SB_.OWLH */
    OSTB = OWXP /* \_SB_.OWXP */
    OSTB = OUNK /* \_SB_.OUNK */
    OSTB = OW98 /* \_SB_.OW98 */
    OSTB = OWME /* \_SB_.OWME */
    OSTB = OW2K /* \_SB_.OW2K */
    OSTB = OUNK /* \_SB_.OUNK */
    OSTB = OUNK /* \_SB_.OUNK */
Local0 = SizeOf (Arg0)
Local1 = SizeOf (Arg1)
If ((Local0 != Local1))
    BUF0 = Arg0
    BUF1 = Arg1
    Local2 = Zero
    Local3 = Derefof (Index (BUF0, Local2))
    Local4 = Derefof (Index (BUF1, Local2))
    If ((Local3 != Local4))
    If ((OSTB >= OW7))
    Index (RPMC, Zero) = 0x05
    Index (RPMC, One) = Zero
    Index (RPMC, 0x02) = Zero
    Index (RPMC, 0x03) = 0x00015F90
    Index (RPMC, 0x04) = 0x03E8
    Index (RPMC, 0x05) = MINA /* \_SB_.PMI0.MINA */
    Index (RPMC, 0x06) = MAXA /* \_SB_.PMI0.MAXA */
    Index (RPMC, 0x07) = 0xFFFFFFFF
    Index (RPMC, 0x08) = 0xFFFFFFFF
    Index (RPMC, 0x09) = MINL /* \_SB_.PMI0.MINL */

```

```

Index (RPMC, 0x0A) = MAXL /* \_SB_.PMI0.MAXL */
Index (RPMC, 0x0B) = "Model"
Index (RPMC, 0x0C) = "Serial"
Index (RPMC, 0x0D) = "OEMInfo"
LENG = 0x04
REQ1 = 0xDC
REQ2 = One
REQ3 = Zero
REQ4 = Zero
BUFF = GPWR = BUFF /* \_SB_.PMI0._PMM.BUFF */
CURP = CURT /* \_SB_.PMI0._PMM.CURT */
AVGP = AVGT /* \_SB_.PMI0._PMM.AVGT */
If (((STAT == Zero) && (CMPC == Zero)))
    PAIV = MINA /* \_SB_.PMI0.MINA */
    LENG = 0x03
    REQ1 = 0xDC
    REQ2 = Zero
    REQ3 = Zero
    BUFF = GPWL = BUFF /* \_SB_.PMI0._PMM.BUFF */
    If ((CMPC == 0x80))
        PAIV = MINA /* \_SB_.PMI0.MINA */
        If (((STAT == Zero) && (CMPC == Zero)))
            LENG = 0x03
            REQ1 = 0xDC
            REQ2 = Zero
            REQ3 = Zero
            BUFF = GPWL = BUFF /* \_SB_.PMI0._GAI.BUFF */
            If ((CMPC == 0x80))
                PAIV = MINA /* \_SB_.PMI0.MINA */
            If (((STAT == Zero) && (CMPC == Zero)))
                PAIV = (0x03E8 * STSP)
            LENG = 0x03
            REQ1 = 0xDC
            REQ2 = Zero
            REQ3 = Zero
            BUFF = GPWL = BUFF /* \_SB_.PMI0._GHL.BUFF */
            If ((CMPC == 0x80))
                HWLV = MAXL /* \_SB_.PMI0.MAXL */
            If (((STAT == Zero) && (CMPC == Zero)))
                HWLV = (0x03E8 * PWRL)
            LENG = 0x0F
            REQ1 = 0xDC
            REQ2 = Zero
            REQ3 = Zero
            REQ4 = Zero
            REQ5 = Zero
            RQPL = (HWLV / 0x03E8)
            RQCT = 0x03E8
            RQRV = Zero
            RQSP = (Arg0 / 0x03E8)
            BUFF = SPWL = BUFF /* \_SB_.PMI0._PAI.BUFF */
            If (((STAT == Zero) && (CMPC == Zero)))
                LENG = 0x04
                REQ1 = 0xDC
                REQ2 = One
                REQ3 = Zero
                REQ4 = Zero
                BUFF = ADPL = BUFF /* \_SB_.PMI0._PAI.BUFF */
                If (((STAT == Zero) && (CMPC == Zero)))
                    PAIV = Arg0
            LENG = 0x0F
            REQ1 = 0xDC
            REQ2 = Zero
            REQ3 = Zero
            REQ4 = Zero
            REQ5 = Zero
            RQPL = (Arg0 / 0x03E8)
            RQCT = 0x03E8
            RQRV = Zero
            RQSP = (PAIV / 0x03E8)
            BUFF = SPWL = BUFF /* \_SB_.PMI0._SHL.BUFF */
            If (((STAT == Zero) && (CMPC == Zero)))
                LENG = 0x04
                REQ1 = 0xDC
                REQ2 = One
                REQ3 = Zero
                REQ4 = Zero
                BUFF = ADPL = BUFF /* \_SB_.PMI0._SHL.BUFF */
                If (((STAT == Zero) && (CMPC == Zero)))
                    HWLV = Arg0
                If ((OSTB >= OWLH))
                    MCMN = 0x0CA4
                    MCMX = 0x0CA4
                    MCLN = 0x02
                    MCIT = Zero
                    If ((0x0B != ^SI01.GBIN ()))
                        MCIT = Zero
                Local0 = INTR /* \_SB_.PCI0.LPCB.SI01.INTR */
            If ((Arg0 == ToUUID ("ed855e0c-6c90-47bf-a62a-26de0fc5ad5c")))
                SUPP = CDW2 /* \WOSC.CDW2 */
                CTRL = CDW3 /* \WOSC.CDW3 */

```

```

If ((\SB.OSTB >= \SB.OWLH))
  If ((OSTB >= OWLH))
If ((Arg0 == ToUUID ("e5c937d0-3553-4d7a-9117-ea4d19c3434d") /* Device Labeling Interface */)
  If ((Arg1 == 0x02) && (Arg2 == 0x07)))
If ((Arg0 == ToUUID ("e5c937d0-3553-4d7a-9117-ea4d19c3434d") /* Device Labeling Interface */)
  If ((Arg1 == 0x02) && (Arg2 == 0x07)))
If ((Arg0 == ToUUID ("e5c937d0-3553-4d7a-9117-ea4d19c3434d") /* Device Labeling Interface */)
  If ((Arg1 == 0x02) && (Arg2 == 0x07)))
If ((Arg0 == ToUUID ("e5c937d0-3553-4d7a-9117-ea4d19c3434d") /* Device Labeling Interface */)
  If ((Arg1 == 0x02) && (Arg2 == 0x07)))
If ((Arg0 == ToUUID ("e5c937d0-3553-4d7a-9117-ea4d19c3434d") /* Device Labeling Interface */)
  If ((SYST == One))
  If ((Arg1 == 0x02) && (Arg2 == 0x07)))
If ((Arg0 == ToUUID ("e5c937d0-3553-4d7a-9117-ea4d19c3434d") /* Device Labeling Interface */)
  If ((SYST == One))
  If ((Arg1 == 0x02) && (Arg2 == 0x07)))
  ^^GFX0.CLID = 0x03
  Arg1 = Arg0
  CMDR = Arg0
\_PR.CPU0._PPC = \_PR.CPPC
  Index (DerefOf (Index (HPSD, Zero)), 0x04) = TCNT /* \TCNT */
  Index (DerefOf (Index (SPSD, Zero)), 0x04) = TCNT /* \TCNT */
  Index (DerefOf (Index (SNPD, Zero)), 0x04) = TCNT /* \TCNT */
  PSDF = Ones
If (((PDC0 & 0x0820) == 0x0820) && (PSCT == Zero)))
If (((PDC0 & 0x0820) == 0x20) && (PSCT == 0x02)))
  \_PR.CPU0._PPC = CPPC /* \_PR_.CPPC */
Local0 = CPDC (Arg0)
Local0 = COSC (Arg0, Arg1, Arg2, Arg3)
Local0 = SizeOf (Arg0)
Local1 = (Local0 - 0x08)
If (!(((IID0 == EID0) && (IID1 == EID1)) && ((
  IID2 == EID2) && (IID3 == EID3))))
  STS0 = 0x06
If ((Arg1 != One))
  STS0 = 0x0A
If (((STS0 == 0x06) || (STS0 == 0x0A)))
  CAP0 &= 0x0BFF
PDC0 = ((PDC0 & 0x7FFFFFFF) | CAP0) /* \_PR_.CPU0.GCAP.CAP0 */
  SDTL |= 0x02
Local0 = \_PR.CPU0.CPDC (Arg0)
Local0 = \_PR.CPU0.COSC (Arg0, Arg1, Arg2, Arg3)
If (((STS1 == 0x06) || (STS1 == 0x0A)))
  CAP1 &= 0x0BFF
PDC1 = ((PDC1 & 0x7FFFFFFF) | CAP1) /* \_PR_.CPU1.GCAP.CAP1 */
If (((PDC1 & 0x09) == 0x09))
PDC0 = PDC1 /* \PDC1 */
  SDTL |= 0x20
  SDTL |= 0x10
Local0 = \_PR.CPU0.CPDC (Arg0)
Local0 = \_PR.CPU0.COSC (Arg0, Arg1, Arg2, Arg3)
If (((STS2 == 0x06) || (STS2 == 0x0A)))
  CAP2 &= 0x0BFF
PDC2 = ((PDC2 & 0x7FFFFFFF) | CAP2) /* \_PR_.CPU2.GCAP.CAP2 */
If (((PDC2 & 0x09) == 0x09))
PDC0 = PDC2 /* \PDC2 */
Local0 = \_PR.CPU0.CPDC (Arg0)
Local0 = \_PR.CPU0.COSC (Arg0, Arg1, Arg2, Arg3)
If (((STS3 == 0x06) || (STS3 == 0x0A)))
  CAP3 &= 0x0BFF
PDC3 = ((PDC3 & 0x7FFFFFFF) | CAP3) /* \_PR_.CPU3.GCAP.CAP3 */
If (((PDC3 & 0x09) == 0x09))
PDC0 = PDC3 /* \PDC3 */
Local0 = \_PR.CPU0.CPDC (Arg0)
Local0 = \_PR.CPU0.COSC (Arg0, Arg1, Arg2, Arg3)
If (((STS4 == 0x06) || (STS4 == 0x0A)))
  CAP4 &= 0x0BFF
PDC4 = ((PDC4 & 0x7FFFFFFF) | CAP4) /* \_PR_.CPU4.GCAP.CAP4 */
If (((PDC4 & 0x09) == 0x09))
PDC0 = PDC4 /* \PDC4 */
Local0 = \_PR.CPU0.CPDC (Arg0)
Local0 = \_PR.CPU0.COSC (Arg0, Arg1, Arg2, Arg3)
If (((STS5 == 0x06) || (STS5 == 0x0A)))
  CAP5 &= 0x0BFF
PDC5 = ((PDC5 & 0x7FFFFFFF) | CAP5) /* \_PR_.CPU5.GCAP.CAP5 */
If (((PDC5 & 0x09) == 0x09))
PDC0 = PDC5 /* \PDC5 */
Local0 = \_PR.CPU0.CPDC (Arg0)
Local0 = \_PR.CPU0.COSC (Arg0, Arg1, Arg2, Arg3)
If (((STS6 == 0x06) || (STS6 == 0x0A)))
  CAP6 &= 0x0BFF
PDC6 = ((PDC6 & 0x7FFFFFFF) | CAP6) /* \_PR_.CPU6.GCAP.CAP6 */
If (((PDC6 & 0x09) == 0x09))
PDC0 = PDC6 /* \PDC6 */
Local0 = \_PR.CPU0.CPDC (Arg0)
Local0 = \_PR.CPU0.COSC (Arg0, Arg1, Arg2, Arg3)
If (((STS7 == 0x06) || (STS7 == 0x0A)))
  CAP7 &= 0x0BFF
PDC7 = ((PDC7 & 0x7FFFFFFF) | CAP7) /* \_PR_.CPU7.GCAP.CAP7 */
If (((PDC7 & 0x09) == 0x09))
PDC0 = PDC7 /* \PDC7 */

```

```

LFMI = SizeOf (_PSS)
LFMP = DerefOf (Index (DerefOf (Index (_PSS, LFMI)), One))
Local0 = Zero
    Local1 = RefOf (TSMF)
    Local2 = SizeOf (TSMF)
    Local1 = RefOf (TSMC)
    Local2 = SizeOf (TSMC)
    Local4 = ((LFMP * (Local2 - Local0)) / Local2)
    Index (DerefOf (Index (DerefOf (Local1), Local0)), One) = Local4
TSSF = Ones
Local0 = (CMDC * 0x38)
Local0 = (CMDC * 0x07)
CMDX = Arg0
A001 = Arg1
If ((Arg0 == 0x02))
    REGF = Arg1
    PIO0 = 0x78
    DMA0 = 0x14
    PIO1 = 0x78
    DMA1 = 0x14
    CHNF = 0x05
    CMDC = Zero
    CMDC = Zero
    PIO0 = 0x78
    DMA0 = 0x14
    PIO1 = 0x78
    DMA1 = 0x14
    CHNF = 0x05
    CMDC = Zero
    CMDC = Zero
If ((Arg0 == 0x02))
    REGF = Arg1
    PIO0 = 0x78
    DMA0 = 0x14
    PIO1 = 0x78
    DMA1 = 0x14
    CHNF = One
    CMDC = Zero
    PIO0 = 0x78
    DMA0 = 0x14
    PIO1 = 0x78
    DMA1 = 0x14
    CHNF = One
    CMDC = Zero
    GENG = One
    PMEG = One
    GENG = Zero
    PMEG = Zero
PSTS = One
LTRS = LTRA /* \LTRA */
OBFS = OBFA /* \OBFA */
    _T_0 = ToInteger (Arg0)
    If ((_T_0 == ToUUID ("e5c937d0-3553-4d7a-9117-ea4d19c3434d")) /* Device Labeling Interface */)
        _T_1 = ToInteger (Arg2)
        If ((_T_1 == Zero))
            If ((Arg1 == 0x02))
                OPTS = One
                OPTS |= 0x40
                OPTS |= 0x10
            If ((_T_1 == 0x04))
                If ((Arg1 == 0x02))
                    If ((_T_1 == 0x06))
                        If ((Arg1 == 0x02))
                            Index (LTRV, Zero) = ((SMSL >> 0x0A) & 0x07)
                            Index (LTRV, One) = (SMSL & 0x03FF)
                            Index (LTRV, 0x02) = ((SNSL >> 0x0A) & 0x07)
                            Index (LTRV, 0x03) = (SNSL & 0x03FF)
                _STA = One
                _STA = Zero
                GENG = One
                PMEG = One
                GENG = Zero
                PMEG = Zero
PSTS = One
LTRS = LTRB /* \LTRB */
OBFS = OBFB /* \OBFB */
    _T_0 = ToInteger (Arg0)
    If ((_T_0 == ToUUID ("e5c937d0-3553-4d7a-9117-ea4d19c3434d")) /* Device Labeling Interface */)
        _T_1 = ToInteger (Arg2)
        If ((_T_1 == Zero))
            If ((Arg1 == 0x02))
                OPTS = One
                OPTS |= 0x40
                OPTS |= 0x10
            If ((_T_1 == 0x04))
                If ((Arg1 == 0x02))
                    If ((_T_1 == 0x06))
                        If ((Arg1 == 0x02))
                            Index (LTRV, Zero) = ((SMSL >> 0x0A) & 0x07)
                            Index (LTRV, One) = (SMSL & 0x03FF)
                            Index (LTRV, 0x02) = ((SNSL >> 0x0A) & 0x07)

```



```

Index (LTRV, 0x03) = (SNSL & 0x03FF)

_STA = One
_STA = Zero
GENG = One
PMEG = One
GENG = Zero
PMEG = Zero
PSTS = One
LTRS = LTRC /* \LTRC */
OBFS = OBFC /* \OBFC */
_T_0 = ToInteger (Arg0)
If ((_T_0 == ToUUID ("e5c937d0-3553-4d7a-9117-ea4d19c3434d") /* Device Labeling Interface */)
    _T_1 = ToInteger (Arg2)
    If ((_T_1 == Zero))
        If ((Arg1 == 0x02))
            OPTS = One
            OPTS |= 0x40
            OPTS |= 0x10
        If ((_T_1 == 0x04))
            If ((Arg1 == 0x02))
                If ((_T_1 == 0x06))
                    If ((Arg1 == 0x02))
                        Index (LTRV, Zero) = ((SMSL >> 0x0A) & 0x07)
                        Index (LTRV, One) = (SMSL & 0x03FF)
                        Index (LTRV, 0x02) = ((SNSL >> 0x0A) & 0x07)
                        Index (LTRV, 0x03) = (SNSL & 0x03FF)

_STA = One
_STA = Zero
If ((AUVD != 0xFFFF))
If (((ABAR & 0xFFFFC004) != 0xFFFFC004) && ((
ABAR & 0xFFFFC000) != Zero)))
    BARA = ABAR /* \_SB_.PCI0.B0D3.ABAR */
    If ((ABAH != Zero))
        BARA |= (ABAH << 0x20)
If (((ABAR & 0xFFFFC004) != 0xFFFFC004) && ((
ABAR & 0xFFFFC000) != Zero)))
    BBAR = Zero
    BBAR = (ABAR & 0xFFFFFFFF)
    If ((ABAH != Zero))
        BBAR |= (ABAH << 0x20)
    BBAR += 0x1000
    EMWA = AUDA /* \AUDA */
    ADWA = AADB /* \AADB */
    EM4W = AUDC /* \AUDC */
ADDR = Arg0
If (((ABAR & 0xFFFFC004) != 0xFFFFC004) && ((
ABAR & 0xFFFFC000) != Zero)))
    If ((CDEC != Zero))
        BBAR = Zero
        BBAR = (ABAR & 0xFFFFFFFF)
        If ((ABAH != Zero))
            BBAR |= (ABAH << 0x20)
        CONT = 0x03E8
        While (((AIRS & One) == One) && (CONT != Zero))
            AIRS |= 0x02
            AVIC = CDEC /* \_SB_.PCI0.B0D3.VSTR.CDEC */
            AIRS |= One
            CONT = 0x03E8
            While (((AIRS & One) == One) && (CONT != Zero))
If (((CADR != Zero) && (CCNT != Zero)))
    IDDX = CADR /* \CADR */
    IDDX += 0x04
If (((ABAR & 0xFFFFC004) != 0xFFFFC004) && ((
ABAR & 0xFFFFC000) != Zero)))
    BBAR = (ABAR & 0xFFFFFFFF)
    CORB &= 0xFFFFFFFF
    RIRB &= 0xFFFFFFFF
    OSD1 &= 0xFFFFFFFF
    OSD2 &= 0xFFFFFFFF
    CRST &= 0xFFFFFFFF
If (((ABAR & 0xFFFFC004) != 0xFFFFC004) && ((
ABAR & 0xFFFFC000) != Zero)))
    BBAR = Zero
    BBAR = (ABAR & 0xFFFFFFFF)
    If ((ABAH != Zero))
        BBAR |= (ABAH << 0x20)
    GCTL |= One
    CONT = 0x03E8
    While (((GCTL & One) == Zero) && (CONT != Zero))
        GCAP &= 0xFFFF
        SSTS |= 0x0F
        GCTL &= 0xFFFFFFFF
        CONT = 0x03E8
        While (((GCTL & One) == One) && (CONT != Zero))
            GCTL |= One
            CONT = 0x03E8
        While (((GCTL & One) == Zero) && (CONT != Zero))
    If (((ABAR & 0xFFFFC004) == 0xFFFFC004) || ((ABAR & 0xFFFFC000
) == Zero)))
        If ((BARA != 0x80000000))
            TBAR = ABAR /* \_SB_.PCI0.B0D3.ABAR */

```

```

    TBAR |= (ABAH << 0x20)
    TCMD = ACMD /* \_SB\_PCI0.B0D3.ACMD */
    ABAH = (BARA >> 0x20)
    ABAR = BARA /* \_SB\_PCI0.B0D3.BARA */
    ACMD = 0x06
    MODB = One
If ((ACMD & 0x06) != 0x06))
    TCMD = ACMD /* \_SB\_PCI0.B0D3.ACMD */
    ACMD = 0x06
    MODC = One
If ((ABAR == BARA))
    ABAR = TBAR /* \_SB\_PCI0.B0D3.TBAR */
    ABAH = (TBAR >> 0x20)
    ACMD = TCMD /* \_SB\_PCI0.B0D3.TCMD */
    ACMD = TCMD /* \_SB\_PCI0.B0D3.TCMD */
If (((ABAR & 0xFFFFC004) != 0xFFFFC004) && ((
ABAR & 0xFFFFC000) != Zero)))
    BBAR = (ABAR & 0xFFFFF0)
If ((Arg0 == Zero))
    AEM4 &= 0xFFFFC000
    AEM4 |= 0x04
    AEM5 &= 0xFFFFC000
    AEM5 |= 0x4B
If ((Arg0 == One))
    AEM4 &= 0xFFFFC000
    AEM4 |= 0x04
    AEM5 &= 0xFFFFC000
    AEM5 |= 0x5A
If ((Arg0 == 0x02))
    AEM4 &= 0xFFFFC000
    AEM4 |= 0x10
    AEM5 &= 0xFFFFC000
    AEM5 |= 0xE1
If ((Arg0 == 0x03))
    AEM4 &= 0xFFFFC000
    AEM4 |= 0x08
    AEM5 &= 0xFFFFC000
    AEM5 |= 0xE1
DSEN = (Arg0 & 0x07)
If (((Arg0 & 0x03) == Zero))
    NDID = Zero
If ((DDL1 != Zero))
    DID1 = SDDL (DDL1)
If ((DDL2 != Zero))
    DID2 = SDDL (DDL2)
If ((DDL3 != Zero))
    DID3 = SDDL (DDL3)
If ((DDL4 != Zero))
    DID4 = SDDL (DDL4)
If ((DDL5 != Zero))
    DID5 = SDDL (DDL5)
If ((DDL6 != Zero))
    DID6 = SDDL (DDL6)
If ((DDL7 != Zero))
    DID7 = SDDL (DDL7)
If ((DDL8 != Zero))
    DID8 = SDDL (DDL8)
If ((DDL9 != Zero))
    DID9 = SDDL (DDL9)
If ((DDL10 != Zero))
    DIDA = SDDL (DDL10)
If ((DDL11 != Zero))
    DIDB = SDDL (DDL11)
If ((DDL12 != Zero))
    DIDC = SDDL (DDL12)
If ((DDL13 != Zero))
    DIDD = SDDL (DDL13)
If ((DDL14 != Zero))
    DIDE = SDDL (DDL14)
If ((DDL15 != Zero))
    DIDF = SDDL (DDL15)
If ((NDID == One))
    Index (TMP1, Zero) = (0x00010000 | DID1)
If ((NDID == 0x02))
    Index (TMP2, Zero) = (0x00010000 | DID1)
    Index (TMP2, One) = (0x00010000 | DID2)
If ((NDID == 0x03))
    Index (TMP3, Zero) = (0x00010000 | DID1)
    Index (TMP3, One) = (0x00010000 | DID2)
    Index (TMP3, 0x02) = (0x00010000 | DID3)
If ((NDID == 0x04))
    Index (TMP4, Zero) = (0x00010000 | DID1)
    Index (TMP4, One) = (0x00010000 | DID2)
    Index (TMP4, 0x02) = (0x00010000 | DID3)
    Index (TMP4, 0x03) = (0x00010000 | DID4)
If ((NDID == 0x05))
    Index (TMP5, Zero) = (0x00010000 | DID1)
    Index (TMP5, One) = (0x00010000 | DID2)
    Index (TMP5, 0x02) = (0x00010000 | DID3)
    Index (TMP5, 0x03) = (0x00010000 | DID4)
    Index (TMP5, 0x04) = (0x00010000 | DID5)

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Index (TMPE, 0x06) = (0x00010000 | DID7)
Index (TMPE, 0x07) = (0x00010000 | DID8)
Index (TMPE, 0x08) = (0x00010000 | DID9)
Index (TMPE, 0x09) = (0x00010000 | DIDA)
Index (TMPE, 0x0A) = (0x00010000 | DIDB)
Index (TMPE, 0x0B) = (0x00010000 | DIDC)
Index (TMPE, 0x0C) = (0x00010000 | DIDD)
Index (TMPE, 0x0D) = (0x00010000 | DIDE)
If ((NDID == 0x0F))
  Index (TMPF, Zero) = (0x00010000 | DID1)
  Index (TMPF, One) = (0x00010000 | DID2)
  Index (TMPF, 0x02) = (0x00010000 | DID3)
  Index (TMPF, 0x03) = (0x00010000 | DID4)
  Index (TMPF, 0x04) = (0x00010000 | DID5)
  Index (TMPF, 0x05) = (0x00010000 | DID6)
  Index (TMPF, 0x06) = (0x00010000 | DID7)
  Index (TMPF, 0x07) = (0x00010000 | DID8)
  Index (TMPF, 0x08) = (0x00010000 | DID9)
  Index (TMPF, 0x09) = (0x00010000 | DIDA)
  Index (TMPF, 0x0A) = (0x00010000 | DIDB)
  Index (TMPF, 0x0B) = (0x00010000 | DIDC)
  Index (TMPF, 0x0C) = (0x00010000 | DIDD)
  Index (TMPF, 0x0D) = (0x00010000 | DIDE)
  Index (TMPF, 0x0E) = (0x00010000 | DIDE)
If (((0x0F00 & DID1) == 0x0400))
  EDPV = One
  NDX = NXD1 /* \NXD1 */
  DIDX = DID1 /* \DID1 */
If ((DID1 == Zero))
If (((SGMD & 0x7F) == One) && CondRef0f (SNXD)))
If (((0x0F00 & DID2) == 0x0400))
  EDPV = 0x02
  NDX = NXD2 /* \NXD2 */
  DIDX = DID2 /* \DID2 */
If ((DID2 == Zero))
If ((LIDS == Zero))
If (((SGMD & 0x7F) == One) && CondRef0f (SNXD)))
If (((0x0F00 & DID3) == 0x0400))
  EDPV = 0x03
  NDX = NXD3 /* \NXD3 */
  DIDX = DID3 /* \DID3 */
If ((DID3 == Zero))
If ((DID3 == Zero))
If (((SGMD & 0x7F) == One) && CondRef0f (SNXD)))
If (((0x0F00 & DID4) == 0x0400))
  EDPV = 0x04
  NDX = NXD4 /* \NXD4 */
  DIDX = DID4 /* \DID4 */
If ((DID4 == Zero))
If ((DID4 == Zero))
If (((SGMD & 0x7F) == One) && CondRef0f (SNXD)))
If (((0x0F00 & DID5) == 0x0400))
  EDPV = 0x05
  NDX = NXD5 /* \NXD5 */
  DIDX = DID5 /* \DID5 */
If ((DID5 == Zero))
If ((DID5 == Zero))
If (((SGMD & 0x7F) == One) && CondRef0f (SNXD)))
If (((0x0F00 & DID6) == 0x0400))
  EDPV = 0x06
  NDX = NXD6 /* \NXD6 */
  DIDX = DID6 /* \DID6 */
If ((DID6 == Zero))
If ((DID6 == Zero))
If (((SGMD & 0x7F) == One) && CondRef0f (SNXD)))
If (((0x0F00 & DID7) == 0x0400))
  EDPV = 0x07
  NDX = NXD7 /* \NXD7 */
  DIDX = DID7 /* \DID7 */
If ((DID7 == Zero))
If ((DID7 == Zero))
If (((SGMD & 0x7F) == One) && CondRef0f (SNXD)))
If (((0x0F00 & DID8) == 0x0400))
  EDPV = 0x08
  NDX = NXD8 /* \NXD8 */
  DIDX = DID8 /* \DID8 */
If ((DID8 == Zero))
If ((DID8 == Zero))
If (((SGMD & 0x7F) == One) && CondRef0f (SNXD)))
If (((0x0F00 & DID9) == 0x0400))
  EDPV = 0x09
  NDX = NXD8 /* \NXD8 */
  DIDX = DID9 /* \DID9 */
If ((DID9 == Zero))
If ((DID9 == Zero))
If (((SGMD & 0x7F) == One) && CondRef0f (SNXD)))
If (((0x0F00 & DIDA) == 0x0400))
  EDPV = 0x0A
  NDX = NXD8 /* \NXD8 */
  DIDX = DIDA /* \DIDA */
If ((DIDA == Zero))

```

```

If ((DIDA == Zero))
If (((SGMD & 0x7F) == One) && CondRef0f (SNXD)))
If (((0x0F00 & DIDB) == 0x0400))
    EDPV = 0x0B
    NXDX = NXD8 /* \NXD8 */
    DIDX = DIDB /* \DIDB */
If ((DIDB == Zero))
If ((DIDB == Zero))
If (((SGMD & 0x7F) == One) && CondRef0f (SNXD)))
If (((0x0F00 & DIDC) == 0x0400))
    EDPV = 0x0C
    NXDX = NXD8 /* \NXD8 */
    DIDX = DIDC /* \DIDC */
If ((DIDC == Zero))
If ((DIDC == Zero))
If (((SGMD & 0x7F) == One) && CondRef0f (SNXD)))
If (((0x0F00 & DIDD) == 0x0400))
    EDPV = 0x0D
    NXDX = NXD8 /* \NXD8 */
    DIDX = DIDD /* \DIDD */
If ((DIDD == Zero))
If ((DIDD == Zero))
If (((SGMD & 0x7F) == One) && CondRef0f (SNXD)))
If (((0x0F00 & DIDE) == 0x0400))
    EDPV = 0x0E
    NXDX = NXD8 /* \NXD8 */
    DIDX = DIDE /* \DIDE */
If ((DIDE == Zero))
If ((DIDE == Zero))
If (((SGMD & 0x7F) == One) && CondRef0f (SNXD)))
If (((0x0F00 & DIDF) == 0x0400))
    EDPV = 0x0F
    NXDX = NXD8 /* \NXD8 */
    DIDX = DIDF /* \DIDF */
If ((DIDF == Zero))
If ((DIDC == Zero))
If (((SGMD & 0x7F) == One) && CondRef0f (SNXD)))
If ((EDPV == Zero))
If ((EDPV == Zero))
If (((SGMD & 0x7F) == One) && CondRef0f (SNXD)))
If ((Arg0 >= Zero) && (Arg0 <= 0x64))
    BRTL = Arg0
Local0 = (Arg0 & 0x0F0F)
Local1 = (0x80000000 | Local0)
If ((DIDL == Local0))
If ((DDL2 == Local0))
If ((DDL3 == Local0))
If ((DDL4 == Local0))
If ((DDL5 == Local0))
If ((DDL6 == Local0))
If ((DDL7 == Local0))
If ((DDL8 == Local0))
If ((DDL9 == Local0))
If ((DDL10 == Local0))
If ((DDL11 == Local0))
If ((DDL12 == Local0))
If ((DDL13 == Local0))
If ((DDL14 == Local0))
If ((DDL15 == Local0))
Local0 = (Arg0 & 0x0F0F)
If ((Zero == Local0))
If ((CADL == Local0))
If ((CAL2 == Local0))
If ((CAL3 == Local0))
If ((CAL4 == Local0))
If ((CAL5 == Local0))
If ((CAL6 == Local0))
If ((CAL7 == Local0))
If ((CAL8 == Local0))
Local0 = (Arg0 & 0x0F0F)
If ((Zero == Local0))
If ((NADL == Local0))
If ((NDL2 == Local0))
If ((NDL3 == Local0))
If ((NDL4 == Local0))
If ((NDL5 == Local0))
If ((NDL6 == Local0))
If ((NDL7 == Local0))
If ((NDL8 == Local0))
If (((Arg0 & 0xC0000000) == 0xC0000000))
    CSTE = NSTE /* \NSTE */
    If ((GESF == Zero))
        PARM = 0x0659
        GESF = Zero
    If ((GESF == One))
        PARM = 0x00700482
        If ((S0ID == One))
            PARM |= 0x0100
        GESF = Zero
    If ((GESF == 0x04))
        PARM &= 0xEFFF0000

```

```

    PARM &= (DerefOf (Index (DBTB, IBTT)) << 0x10)
    PARM |= IBTT /* \_SB_.PCI0.GFX0.PARM */
    GESF = Zero
If ((GESF == 0x05))
    PARM = IPSC /* \IPSC */
    PARM |= (IPAT << 0x08)
    PARM += 0x0100
    PARM |= (LIDS << 0x10)
    PARM += 0x00010000
    PARM |= (IBIA << 0x14)
    GESF = Zero
If ((GESF == 0x07))
    PARM = GIVD /* \_SB_.PCI0.GFX0.GIVD */
    PARM ^= One
    PARM |= (GMFN << One)
    PARM |= 0x1800
    PARM |= (IDMS << 0x11)
    PARM |= (DerefOf (Index (DerefOf (Index (CDCT, HVC0)), CDVL)) <<
    GESF = One
If ((GESF == 0x0A))
    PARM = Zero
    PARM |= 0x03
    GESF = Zero
If ((GESF == 0x0B))
    PARM = KSV0 /* \KSV0 */
    GESF = KSV1 /* \KSV1 */
GESF = Zero
If ((GESF == Zero))
    PARM = Zero
    PARM = 0x000F87DD
    GESF = Zero
If ((GESF == One))
    GESF = Zero
    PARM = Zero
If ((GESF == 0x03))
    GESF = Zero
    PARM = Zero
If ((GESF == 0x04))
    GESF = Zero
    PARM = Zero
If ((GESF == 0x05))
    GESF = Zero
    PARM = Zero
If ((GESF == 0x07))
    If ((S0ID == One))
        If (((PARM & 0xFF) == One))
    If ((PARM == Zero))
        Local0 = CLID /* \_SB_.PCI0.GFX0.CLID */
        CLID &= 0x0F
    GESF = Zero
    PARM = Zero
If ((GESF == 0x08))
    If ((S0ID == One))
        Local0 = ((PARM >> 0x08) & 0xFF)
        If ((Local0 == Zero))
    GESF = Zero
    PARM = Zero
If ((GESF == 0x09))
    IBTT = (PARM & 0xFF)
    GESF = Zero
    PARM = Zero
If ((GESF == 0x0A))
    IPSC = (PARM & 0xFF)
    IPAT = ((PARM >> 0x08) & 0xFF)
    IBIA = ((PARM >> 0x14) & 0x07)
    GESF = Zero
    PARM = Zero
If ((GESF == 0x0B))
    IF1E = ((PARM >> One) & One)
    IDMS = ((PARM >> 0x0D) & 0x0F)
    IDMS = ((PARM >> 0x11) & 0x0F)
    GESF = Zero
    PARM = Zero
If ((GESF == 0x10))
    GESF = Zero
    PARM = Zero
If ((GESF == 0x11))
    PARM = (LIDS << 0x08)
    PARM += 0x0100
    GESF = Zero
If ((GESF == 0x12))
    If (((PARM >> One) == One))
        ISSC = One
        GESF = Zero
        ISSC = Zero
    GESF = Zero
    PARM = Zero
If ((GESF == 0x13))
    GESF = Zero
    PARM = Zero
If ((GESF == 0x14))

```

```

    PAVP = (PARM & 0x0F)
    GESF = Zero
    PARM = Zero
If ((GESF == 0x15))
    If ((PARM == One))
        \_SB.PCI0.AUDE != 0x20
    If ((PARM == Zero))
        \_SB.PCI0.AUDE &= 0xDF
    GESF = Zero
    PARM = Zero
If ((GESF == 0x16))
    Local0 = (PARM & 0x03)
    GESF = Zero
    GESF = Zero
If ((GEFC == 0x04))
    GXFC = GBDA ()
If ((GEFC == 0x06))
    GXFC = SBCB ()
GEFC = Zero
SCIS = One
GSSE = Zero
SCIE = Zero
Return ((CSTS == 0x03))
CEVT = Arg0
CSTS = 0x03
If (((CHPD == Zero) && (Arg1 == Zero)))
    TIDX = Arg0
If ((Arg0 == One))
    CLID = 0x03
    CLID = Arg0
    CLID != 0x80000000
CCLK = Arg0
IUER &= 0xC0
IUER ^= (One << Arg0)
If ((Arg0 <= 0x04))
If ((Arg0 >= 0x05) && (Arg0 <= 0x07))
    ASLC = (One << Arg0)
    ASLE = One
    Local2 = Zero
    While (((Local2 < 0xFA) && (ASLC != Zero)))
If ((Arg0 == 0x02))
    Local0 = (CPFM & 0x0F)
    Local1 = (EPFM & 0x0F)
    If ((Local0 == One))
        PFIT = 0x06
        PFIT = 0x08
        PFIT = One
    If ((Local0 == 0x06))
        PFIT = 0x08
        PFIT = One
        PFIT = 0x06
    If ((Local0 == 0x08))
        PFIT = One
        PFIT = 0x06
        PFIT = 0x08
    PFIT ^= 0x07
    PFIT != 0x80000000
    ASLC = 0x04
    If ((Arg0 == One))
        BCLP = ((Arg1 * 0xFF) / 0x64)
        BCLP != 0x80000000
        ASLC = 0x02
        If ((Arg0 == Zero))
            ALSI = Arg1
            ASLC = One
ASLE = One
    If ((PNHM == 0x00306C1))
/* 0060 */ 0x3C, 0x00, 0x01, 0x00, 0x3D, 0x00, 0x01, 0x00, /* <...=... */
/* 0060 */ 0x3C, 0x00, 0x01, 0x00, 0x3D, 0x00, 0x01, 0x00, /* <...=... */
/* 0060 */ 0x3C, 0x00, 0x01, 0x00, 0x3D, 0x00, 0x01, 0x00, /* <...=... */
    PEGI = Arg0
    PEBA = \XBAS
    PDEV = GDEV (PEGI)
    PFUN = GFUN (PEGI)
    If ((CCHK (PEGI, One) == Zero))
    If ((PCSL >= 0x04))
        If ((RC7A == One))
    If ((PBGE != Zero))
        CBDL = GUBC (PEGI)
        MBDL = GMBX (PEGI)
        CBDL = MBDL /* \_SB_.PCI0.MBDL */
TCNT = Zero
    If ((VCNP () == Zero))
        TCNT += 0x10
    PEGI = Arg0
    PEBA = \XBAS
    PDEV = GDEV (PEGI)
    PFUN = GFUN (PEGI)
    If ((CCHK (PEGI, Zero) == Zero))
    TCNT = Zero
    If ((LNKS () == Zero))

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```

TCNT += 0x10
If ((PBGE != Zero))
    MBDL = GMXB (PEGI)
If ((PCSL >= 0x04))
    If ((RC7A == One))
If ((OTSD == Zero))
    TIDX = Zero
    PEBA = \XBAS
    PDEV = GDEV (TIDX)
    PFUN = GFUN (TIDX)
    Local7 = MMRW (PEBA, PBUS, PDEV, PFUN, VIOF)
    If ((Local7 != IVID))
        OTSD = One
Local7 = Arg0
Local7 |= (Arg1 << 0x14)
Local7 |= (Arg2 << 0x0F)
Local7 |= (Arg3 << 0x0C)
Local7 |= Arg4
Local7 = Arg0
Local7 |= (Arg1 << 0x14)
Local7 |= (Arg2 << 0x0F)
Local7 |= (Arg3 << 0x0C)
Local7 |= Arg4
TEMP = Arg5
Local7 = Arg0
Local7 |= (Arg1 << 0x14)
Local7 |= (Arg2 << 0x0F)
Local7 |= (Arg3 << 0x0C)
Local7 |= Arg4
Local7 = Arg0
Local7 |= (Arg1 << 0x14)
Local7 |= (Arg2 << 0x0F)
Local7 |= (Arg3 << 0x0C)
Local7 |= Arg4
TEMP = Arg5
Local7 = Arg0
Local7 |= (Arg1 << 0x14)
Local7 |= (Arg2 << 0x0F)
Local7 |= (Arg3 << 0x0C)
Local7 |= Arg4
Local7 = Arg0
Local7 |= (Arg1 << 0x14)
Local7 |= (Arg2 << 0x0F)
Local7 |= (Arg3 << 0x0C)
Local7 |= Arg4
TEMP = Arg5
Local7 = MMRW (Arg0, Arg1, Arg2, Arg3, VIOF)
If ((Local7 == IVID))
    TMP0 = MMRW (Arg0, Arg1, Arg2, Arg3, DSOF)
    If (((TMP0 & 0x10) == Zero))
    TMP2 = MMRB (Arg0, Arg1, Arg2, Arg3, CPOF)
        TMP2 &= 0xFC
        If ((TMP2 == Zero))
            TMP1 = MMRB (Arg0, Arg1, Arg2, Arg3, TMP2)
            If ((Arg4 == TMP1))
                TMP2 = MMRB (Arg0, Arg1, Arg2, Arg3, (TMP2 + One))
INDX = Zero
PLEN = 0x04
INDX += (Arg0 * PLEN)
Local7 = MMRB (PEBA, PBUS, PDEV, PFUN, SBOF)
POFF = SCAP (PEBA, Local7, EDEV, EFN0, 0x10)
POFF += 0x0C
    Local7 = MMRB (PEBA, PBUS, PDEV, PFUN, SBOF)
    Index (LCAP, INDX) = MMRB (PEBA, Local7, EDEV, EFN0, POFF)
Local7 = Zero
PLEN = 0x04
INDX = (PLEN - One)
INDX += (Arg0 * PLEN)
    Local7 <<= 0x08
    Local0 = Derefof (Index (LCAP, INDX))
    Local7 |= Local0
Local7 = MMRD (PEBA, PBUS, PDEV, PFUN, 0xAC)
Local7 >>= 0x04
Local7 &= 0x3F
Local6 = Arg0
Local6 >>= 0x04
Local6 &= 0x3F
    Local0 = (Local7 - Local6)
    Local0 = Zero
HSTR = MMRD (PEBA, PBUS, PDEV, PFUN, 0x0504)
HSTR >>= 0x10
HSTR &= 0x03
If ((Arg0 == Zero))
    If ((HSTR == 0x03))
        Local0 = 0x08
        Local0 = 0x04
    If ((Arg0 == One))
        If ((HSTR == 0x02))
            Local0 = 0x04
            If ((HSTR == Zero))
                Local0 = 0x02

```



```

        If ((Arg0 == 0x02))
            If ((HSTR == Zero))
                Local10 = 0x02
FBDL = Zero
CBDL = Zero
HSTR = MMRD (PEBA, PBUS, PDEV, PFUN, 0x0504)
HSTR >>= 0x10
HSTR &= 0x03
LREV = MMRD (PEBA, PBUS, PDEV, PFUN, 0x0D0C)
LREV >>= 0x14
LREV &= One
If ((Arg0 == Zero))
    If ((HSTR == 0x03))
        FBDL = Zero
        CBDL = 0x08
        If ((LREV == Zero))
            FBDL = Zero
            CBDL = 0x04
            FBDL = 0x04
            CBDL = 0x04
    If ((Arg0 == One))
        If ((HSTR == 0x02))
            If ((LREV == Zero))
                FBDL = 0x04
                CBDL = 0x04
                FBDL = Zero
                CBDL = 0x04
            If ((HSTR == Zero))
                If ((LREV == Zero))
                    FBDL = 0x04
                    CBDL = 0x02
                    FBDL = 0x02
                    CBDL = 0x02
        If ((Arg0 == 0x02))
            If ((HSTR == Zero))
                If ((LREV == Zero))
                    FBDL = 0x06
                    CBDL = 0x02
                    FBDL = Zero
                    CBDL = 0x02
INDX = One
If ((CBDL != Zero))
    While ((INDX <= CBDL))
        FBDL = Zero
        CBDL = Arg1
        If ((CBDL == Zero))
            HSTR = MMRD (PEBA, PBUS, PDEV, PFUN, 0x0504)
            HSTR >>= 0x10
            HSTR &= 0x03
            LREV = MMRD (PEBA, PBUS, PDEV, PFUN, 0x0D0C)
            LREV >>= 0x14
            LREV &= One
        If ((Arg0 == Zero))
            If ((HSTR == 0x03))
                If ((LREV == Zero))
                    FBDL = (0x08 - CBDL)
                    FBDL = Zero
                If ((LREV == Zero))
                    FBDL = (0x04 - CBDL)
                    FBDL = 0x04
            If ((Arg0 == One))
                If ((HSTR == 0x02))
                    If ((LREV == Zero))
                        FBDL = (0x08 - CBDL)
                        FBDL = Zero
                    If ((HSTR == Zero))
                        If ((LREV == Zero))
                            FBDL = (0x06 - CBDL)
                            FBDL = 0x02
                If ((Arg0 == 0x02))
                    If ((HSTR == Zero))
                        If ((LREV == Zero))
                            FBDL = (0x08 - CBDL)
                            FBDL = Zero
INDX = One
While ((INDX <= CBDL))
    If ((Arg0 == Zero))
        If ((P0UB == Zero))
            If ((Arg0 == One))
                If ((P1UB == Zero))
                    If ((Arg0 == 0x02))
                        If ((P2UB == Zero))
Local17 = MMRW (PEBA, PBUS, GDEV (Zero), GFUN (Zero), VIOF)
If ((Local17 == IVID))
Local17 = PEBA /* \_SB\_PCI0.PEBA */
Local17 |= (PBUS << 0x14)
Local17 |= (GDEV (Zero) << 0x0F)
Local17 |= (GFUN (Zero) << 0x0C)
Local17 |= 0x091C
Local17 += (Arg0 * 0x20)
TEMP = Arg1

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```

Local7 = Zero
Local6 = RLCA (Arg0)
If ((Arg0 == Zero))
  If ((P0UB == 0xFF))
    Local5 = GULC (Local6)
    Local7 = (Local5 / 0x02)
    If ((P0UB != Zero))
      Local7 = P0UB /* \P0UB */
  If ((Arg0 == One))
    If ((P1UB == 0xFF))
      Local5 = GULC (Local6)
      Local7 = (Local5 / 0x02)
      If ((P1UB != Zero))
        Local7 = P1UB /* \P1UB */
    If ((Arg0 == 0x02))
      If ((P2UB == 0xFF))
        Local5 = GULC (Local6)
        Local7 = (Local5 / 0x02)
        If ((P2UB != Zero))
          Local7 = P2UB /* \P2UB */
Local7 = PEBA /* \_SB_.PCI0.PEBA */
Local7 |= (PBUS << 0x14)
Local7 |= (PDEV << 0x0F)
Local7 |= (PFUN << 0x0C)
Local7 |= 0x0C20
TEMP = Arg0
Local7 = PEBA /* \_SB_.PCI0.PEBA */
Local7 |= (PBUS << 0x14)
Local7 |= (PDEV << 0x0F)
Local7 |= (PFUN << 0x0C)
Local7 |= 0xB0
TEMP = Arg0
Local7 = PEBA /* \_SB_.PCI0.PEBA */
Local7 |= (PBUS << 0x14)
Local7 |= (PDEV << 0x0F)
Local7 |= (PFUN << 0x0C)
Local7 |= 0x0214
Local7 = PEBA /* \_SB_.PCI0.PEBA */
Local7 |= (PBUS << 0x14)
Local7 |= (PDEV << 0x0F)
Local7 |= (PFUN << 0x0C)
Local7 |= 0x011A
Local7 = PEBA /* \_SB_.PCI0.PEBA */
Local7 |= (PBUS << 0x14)
Local7 |= (PDEV << 0x0F)
Local7 |= (PFUN << 0x0C)
Local7 |= 0x0508
TEMP = Arg0
Local7 = PEBA /* \_SB_.PCI0.PEBA */
Local7 |= (HBRB << 0x14)
Local7 |= (HBRD << 0x0F)
Local7 |= (HBRF << 0x0C)
Local7 |= 0x48
Local7 = MCHA ()
Local7 <<= 0x0F
Local7 += 0x5DA8
TEMP = Arg0
If ((Arg0 == Zero))
  Local0 = One
  If ((Arg0 == One))
    Local0 = One
If ((Arg0 == 0x02))
  Local0 = One
If ((Arg0 == Zero))
  Local0 = Zero
  If ((Arg0 == One))
    Local0 = One
If ((Arg0 == 0x02))
  Local0 = 0x02
Local7 = MMRW (PEBA, PBUS, PDEV, PFUN, VIOF)
If ((Local7 == IVID))
If ((Arg0 != Zero))
  Local7 = MMRW (PEBA, PBUS, GDEV (Zero), GFUN (Zero), VIOF)
  If ((Local7 == IVID))
If ((Arg1 == Zero))
  If ((REPF (Arg0) == Zero))
  If ((Arg1 == One))
    If ((REPF (Arg0) == One))
INDX = Zero
PLEN = 0x04
INDX += (Arg0 * PLEN)
POFF = 0x2C
  Local7 = MMRB (PEBA, PBUS, PDEV, PFUN, SB0F)
  Index (SSYS, INDX) = MMRB (PEBA, Local7, EDEV, EFN0, POFF)
INDX = Zero
PLEN = 0x04
INDX += (Arg0 * PLEN)
POFF = 0x2C
  Local7 = MMRB (PEBA, PBUS, PDEV, PFUN, SB0F)
INDX = Zero
PLEN = 0x02

```

```

INDX += (Arg0 * PLEN)
Local7 = MMRB (PEBA, PBUS, PDEV, PFUN, SBOF)
POFF = SCAP (PEBA, Local7, EDEV, EFN0, 0x10)
POFF += 0x10
    Local7 = MMRB (PEBA, PBUS, PDEV, PFUN, SBOF)
    Index (LCTL, INDX) = MMRB (PEBA, Local7, EDEV, EFN0, POFF)
INDX = Zero
PLEN = 0x02
INDX += (Arg0 * PLEN)
Local7 = MMRB (PEBA, PBUS, PDEV, PFUN, SBOF)
POFF = SCAP (PEBA, Local7, EDEV, EFN0, 0x10)
POFF += 0x10
Local7 = MMRB (PEBA, PBUS, PDEV, PFUN, SBOF)
Local0 = MMRW (PEBA, Local7, EDEV, EFN0, POFF)
Local0 &= 0xFFBC
Local1 = Derefof (Index (LCTL, INDX))
Local2 = Derefof (Index (LCTL, (INDX + One)))
Local2 <<= 0x08
Local1 |= Local2
Local1 &= 0x43
Local0 |= Local1
Local7 = MMRB (PEBA, PBUS, PDEV, PFUN, SBOF)
Local7 = MMRB (PEBA, PBUS, PDEV, PFUN, SBOF)
Local6 = MMRW (PEBA, Local7, EDEV, EFN1, VIOF)
If ((Local6 != IVID))
    Local7 = MMRB (PEBA, PBUS, PDEV, PFUN, SBOF)
    Local0 = MMRW (PEBA, Local7, EDEV, EFN1, POFF)
    Local0 &= 0xFFBC
    Local1 = Derefof (Index (LCTL, INDX))
    Local2 = Derefof (Index (LCTL, (INDX + One)))
    Local2 <<= 0x08
    Local1 |= Local2
    Local1 &= 0x43
    Local0 |= Local1
    Local7 = MMRB (PEBA, PBUS, PDEV, PFUN, SBOF)
If ((Arg0 == Zero))
    If ((Arg0 == One))
        If ((Arg0 == 0x02))
            SPGA |= (One << Arg0)
Local0 = (SPGA & (One << Arg0))
If ((Local0 == Zero))
If ((Arg0 == Zero))
    If ((Arg0 == One))
        If ((Arg0 == 0x02))
            INDX = Zero
            WLSB = Derefof (Index (DCLR, INDX))
            WMSB = Derefof (Index (DCLR, INDX))
            POFF = ((WMSB << 0x08) | WLSB)
            PLEN = Derefof (Index (DCLR, INDX))
            If ((POFF == 0xFFFF))
                Local7 = MMRW (PEBA, PBUS, GDEV (Zero), GFUN (Zero), VIOF)
                If ((Local7 == IVID))
                    INDX = Zero
                    WLSB = Derefof (Index (DCMN, INDX))
                    WMSB = Derefof (Index (DCMN, INDX))
                    POFF = ((WMSB << 0x08) | WLSB)
                    PLEN = Derefof (Index (DCMN, INDX))
                    If ((POFF == 0xFFFF))
                        Index (DCMN, INDX) = MMRB (PEBA, PBUS, GDEV (Zero), GFUN (Zero), VIOF)
                Local7 = MMRW (PEBA, PBUS, GDEV (Zero), GFUN (Zero), VIOF)
                If ((Local7 == IVID))
                    INDX = Zero
                    WLSB = Derefof (Index (DCMN, INDX))
                    WMSB = Derefof (Index (DCMN, INDX))
                    POFF = ((WMSB << 0x08) | WLSB)
                    PLEN = Derefof (Index (DCMN, INDX))
                    If ((POFF == 0xFFFF))
                        INDX = Zero
                        WLSB = Derefof (Index (DPG0, INDX))
                        WMSB = Derefof (Index (DPG0, INDX))
                        POFF = ((WMSB << 0x08) | WLSB)
                        PLEN = Derefof (Index (DPG0, INDX))
                        If ((POFF == 0xFFFF))
                            Index (DPG0, INDX) = MMRB (PEBA, PBUS, PDEV, PFUN, POFF)
                    INDX = Zero
                    WLSB = Derefof (Index (DPG0, INDX))
                    WMSB = Derefof (Index (DPG0, INDX))
                    POFF = ((WMSB << 0x08) | WLSB)
                    PLEN = Derefof (Index (DPG0, INDX))
                    If ((POFF == 0xFFFF))
                        INDX = Zero
                        WLSB = Derefof (Index (DPG1, INDX))
                        WMSB = Derefof (Index (DPG1, INDX))
                        POFF = ((WMSB << 0x08) | WLSB)
                        PLEN = Derefof (Index (DPG1, INDX))
                        If ((POFF == 0xFFFF))
                            Index (DPG1, INDX) = MMRB (PEBA, PBUS, PDEV, PFUN, POFF)
                    INDX = Zero
                    WLSB = Derefof (Index (DPG1, INDX))
                    WMSB = Derefof (Index (DPG1, INDX))
                    POFF = ((WMSB << 0x08) | WLSB)

```

```

    PLEN = Derefof (Index (DPG1, INDX))
    If ((POFF == 0xFFFF))
INDEX = Zero
    WLSB = Derefof (Index (DPG2, INDX))
    WMSB = Derefof (Index (DPG2, INDX))
    POFF = ((WMSB << 0x08) | WLSB)
    PLEN = Derefof (Index (DPG2, INDX))
    If ((POFF == 0xFFFF))
        Index (DPG2, INDX) = MMRB (PEBA, PBUS, PDEV, PFUN, POFF)
INDEX = Zero
    WLSB = Derefof (Index (DPG2, INDX))
    WMSB = Derefof (Index (DPG2, INDX))
    POFF = ((WMSB << 0x08) | WLSB)
    PLEN = Derefof (Index (DPG2, INDX))
    If ((POFF == 0xFFFF))
SEPA |= (One << Arg0)
Local0 = (SEPA & (One << Arg0))
If ((Local0 == Zero))
If ((Arg0 == Zero))
    If ((Arg0 == One))
        If ((Arg0 == 0x02))
If ((Arg1 == Zero))
    If ((Arg0 == Zero))
    If ((Arg1 == One))
        If ((Arg0 == Zero))
INDEX = Zero
PLEN = One
INDEX += (Arg0 * PLEN)
    Index (ONOF, INDX) = Arg1
INDEX = Zero
PLEN = One
INDEX += (Arg0 * PLEN)
    Local0 = Derefof (Index (ONOF, INDX))
TIDX = Zero
    INDEX = Zero
    PLEN = One
    INDEX += (TIDX * PLEN)
        Index (ONOF, INDX) = One
    If ((SGGP != One))
    If ((SGPI (PWOK) == One))
If ((PVID != 0xFFFF))
Local2 = Zero
    If ((SGGP == One))
        Local7 = (Arg0 >> 0x07)
        Local7 &= One
        Local6 = (Arg0 & 0x7F)
        Local5 = 0x0C
        Local4 = Local6
        Local5 = 0x38
        Local4 = (Local6 - 0x20)
        Local5 = 0x48
        Local4 = (Local6 - 0x40)
    If (((Arg0 & 0x7F) <= 0x4B))
        Local3 = (\GBAS + Local5)
        Local2 = TEMP /* \_SB_.PCI0.PEG0.PEGP.SGPI.TEMP */
    Local2 >= Local4
    If ((Local7 == Zero))
        Local2 = ~Local2
    Local2 &= One
Local2 = Arg1
    If ((SGGP == One))
        Local7 = (Arg0 >> 0x07)
        Local7 &= One
        Local6 = (Arg0 & 0x7F)
        Local5 = 0x0C
        Local4 = Local6
        Local5 = 0x38
        Local4 = (Local6 - 0x20)
        Local5 = 0x48
        Local4 = (Local6 - 0x40)
    If ((Local7 == Zero))
        Local2 = ~Local2
    Local2 &= One
    If (((Arg0 & 0x7F) <= 0x4B))
        Local3 = (\GBAS + Local5)
        Local1 = TEMP /* \_SB_.PCI0.PEG0.PEGP.SGPO.TEMP */
        Local2 <= Local4
        Local0 = (One << Local4)
        Local1 &= ~Local0
        Local1 |= Local2
        TEMP = Local1

```

ネットワークインターフェースデータ詳細一覧

```
bge0: flags=8843<UP,BROADCAST,RUNNING,SIMPLEX,MULTICAST> metric 0 mtu 1500
options=c019b<RXCSUM, TXCSUM, VLAN_MTU, VLAN_HWTAGGING, VLAN_HWCSUM, TSO4, VLAN_HWTSO, LINKSTATE>
ether 94:de:80:ff:25:96
inet 192.168.10.127 netmask 0xfffff00 broadcast 192.168.10.255
nd6 options=29<PERFORMNUD,IFDISABLED,AUTO_LINKLOCAL>
media: Ethernet autoselect (1000baseT <full-duplex>)
status: active
bge1: flags=8802<BROADCAST,SIMPLEX,MULTICAST> metric 0 mtu 1500
options=c019b<RXCSUM, TXCSUM, VLAN_MTU, VLAN_HWTAGGING, VLAN_HWCSUM, TSO4, VLAN_HWTSO, LINKSTATE>
ether 94:de:80:ff:25:97
nd6 options=29<PERFORMNUD,IFDISABLED,AUTO_LINKLOCAL>
media: Ethernet autoselect
lo0: flags=8049<UP,LOOPBACK,RUNNING,MULTICAST> metric 0 mtu 16384
options=600003<RXCSUM, TXCSUM, RXCSUM_IPV6, TXCSUM_IPV6>
inet6 ::1 prefixlen 128
inet6 fe80::1%lo0 prefixlen 64 scopeid 0x3
inet 127.0.0.1 netmask 0xff000000
nd6 options=21<PERFORMNUD,AUTO_LINKLOCAL>
```

デバイス認識詳細一覧

2	1 dr-xr-xr-x	9 root	wheel	512 Nov 12 01:06 /dev
100	0 lrwxr-xr-x	1 root	wheel	12 Nov 11 16:07 /dev/log ->
/var/run/log				
117	0 lrwxr-xr-x	1 root	wheel	12 Nov 12 01:07 /dev/dumpdev ->
/dev/mfid0p3				
3	0 crw-r-----	1 root	operator	0x3 Nov 11 16:06 /dev/geom.ctl
4	0 cr--r--r--	1 root	wheel	0x4 Nov 11 16:06 /dev/sndstat
5	0 crw-----	1 root	wheel	0x5 Nov 11 16:06 /dev/devctl
6	0 crw-----	1 root	wheel	0x6 Nov 11 16:07 /dev/console
7	0 crw-rw-rw-	1 root	wheel	0x7 Nov 11 16:06 /dev/tty
8	0 crw-----	1 root	wheel	0x8 Nov 11 16:06 /dev/fido
9	0 crw-----	1 root	wheel	0x9 Nov 11 16:07 /dev/bpf
10	0 lrwxr-xr-x	1 root	wheel	3 Nov 11 16:06 /dev/bpf0 -> bpf
11	0 crw-rw-rw-	1 root	wheel	0xb Nov 11 16:13 /dev/null
12	0 crw-rw-rw-	1 root	wheel	0xc Nov 11 16:06 /dev/zero
13	0 crw-r--r--	1 root	wheel	0xd Nov 11 16:06 /dev/pci
14	0 crw-----	1 root	kmem	0xe Nov 11 16:06 /dev/nfslock
15	0 crw-rw-rw-	1 root	wheel	0xf Nov 12 01:07 /dev/random
16	0 lrwxr-xr-x	1 root	wheel	6 Nov 11 16:06 /dev/urandom ->
random				
17	0 crw-----	1 root	kmem	0x11 Nov 11 16:06 /dev/audit
113	1 dr-xr-xr-x	2 root	wheel	512 Nov 11 16:06 /dev/fd
18	0 crw-rw-rw-	1 root	wheel	0x12 Nov 11 16:06 /dev/fd/0
20	0 crw-rw-rw-	1 root	wheel	0x14 Nov 11 16:06 /dev/fd/1
22	0 crw-rw-rw-	1 root	wheel	0x16 Nov 11 16:06 /dev/fd/2
19	0 lrwxr-xr-x	1 root	wheel	4 Nov 11 16:06 /dev/stdin -> fd/0
21	0 lrwxr-xr-x	1 root	wheel	4 Nov 11 16:06 /dev/stdout -> fd/1
23	0 lrwxr-xr-x	1 root	wheel	4 Nov 11 16:06 /dev/stderr -> fd/2
24	0 crw-----	1 root	wheel	0x18 Nov 11 16:06 /dev/io
25	0 crw-rw-rw-	1 root	wheel	0x19 Nov 11 16:06 /dev/midstat
26	0 crw-----	1 root	wheel	0x1a Nov 11 16:06 /dev/synmouse
27	0 crw-----	1 root	wheel	0x1b Nov 11 16:06 /dev/kbdmux0
28	0 lrwxr-xr-x	1 root	wheel	7 Nov 11 16:06 /dev/kbd1 ->
kbdmux0				
29	0 crw-----	1 root	wheel	0x1d Nov 11 16:06 /dev/klog
30	0 crw-r-----	1 root	kmem	0x1e Nov 11 16:06 /dev/mem
31	0 crw-r-----	1 root	kmem	0x1f Nov 11 16:06 /dev/kmem
32	0 crw-r--r--	1 root	wheel	0x20 Nov 11 16:06 /dev/acpi
33	0 crw-rw----	1 root	operator	0x21 Nov 11 16:06 /dev/apmctl
34	0 crw-rw-r--	1 root	operator	0x22 Nov 11 16:06 /dev/apm
35	0 crw-----	1 root	wheel	0x23 Nov 11 16:06 /dev/hpet0
36	0 crw-r-----	1 root	operator	0x24 Nov 11 16:06 /dev/mfi0
37	0 lrwxr-xr-x	1 root	wheel	4 Nov 11 16:06
/dev/megaraid_sas_ioctl_node -> mfi0				
38	0 crw-----	1 root	wheel	0x26 Nov 11 16:06 /dev/ttyu0
39	0 crw-----	1 root	wheel	0x27 Nov 11 16:06 /dev/ttyu0.init
40	0 crw-----	1 root	wheel	0x28 Nov 11 16:06 /dev/ttyu0.lock
41	0 crw-rw----	1 uucp	dialer	0x29 Nov 11 16:06 /dev/cuau0
42	0 crw-rw----	1 uucp	dialer	0x2a Nov 11 16:06 /dev/cuau0.init
43	0 crw-rw----	1 uucp	dialer	0x2b Nov 11 16:06 /dev/cuau0.lock
44	0 crw-----	1 root	wheel	0x2c Nov 11 16:06 /dev/ttyu1
45	0 crw-----	1 root	wheel	0x2d Nov 11 16:06 /dev/ttyu1.init
46	0 crw-----	1 root	wheel	0x2e Nov 11 16:06 /dev/ttyu1.lock
47	0 crw-rw----	1 uucp	dialer	0x2f Nov 11 16:06 /dev/cuau1
48	0 crw-rw----	1 uucp	dialer	0x30 Nov 11 16:06 /dev/cuau1.init
49	0 crw-rw----	1 uucp	dialer	0x31 Nov 11 16:06 /dev/cuau1.lock
50	0 crw-----	1 root	tty	0x32 Nov 11 16:14 /dev/ttyv0
51	0 crw-----	1 root	wheel	0x33 Nov 11 16:07 /dev/ttyv1
52	0 crw-----	1 root	wheel	0x34 Nov 11 16:07 /dev/ttyv2
53	0 crw-----	1 root	wheel	0x35 Nov 11 16:07 /dev/ttyv3
54	0 crw-----	1 root	wheel	0x36 Nov 11 16:07 /dev/ttyv4
55	0 crw-----	1 root	wheel	0x37 Nov 11 16:07 /dev/ttyv5
56	0 crw-----	1 root	wheel	0x38 Nov 11 16:07 /dev/ttyv6
57	0 crw-----	1 root	wheel	0x39 Nov 11 16:07 /dev/ttyv7
58	0 crw-----	1 root	wheel	0x3a Nov 11 16:06 /dev/ttyv8
59	0 crw-----	1 root	wheel	0x3b Nov 11 16:06 /dev/ttyv9
60	0 crw-----	1 root	wheel	0x3c Nov 11 16:06 /dev/ttyva
61	0 crw-----	1 root	wheel	0x3d Nov 11 16:06 /dev/ttyvb
62	0 crw-----	1 root	wheel	0x3e Nov 11 16:06 /dev/ttyvc
63	0 crw-----	1 root	wheel	0x3f Nov 11 16:06 /dev/ttyvd
64	0 crw-----	1 root	wheel	0x40 Nov 11 16:06 /dev/ttyve
65	0 crw-----	1 root	wheel	0x41 Nov 11 16:06 /dev/ttyvf
66	0 crw-----	1 root	wheel	0x42 Nov 11 16:06 /dev/conslectl
67	0 crw-----	1 root	wheel	0x43 Nov 11 16:06 /dev/mdctl
114	1 dr-xr-xr-x	2 root	wheel	512 Nov 11 16:06 /dev/usb
68	0 crw-----	1 root	operator	0x44 Nov 11 16:06 /dev/usb/0.1.0
75	0 crw-----	1 root	operator	0x4b Nov 11 16:06 /dev/usb/1.1.0
77	0 crw-----	1 root	operator	0x4d Nov 11 16:06 /dev/usb/2.1.0
79	0 crw-----	1 root	operator	0x4f Nov 11 16:06 /dev/usb/1.1.1
80	0 crw-----	1 root	operator	0x50 Nov 11 16:06 /dev/usb/0.1.1
81	0 crw-----	1 root	operator	0x51 Nov 11 16:06 /dev/usb/2.1.1
83	0 crw-----	1 root	operator	0x53 Nov 11 16:06 /dev/usb/0.2.0
85	0 crw-----	1 root	operator	0x55 Nov 11 16:06 /dev/usb/0.2.1
86	0 crw-----	1 root	operator	0x56 Nov 11 16:06 /dev/usb/1.2.0
88	0 crw-----	1 root	operator	0x58 Nov 11 16:06 /dev/usb/2.2.0

90	0 crw-----	1 root	operator	0x5a Nov 11 16:06	/dev/usb/l1.2.1
91	0 crw-----	1 root	operator	0x5b Nov 11 16:06	/dev/usb/l2.2.1
102	0 crw-----	1 root	operator	0x66 Nov 11 16:06	/dev/usb/l0.3.0
104	0 crw-----	1 root	operator	0x68 Nov 11 16:06	/dev/usb/l0.3.1
107	0 crw-----	1 root	operator	0x6b Nov 11 16:06	/dev/usb/l0.4.0
109	0 crw-----	1 root	operator	0x6d Nov 11 16:06	/dev/usb/l0.4.1
110	0 crw-----	1 root	operator	0x6e Nov 11 16:06	/dev/usb/l0.4.2
69	0 lrwxr-xr-x	1 root	wheel	9 Nov 11 16:06	/dev/ugen0.1 ->
usb/l0.1.0					
70	0 crw-----	1 root	wheel	0x46 Nov 11 16:06	/dev/ufssuspend
73	0 crw-r--r--	1 root	operator	0x49 Nov 11 16:06	/dev/usbctl
74	0 crw-----	1 root	operator	0x4a Nov 11 16:06	/dev/xpt0
76	0 lrwxr-xr-x	1 root	wheel	9 Nov 11 16:06	/dev/ugen1.1 ->
usb/l1.1.0					
78	0 lrwxr-xr-x	1 root	wheel	9 Nov 11 16:06	/dev/ugen2.1 ->
usb/l2.1.0					
82	0 cr--r--r--	1 root	wheel	0x52 Nov 11 16:06	/dev/devstat
84	0 lrwxr-xr-x	1 root	wheel	9 Nov 11 16:06	/dev/ugen0.2 ->
usb/l0.2.0					
87	0 lrwxr-xr-x	1 root	wheel	9 Nov 11 16:06	/dev/ugen1.2 ->
usb/l1.2.0					
89	0 lrwxr-xr-x	1 root	wheel	9 Nov 11 16:06	/dev/ugen2.2 ->
usb/l2.2.0					
92	0 crw-----	1 root	operator	0x5c Nov 11 16:06	/dev/pass0
93	0 crw-r-----	1 root	operator	0x5d Nov 11 16:06	/dev/mfid0
94	0 crw-r-----	1 root	operator	0x5e Nov 11 16:06	/dev/mfid0p1
95	0 crw-r-----	1 root	operator	0x5f Nov 12 01:06	/dev/mfid0p2
96	0 crw-r-----	1 root	operator	0x60 Nov 12 01:06	/dev/mfid0p3
97	0 crw-r-----	1 root	operator	0x61 Nov 11 16:06	/dev/cd0
115	1 dr-xr-xr-x	2 root	wheel	512 Nov 11 16:06	/dev/gptid
98	0 crw-r-----	1 root	operator	0x62 Nov 11 16:06	/dev/gptid
/f5921164-87b4-11e5-a1fa-94de80ff2596					
103	0 lrwxr-xr-x	1 root	wheel	9 Nov 11 16:06	/dev/ugen0.3 ->
usb/l0.3.0					
105	0 crw-----	1 root	wheel	0x69 Nov 11 16:06	/dev/ukbd0
106	0 lrwxr-xr-x	1 root	wheel	5 Nov 11 16:06	/dev/kbd0 -> ukbd0
108	0 lrwxr-xr-x	1 root	wheel	9 Nov 11 16:06	/dev/ugen0.4 ->
usb/l0.4.0					
111	0 crw-----	1 root	wheel	0x6f Nov 11 16:06	/dev/ukbd1
112	0 lrwxr-xr-x	1 root	wheel	5 Nov 11 16:06	/dev/kbd2 -> ukbd1
99	0 crw-r--r--	1 root	operator	0x63 Nov 11 16:07	/dev/ums0

更新履歷

更新履歴

- 2015年11月11日 FreeBSD動作検証レポート2015年11月11日版作成

FreeBSD動作検証レポート

R110g-1E / 10.2-RELEASE

対象ハードウェア

maker:NEC, product:Express5800/R110g-1E
[N8100-2174Y], serial:4800112, version:FR1.0

対象OSバージョン

FreeBSD 10.2-RELEASE #0: Wed Nov 11 16:02:59
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2015年11月11日版

BSDコンサルティング株式会社

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